5. Shielded Type SC, SA

Discontinued



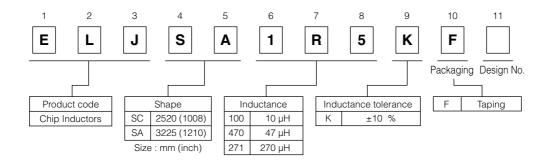
Features

- Magnetic shield effect using ferrite mixed within outer molding.
- Capable of being Re-flow or flow soldered.
- Good for mounting.
- RoHS compliant

Recommended Applications

• AV equipment, Wireless communication equipment and various types of general electronic equipment.

Explanation of Part Numbers



Storage Conditions

- Package
- : Normal temperature (-5 to 35 °C), normal humidity (85 %RH max.), shall not be exposed to direct sunlight and harmful gases and care should be taken so as not to cause dew. ● Operating Temperature : -20 to +85 °C

Storage Period

• Solderability may be reduced due to the conditions of high temperature and high humidity which causes the oxidation of tin-plated terminals. Even if storage conditions are within specified limits, solderability may be reduced with the passage of time. Therefore, please control the storage conditions and try to use the product within 6 months of receipt.

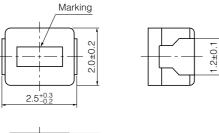
Packaging Methods, Soldering Conditions and Safety Precautions

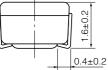
Please see Data Files.

Design and specifications are each subject to change without notice. Ask factory for the current technical specifications before purchase and/or use. Should a safety concern arise regarding this product, please be sure to contact us immediately. 01 Sep. 2012

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- SC Type 2520 (1008)
- Dimensions in mm (not to scale)



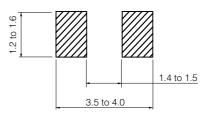


Standard Packing Quantity

• 2000 pcs./Reel

Standard Parts

• Recommended Land Pattern in mm (not to scale)

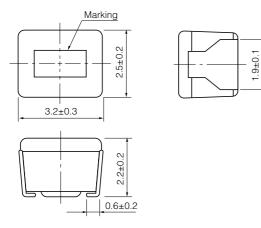


	Inductance			(Ç	SRF *1	Rdc *2	DC Current
Part No.	(µH)	Tolerance (%)	Test Freq. (MHz)	min.	Test Freq. (MHz)	(MHz) min.	(Ω) max.	(mA) max.
ELJSC270□F	27					20	4.50	18
ELJSC330□F	33					18	5.20	14
ELJSC390□F	39			40		15	5.70	13
ELJSC470□F	47	 K : ±10 %	2.52		2.52	14	6.60	12
ELJSC560□F	56	T. ± 10 %				13	7.10	10
ELJSC680□F	68]	13	6.50	17
ELJSC820□F	82]		25		13	7.40	14
ELJSC101□F	100		0.796		0.796	12	8.40	10

 \Box : Symbol of Tolerance $~\ast1$: Self Resonant Frequency $~\ast2$: DC Resistance

Design and specifications are each subject to change without notice. Ask factory for the current technical specifications before purchase and/or use. Should a safety concern arise regarding this product, please be sure to contact us immediately. 01 Se

- SA Type 3225 (1210)
- Dimensions in mm (not to scale)

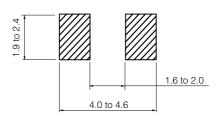


Standard Packing Quantity

• 2000 pcs./Reel

Standard Parts

• Recommended Land Pattern in mm (not to scale)

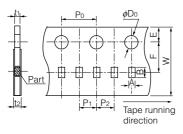


		Inductance		(Ç	SRF *1	Rdc *2	DC Current
Part No.	Part No. (µH) Tolerance Test Freq. min.		min.	Test Freq. (MHz)	(MHz) min.	(Ω) max.	(mA) max.	
ELJSA100KF	10					30	1.8	18
ELJSA120KF	12					28	2.0	17
ELJSA150KF	15					25	2.2	15
ELJSA180KF	18					23	2.5	13
ELJSA220KF	22				5	20	2.8	12
ELJSA270KF	27				5	18	3.2	10
ELJSA330KF	33		1			17	3.5	10
ELJSA390KF	39					15	3.8	9
ELJSA470KF	47	K:±10 %		40		14	4.0	8
ELJSA560KF	56	T ∧ . ± 10 %		40		13	4.5	7
ELJSA680KF	68					12	5.0	6
ELJSA820KF	82					11	6.0	6
ELJSA101KF	100					10	7.0	5
ELJSA121KF	120				1.5	9	8.0	5
ELJSA151KF	150				1.5	5	9.0	5
ELJSA181KF	180		0.1			5	11.0	5
ELJSA221KF	220	1	0.1			4	12.0	5
ELJSA271KF	270	1				4	14.0	5

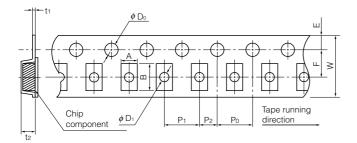
*1 : Self Resonant Frequency *2 : DC Resistance

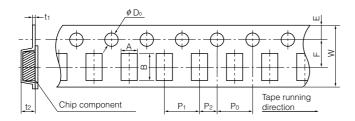
Design and specifications are each subject to change without notice. Ask factory for the current technical specifications before purchase and/or use. Should a safety concern arise regarding this product, please be sure to contact us immediately. 01 S

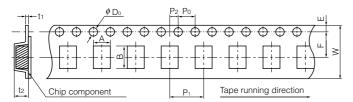
- Packaging Methods (Taping)
- Punched Carrier Tape Dimensions in mm (not to scale)



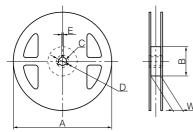
• Embossed Carrier Tape Dimensions in mm (not to scale)







• Taping Reel Dimensions in mm (not to scale)



Standard Packing Quantity/Reel

Quantity Types	Quantity		
RF, QF, PF	10000 pcs.		
RE, QE, PE, ND	3000 pcs.		
NC, FC, PC, LC, SC	2000 pcs.		
NA, FA, PA, LA, SA, EA, DA	2000 pcs.		
FB, PB	500 pcs.		

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	А	В	W	E	F	P1
rf, qf, pf	0.71	1.21	8.0	1.75	3.5	2.0
	P ₂	P₀	¢D∘	t1	t2	
RF, QF, PF	P ₂ 2.0	P ₀	<i>¢</i> D₀ <i>¢</i> 1.5	t1 0.7	t² 1.0	

● Type □E, Type ND, Type □C

	А	В	W	E	F	P1			
RE, QE, PE	1.0	1.8	8.0	1.75	3.5	4.0			
ND	1.45	2.25	8.0	1.75	3.5	4.0			
NC, FC, PC, LC, SC	2.40	2.90	8.0	1.75	3.5	4.0			
	P ₂	Po	¢D₀	ϕD_1	t1	t2			
RE, QE, PE	2.0	4.0	<i>¢</i> 1.5	<i>\$</i> 0.6	(0.27)	1.2			
ND	2.0	4.0	<i>¢</i> 1.5	<i>¢</i> 1.0	(0.25)	1.55			
NC, FC, PC, LC, SC	2.0	4.0	<i>¢</i> 1.5	<i>ø</i> 1.1	(0.25)	1.85			

● Туре □А

•) • • = •						
	А	В	W	E	F	P1
NA, FA, PA, LA, SA, EA, DA	2.80	3.60	8.0	1.75	3.5	4.0
	P ₂	P٥	ϕ_{D^0}	t1	t2	
NA, FA, PA, LA, SA, EA, DA	2.0	4.0	<i>¢</i> 1.5	(0.25)	2.40	

) Туре ⊟В

	А	В	W	E	F	P1
FB, PB	3.60	4.90	12.0	1.75	5.5	8.0
	P ₂	Po	¢D₀	t1	t2	
	· ·	•	101			
FB, PB	2.0	4.0	<i>¢</i> 1.5	(0.30)	3.50	

Parts Types	А	В	С	D	E	W
RF, QF, PF, RE, QE, PE, ND, NC, FC, PC, LC, SC, NA, FA, PA, LA, SA, EA, DA	180	60	13	21	2	9
FB, PB	180	60	13	21	2	13

Under conditions of high temperature and humidity deterioration of the taping and packaging may be accelerated.

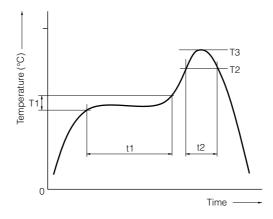
Please carefully control storage conditions and use the product within 6 months of receipt.

Design and specifications are each subject to change without notice. Ask factory for the current technical specifications before purchase and/or use. Should a safety concern arise regarding this product, please be sure to contact us immediately. 03

4

Soldering Conditions

Reflow soldering conditions



Pb free solder recommended temperature profile

Туре	Preheat		Sold	ering	Peak Ten	Time of	
туре	T1 [°C]	t1 [s]	T2 [°C]	t2 [s]	Т3	T3 Limit	Reflow
□F	150 to 180	60 to 120	230 °C	40 max.	250 °C, 10 s	260 °C, 10 s	2 times max.
DE	150 to 180	60 to 120	230 °C	40 max.	250 °C, 10 s	260 °C, 10 s	2 times max.
	150 to 180	60 to 120	230 °C	40 max.	245 °C, 10 s	250 °C, 10 s	2 times max.
	150 to 180	60 to 120	230 °C	40 max.	245 °C, 10 s	250 °C, 10 s	2 times max.
□A	150 to 180	60 to 120	230 °C	40 max.	245 °C, 10 s	250 °C, 10 s	2 times max.
□B	150 to 180	60 to 120	230 °C	40 max.	245 °C, 10 s	250 °C, 10 s	2 times max.

Flow soldering conditions

Preheat: 130 to 150 °C, 60 to 180 s, Soldering: 260 °C, 5 s max.

Notes

- Solderability may be reduced due to the conditions of high temperature and high humidity which causes the oxidation of tin-plated terminals. Even if storage conditions are within specified limits, solderability may be reduced with the passage of time. Therefore, please control the storage conditions and try to use the product within 6 months of receipt.
- In case the product has been stored for a period longer than 6 months, use the product only after confirmation of its solderability.

Design and specifications are each subject to change without notice. Ask factory for the current technical specifications before purchase and/or use. Should a safety concern arise regarding this product, please be sure to contact us immediately. 03 S

▲ Safety Precautions

(Common precautions for Chip Inductors)

- When using our products, no matter what sort of equipment they might be used for, be sure to make a written agreement on the specifications with us in advance. The design and specifications in this catalog are subject to change without prior notice.
- Do not use the products beyond the specifications described in this catalog.
- This catalog explains the quality and performance of the products as individual components. Before use, check and evaluate their operations when installed in your products.
- Install the following systems for a failsafe design to ensure safety if these products are to be used in equipment where a defect in these products may cause the loss of human life or other significant damage, such as damage to vehicles (automobile, train, vessel), traffic lights, medical equipment, aerospace equipment, electric heating appliances, combustion/gas equipment, rotating equipment, and disaster/crime prevention equipment.
- * Systems equipped with a protection circuit and a protection device
- * Systems equipped with a redundant circuit or other system to prevent an unsafe status in the event of a single fault

\triangle Precautions for use

1. Operation range and environments

- (1) These products are designed and manufactured for general and standard use in general electronic equipment (e.g. AV equipment, home electric appliances, office equipment, information and communication equipment)
- (2) These products are not intended for use in the following special conditions. Before using the products, carefully check the effects on their quality and performance, and determine whether or not they can be used.
 - In liquid, such as water, oil, chemicals, or organic solvent
 - In direct sunlight, outdoors, or in dust
 - In salty air or air with a high concentration of corrosive gas, such as Cl₂, H₂S, NH₃, SO₂, or NO₂
 - In an environment where these products cause dew condensation

2. Handling

- (1) Do not bring magnets or magnetized materials close to the product. The influence of their magnetic field can change the inductance value.
- ② Do not apply strong mechanical shocks by either dropping or collision with other parts. Excessive schock can damage the part.

3. Land pattern design

- ① Please refer to the recommended land pattern for each type shown on the datasheet.
- ② Avoid placing the chip inductor on any metal pattern except the recommended land pattern because a drop of Q and mutual conductance may occur.
- ③ In case of flow soldering, venting of soldering flux gases should be made for high density assemblies to get a good solder connection.
- (4) In case of reflow soldering, consider the layout because taller components close to chip inductor tend to block thermal conduction.

4. Mounting

- (1) In general, magnetic and electric characteristics of ferrite cores can be changed by applying excessively strong force. Placement force should not exceed 20 N.
- (2) Do not bend or twist the PWB after mounting the part.

5. Cleaning

① Do not use acid or alkali agents. Some cleaning solvents may damage the part.

Confirm by testing the reliability in advance of mass production.

(2) If Ultrasonic cleaning is used, please confirm the reliability in advance.

It is possible that combined resonance of component and PWB and cavitation can cause an abnormal vibration mode to exist causing damage.

6. Caution about applying excessive current

The rated current is defined as the smaller value of either the current value when the inductance drops 10 % down from the initial point or the current value when the average temperature of coil inside rises 20 °C up from the initial point. Do not operate product over the specific max. current.

<Package markings>

Package markings include the product number, quantity, and country of origin. In principle, the country of origin should be indicated in English.

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Chip Inductors





Ceramic Core/Laser-Cut and wire wound type chip inductors for automatic and high-density mounting Wide variation product line-up correspond to various needs

Recommended Applications

• Cellular phones, wireless communication equipment (W-LAN, Bluetooth), various modules, HIC, TV, VTR, PC & peripherals, DVD, DSC, STB.

Non wound Wire wound Technology Case Size 1608 Size 2012 Size 2520 Size 3225 Size 1005 Size 4532 Usage (0402) (0603)(0805)(1008)(1210)(1812)ELJRF ELJRE ELJNC **ELJNA** ELJND High Freq. Use چې ø 0 1.0-220 nH 10-1000 nH 10-820 nH 47-8200 nH 1.0-100 nH ELJQF ELJQE High Freq. ð High-Q 1.0-39 nH 2.2-56 nH ELJFC **ELJFA** ELJFB General Use 0.22–1000 µH 0.22-100 µH 0.22-220 µH ELJPF ELJPE ELJPC/PC□3 ELJPA/PA 2 ELJPB ELJLC ELJLA **High Power** 2.2-10 nH 2.2-22 nH 1.0-330 µH 1.0-33 µH 10-220 uH ELJSC **ELJSA** Magnetically Shielded 27-100 µH 10–270 µH **ELJEA** Low DC Resistance 1.0–330 µH ELJDA/ELJFA Signal **Processing Use** (Low Distortion Type) 39–100 µH

■ Inductors · Selection Guide

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Size : mm (inch)