

AEC-Q101 Qualified

4V Drive Nch MOSFET

RHU002N06FRA

Structure

Silicon N-channel MOSFET transistor

Features

- 1) Low on-resistance.
- 2) High ESD.
- 3) High-speed switching.
- 4) Low-voltage drive (4V).
- 5) Drive circuits can be simple.
- 6) Parallel use is easy.

Applications

Switching

Packaging specifications

	Package	Taping
	Code	T106
Type	Basic ordering unit (pieces)	3000
RHU002N06	0	

●Absolute maximum ratings (Ta=25°C)

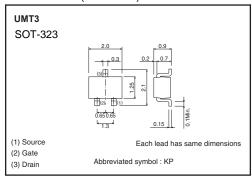
Parameter		Symbol	Limits	Unit
Drain-source voltage		VDSS	60	V
Gate-source voltage		Vgss	±20	V
Drain augreent	Continuous	lo	±200	mA
Drain current	Pulsed	IDP *1	±800	mA
Source current	Continuous	Is	200	mA
(Body diode)	Pulsed	Isp*1	800	mA
Total power dissipation		Pp *2	200	mW
Channel temperature		Tch	150	°C
Storage temperature		Tstg	-55 to +150	°C

Thermal resistance

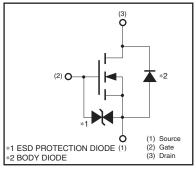
Parameter	Symbol	Limits	Unit
Channel to ambient	Rth (ch-a)*	625	°C/W

^{*} With each pin mounted on the recommended land.

●Dimensions (Unit: mm)



●Equivalent circuit



* A protection diode has been built in between the gate and the source to protect against static electricity when the product is in use. Use the protection circuit when fixed voltages are

^{*1} Pw≤10µs, Duty cycle≤1% *2 Each terminal mounted on a recommended

RHU002N06FRA Data Sheet

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Conditions	
Gate leakage current	Igss	-	-	±10	μΑ	Vgs=±20V, Vps=0V	
Drain-source breakdown voltage	V (BR) DSS	60	_	-	V	In=1mA, Vgs=0V	
Drain cutoff current	IDSS	-	-	1	μΑ	V _{DS} =60V, V _{GS} =0V	
Gate threshold voltage	VGS (th)	1	_	2.5	V	V _{DS} =10V, I _D =1mA	
Data and the second		-	1.7	2.4	Ω	In=200mA, Vgs=10V	
Drain-source on-state resistance	RDS (on)*	-	2.8	4.0	22	In=200mA, Vgs=4V	
Forward transfer admittance	I Yfs I*	0.1	_	-	S	Vps=10V, Ip=200mA	
Input capacitance	Ciss	_	15	-	pF	V _{DS} =10V V _{GS} =0V	
Output capacitance	Coss	_	8	-	pF		
Reverse transfer capacitance	Crss	_	4	-	pF	f=1MHz	
Turn-on delay time	td (on)*	_	6	-	ns	Ip=100mA, Vpp≒30V	
Rise time	tr*	_	5	-	ns	V _{GS} =10V	
Turn-off delay time	td (off)*	_	12	_	ns	R _L =300Ω R _G =10Ω	
Fall time	t _f *	-	95	-	ns		
Total gate charge	Q _g *	-	2.2	4.4	nC	V _{DD} ≒30V	
Gate-source charge	Q _{gs} *	-	0.6	-	nC	V _{GS} =10V I _D =200mA	
Gate-drain charge	Q _{gd} *	-	0.3	-	nC		

^{*} Pulsed

●Body diode characteristics (Source-drain) (Ta=25°C)

			, ,			
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	V _{SD} *	_	_	1.2	V	Is=200mA, V _{GS} =0V

^{*}Pulsed

•Electrical characteristic curves

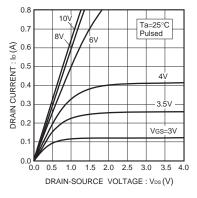


Fig.1 Typical Output Characteristics

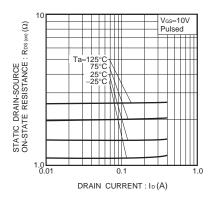


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current (I)

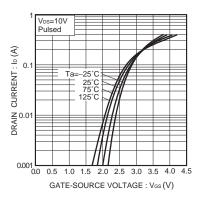


Fig.2 Typical Transfer Characteristics

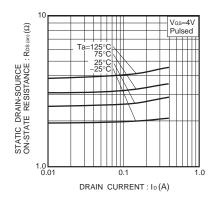


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current (II)

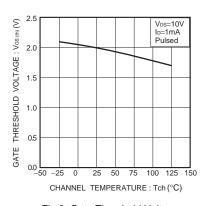


Fig.3 Gate Threshold Voltage vs. Channel Temperature

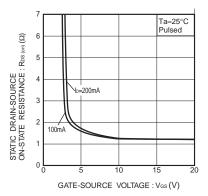


Fig.6 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

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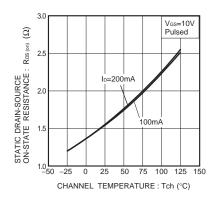


Fig.7 Static Drain-Source On-State Resistance vs. Channel Temperature

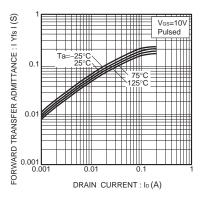


Fig.10 Forward Transfer Admittance vs. Drain Current

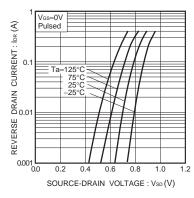


Fig.8 Reverse Drain Current vs. Source-Drain Voltage (I)

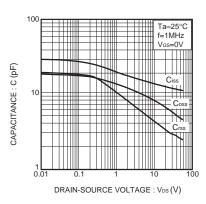


Fig.11 Typical Capacitance vs. Drain-Source Voltage

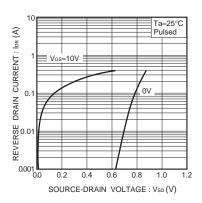


Fig.9 Reverse Drain Current vs. Source-Drain Voltage (II)

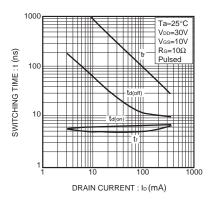


Fig.12 Switching Characteristics

•Switching characteristics measurement circuit

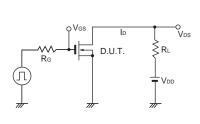


Fig.13 Switching time test circuit

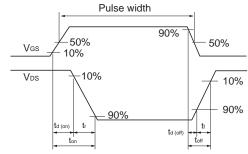


Fig.14 Switching time waveforms

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Ì	JÁPAN	USA	EU	CHINA	
Γ	CLASSⅢ	CL ACCIII	CLASS II b	СГУССШ	
Γ	CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ	

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
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- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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