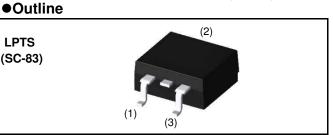
Datasheet

AEC-Q102 Qualified

V_{DSS} 100V $R_{DS(on)}(Max.)$ $27m\Omega$ I_D 40A \overline{P}_D 50W

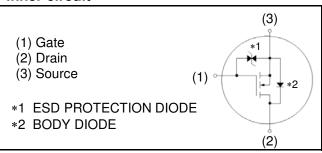
LPTS



Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Drive circuits can be simple.
- 4) Parallel use is easy.
- 5) Pb-free lead plating; RoHS compliant
- 6) 100% Avalanche tested

●Inner circuit



Application

Switching Power Supply

Automotive Motor Drive

Automotive Solenoid Drive

Packaging specifications

	Packaging	Taping
	Reel size (mm)	330
Type	Tape width (mm)	16
Туре	Basic ordering unit (pcs)	2,500
	Taping code	TL
	Marking	RSJ400N10

• Absolute maximum ratings($T_a = 25$ °C)

Parameter	Symbol	Value	Unit	
Drain - Source voltage	V_{DSS}	100	V	
Continuous drain current	T _c = 25°C	I _D *1	±40	А
	T _c = 100°C	I _D *1	±22	А
Pulsed drain current	I _{D,pulse} *2	±80	Α	
Gate - Source voltage	V_{GSS}	±20	V	
Avalanche energy, single pulse		E _{AS} *3	14.6	mJ
Avalanche current		I _{AR} *3	10	Α
Power dissination	$T_c = 25^{\circ}C$	P _D	50	W
Power dissipation $T_a = 25^{\circ}C^{*4}$		P _D	1.35	W
Junction temperature	T _j	150	°C	
Range of storage temperature		T _{stg}	-55 to +150	°C

●Thermal resistance

Parameter	Symbol	Values			Unit
Farameter	Symbol	Min.	Тур.	Max.	Offic
Thermal resistance, junction - ambient	R_{thJC}	-	-	2.5	°C/W
Thermal resistance, junction - ambient *4	R_{thJA}	-	-	92.6	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C

• Electrical characteristics ($T_a = 25$ °C)

Parameter	Symbol Conditions		Values			Unit	
Farameter	Syllibol	Conditions		Тур.	Max.	Offic	
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V$, $I_D = 1mA$	100	-	-	V	
		$V_{DS} = 100V, V_{GS} = 0V$			4		
Zara gata valtaga drain aurrent		$T_j = 25^{\circ}C$	-	-	1		
Zero gate voltage drain current	I _{DSS}	$V_{DS} = 100V, V_{GS} = 0V$		100	μΑ		
		T _j = 125°C		-	100		
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V, \ V_{DS} = 0V$	-	-	±10	μΑ	
Gate threshold voltage	V _{GS (th)}	$V_{DS} = 10V, I_{D} = 1mA$	1.0	-	2.5	V	
	*5	$V_{GS} = 10V, I_D = 40A$	-	19	27		
Static drain - source		$V_{GS} = 4.0V, I_D = 40A$	-	21	30	m O	
on - state resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 40A$		40	60	mΩ	
		T _j = 125°C	_	42	60		
Forward transfer admittance	g _{fs}	$V_{DS} = 10V, I_D = 40A$	23	56	-	S	

●Electrical characteristics(T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit	
rarameter	Syllibol	Conditions	Min.	Тур.	Max.	Offic	
Input capacitance	C_{iss}	$V_{GS} = 0V$	-	3600	-		
Output capacitance	C _{oss}	V _{DS} = 25V	-	270	-	pF	
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	180	-		
Turn - on delay time	$t_{d(on)}$ *5	$V_{DD} \simeq 50V, V_{GS} = 10V$	-	25	-		
Rise time	t _r *5	I _D = 20A	-	80	-	nc	
Turn - off delay time	t _{d(off)} *5	$R_L = 12\Omega$	-	205	-	ns	
Fall time	t _f *5	$R_G = 10\Omega$	-	250	-		

•Gate Charge characteristics($T_a = 25$ °C)

Parameter	Symbol Conditions -		Values			Unit
r ai ainietei			Min.	Тур.	Max.	Offic
Total gate charge	Q_g^{*5}	$V_{DD} \simeq 50V$	-	90	-	
Gate - Source charge	Q _{gs} *5	I _D = 40A	-	12	1	nC
Gate - Drain charge	Q _{gd} *5	$V_{GS} = 10V$	-	18	1	
Gate plateau voltage	V _(plateau)	$V_{DD} \simeq 50V$, $I_D = 40A$	-	3.1	-	V

●Body diode electrical characteristics (Source-Drain)(T_a = 25°C)

Parameter	Symbol Conditions -			Unit		
r arameter			Min.	Тур.	Max.	Offic
Continuous source current	l _S *1	T _c = 25°C	ı	-	40	Α
Pulsed source current	I _{SM} *2	1 _c – 25 0	ı	-	80	Α
Forward voltage	V_{SD}^{*5}	$V_{GS} = 0V, I_S = 40A$	-	-	1.5	V
Reverse recovery time	t _{rr} *5	I _S = 40A	-	66	-	ns
Reverse recovery charge	Q _{rr} *5	di/dt = 100A/μs	-	100	-	μС

^{*1} Limited only by maximum temperature allowed.



^{*2} Pw \leq 10 μ s, Duty cycle \leq 1%

^{*3} L \simeq 200 μ H, V_{DD} = 50V, Rg = 10 Ω , starting T_j = 25°C

^{*4} Mounted on a epoxy PCB FR4 (27mm × 25mm × 0.8mm)

^{*5} Pulsed

Fig.1 Power Dissipation Derating Curve

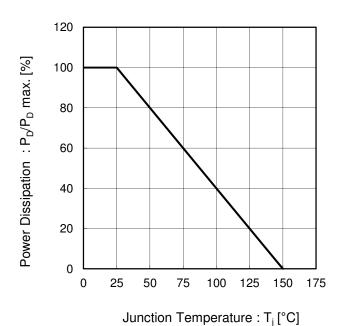
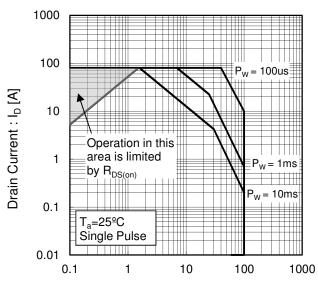
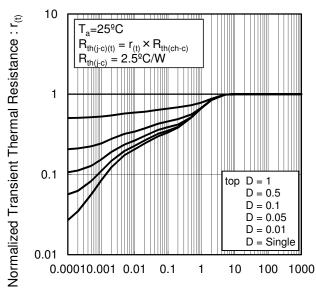


Fig.2 Maximum Safe Operating Area



Drain - Source Voltage : V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width



Pulse Width: Pw[s]

Fig.4 Avalanche Current vs Inductive Load

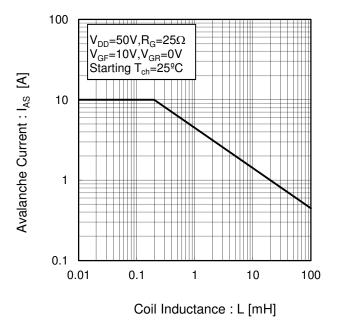
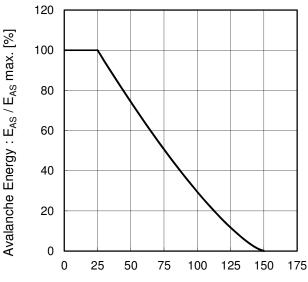
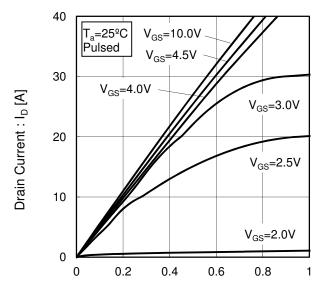


Fig.5 Avalanche Energy Derating Curve vs Junction Temperature



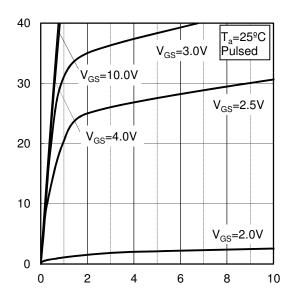
Junction Temperature : T_i [°C]

Fig.6 Typical Output Characteristics(I)



Drain - Source Voltage : $V_{DS}\left[V\right]$

Fig.7 Typical Output Characteristics(II)



Drain - Source Voltage : V_{DS} [V]

Drain Current : I_D [A]

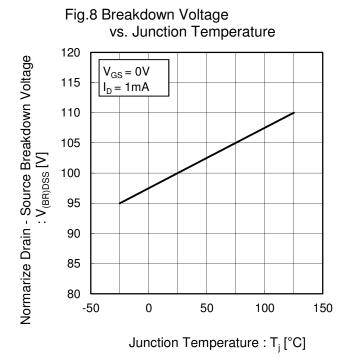
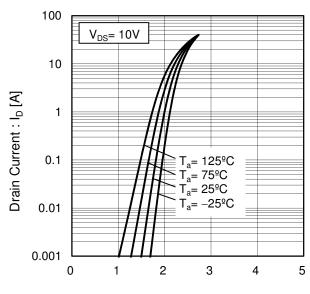


Fig.9 Typical Transfer Characteristics



Gate - Source Voltage : V_{GS} [V]

Fig.10 Gate Threshold Voltage vs. Junction Temperature

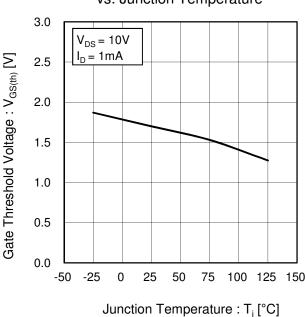
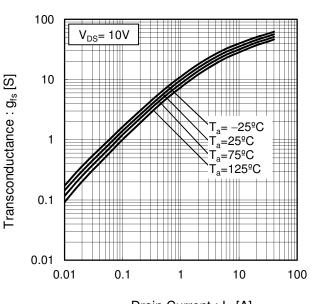


Fig.11 Transconductance vs. Drain Current



Drain Current: I_D [A]

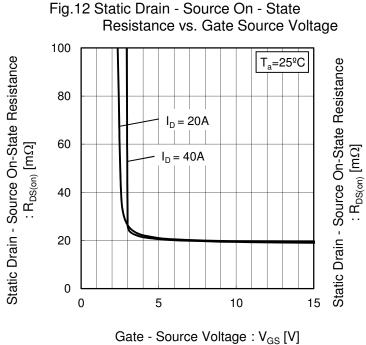


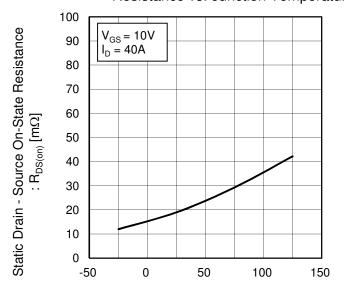
Fig. 13 Static Drain - Source On - State Resistance vs. Drain Current(I)

T_a=25°C

V_{GS}= 10V
V_{GS}= 4.0V

Drain Current : I_D [A]

Fig.14 Static Drain - Source On - State Resistance vs. Junction Temperature



Resistance vs. Drain Current(II)

1000 $V_{GS} = 10V$ $T_a = 125^{\circ}C$ $T_a = 75^{\circ}C$ $T_a = 25^{\circ}C$ $T_a = -25^{\circ}C$ $T_a = -25^{\circ}C$

Fig.15 Static Drain - Source On - State

Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)

1000

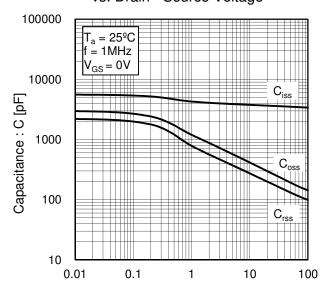
V_{GS}= 4.5V $T_a=125^{\circ}C$ $T_a=75^{\circ}C$ $T_a=25^{\circ}C$ $T_a=25^{\circ}C$ $T_a=25^{\circ}C$ $T_a=25^{\circ}C$ $T_a=25^{\circ}C$ $T_a=100$ Drain Current: $T_a=100$

Fig.17 Static Drain - Source On - State Resistance vs. Drain Current(IV) 1000 Static Drain - Source On-State Resistance $V_{GS} = 4.0V$ $: R_{DS(on)} \left[m\Omega \right]$ _a=125ºC =75ºC 100 10 0.01 0.1 1 10 100 Drain Current: I_D [A]

120 100 Drain Current Dissipation : I_D/I_D max. (%) 80 60 40 20 0 25 50 75 100 125 150 175 0 Junction Temperature : T_i [°C]

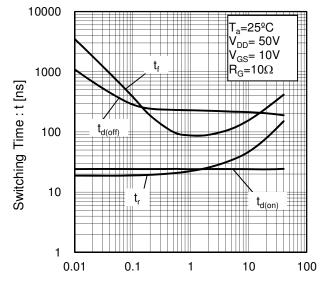
Fig.18 Drain Current Derating Curve

Fig.19 Typical Capacitance vs. Drain - Source Voltage



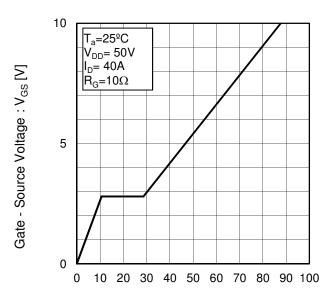
Drain - Source Voltage : V_{DS} [V]

Fig.20 Switching Characteristics



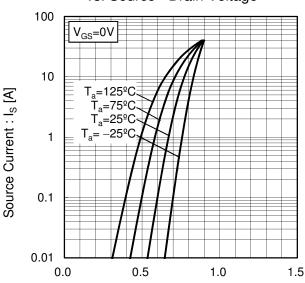
Drain Current : I_D [A]

Fig.21 Dynamic Input Characteristics



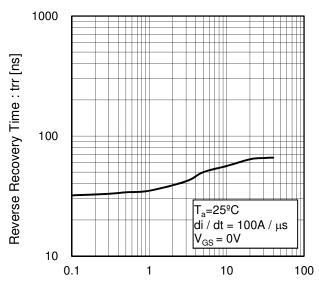
Total Gate Charge : Q_g [nC]

Fig.22 Source Current vs. Source - Drain Voltage



Source-Drain Voltage: V_{SD} [V]

Fig23 Reverse Recovery Time vs.Source Current



Source Current : I_S [A]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

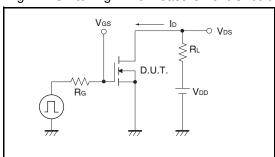


Fig.2-1 Gate Charge Measurement Circuit

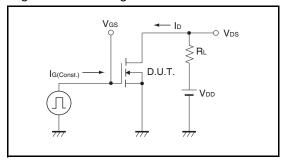


Fig.3-1 Avalanche Measurement Circuit

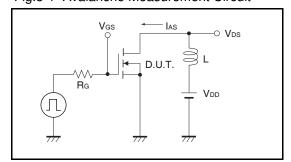


Fig.1-2 Switching Waveforms

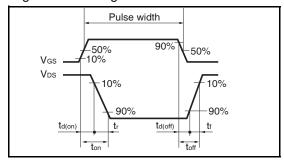


Fig.2-2 Gate Charge Waveform

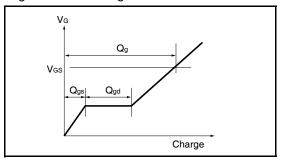
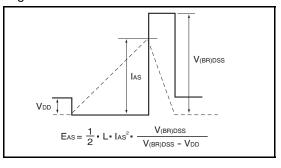
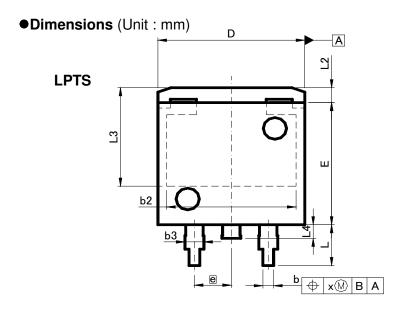
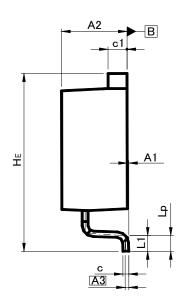
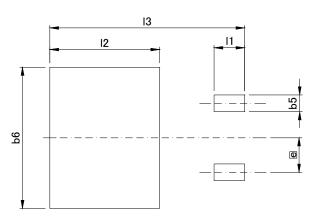


Fig.3-2 Avalanche Waveform









Patterm of terminal position areas

DIM	MILIM	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
A1	0.00	0.30	0	0.012	
A2	4.30	4.70	0.169	0.185	
A3	0.:	25	0.	01	
b	0.68	0.98	0.027	0.039	
b2	8.	90	0.	35	
b3	1.14	1.44	0.045	0.057	
С	0.30	0.60	0.012	0.024	
c1	1.10	1.50	0.043	0.059	
D	9.80	10.40	0.386	0.409	
E	8.80	9.20	0.346	0.362	
е	2.	54	0.	10	
HE	12.80	13.40	0.504	0.528	
L	2.70	3.30	0.106	0.13	
L1	0.90	1.50	0.035	0.059	
L2	1.10		0.043		
L3	7.25		0.285		
L4	1.00		0.039		
Lp	0.90	1.50	0.035	0.059	
Х	_	0.25	_	0.01	

DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
b5	-	1.23	ı	0.049	
b6	-	10.40	-	0.409	
11	-	2.10	-	0.083	
12	=	7.55	_	0.297	
13	-	13.40	-	0.528	

Dimension in mm/inches

Notice

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JAPAN	USA	FU	CHINA
CLASSII		CLASS II b	• • • • • • • • • • • • • • • • • • • •
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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 - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
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 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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