DN8650

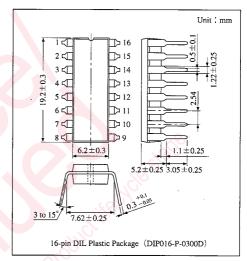
7-circuit Darlington Driver Array

Overview

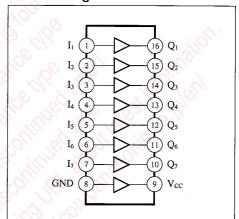
The DN8650, which is an NPN Darlington type buffer with PNP transistor for input, is a 7-circuit non-inverting type driver array.

Features

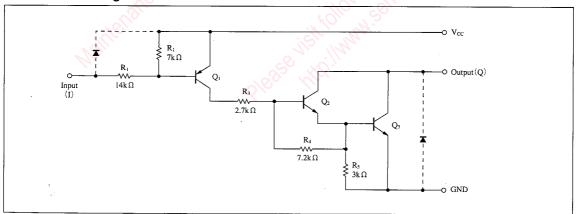
- 7-circuit buffer
- Low output breakdown voltage: V_{CE(SUS)} =35V (min)
- Large output current : I_O=500mA (max)
- · Low active input
- Directly connectable to MOS LSI and TTL in the 5V power supply system
- 16-pin plastic DIL package



■Block Diagram



Schematic Diagram



■ Absolute Maximum Ratings $(Ta=25^{\circ}C)$

Parameter	Symbol	Rating	Unit
Supply voltage	$V_{\rm cc}$	-0.5 to $+7.0$	V
Input voltage	V _I	-22 to $V_{\rm CC}+0.5$	V
Input current	I _I	10	mA
Output breakdown	V _{CE(SUS)}	-0.5 to + 35	V
Output current	Io	500	mA
GND pin current	I_{GND}	2.3	A
Power dissipation	P _D	1.0	W
Operating ambient temperature	Topr	-20 to +75	C
Storage temperature	T _{stg}	-55 to +150	°C

■ Recommended Operating Range (Ta=25°C)

Parameter	Symbol	min	typ	max	Unit
Supply voltage	V_{cc}	4.5	5.0	5.5	V
Output voltage	Vo	0		35	V
Output current	Io	0	— à	350 *1	mA
		0	- 40	300 *2	mA
Input voltage	V _I	-20	2,0	V_{cc}	V
Power consumption	Po		11/2	0.44	W

■ Electrical Characteristics (Ta=25°C)

Parameter	Symbol	Condition	min	typ	max	Unit
Output leakage current	I_{OLK}	$V_{CC}=5.5V, I_I=0, V_O=35V$		=	100	μ A
Output saturation voltage	$V_{\text{CE(sat)}}$	$V_{CC}=4.5V, V_{I}=0.8V, I_{O}=350mA$	912	1.4	2.2	V
Input current (output ON)	I _{I(ON)}	$V_{CC} = 5.5V, V_1 = 0.4V$		-0.32	-0.45	mA
		$V_{CC} = 5.5V, V_I = -20V$	10°	<u> </u>	-2.6	mA
Input current (output OFF)	$I_{\rm I(OFF)}$	30 00 21	-50	-100	-40	μΑ
Input voltage (output ON)	$V_{I(ON)}$	in lie in	-20		V _{cc} -3.5	V
Current consumption (output ON)	$I_{CC(ON)}$	V _{CC} =5.5V, Total input	2,	13.5	18.7	mA
Current consumption (output OFF)	I _{CC(OFF)}	V _{CC} =5.5V, Total input			100	μΑ
Turn-on time	t _{on}	$V_{cc}=5V$, $V_o=35V$		0.1	_	μs
Turn-off time	t _{OFF}	$R_L=100\Omega$, $C_L=15pF$		3		μs
Non	Ŕ	lege kithilih				

When one circuit operates at DC When all the seven circuits operate at $t_{pw}\!=\!25ms$ and $D_F\!=\!10\%$

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