



Clock Oscillators Surface Mount Type

KC3225L-P2/ KC3225L-P3 Series



LV-PECL/ 3.3V or 2.5V/ 3.2x2.5mm



RoHS Compliant

Features

- Miniature ceramic package
- Highly reliable with seam welding
- LV-PECL output
- Supply voltage $V_{CC} = 3.3V, 2.5V$
- $\pm 25 \times 10^{-6}$ available
- Low Phase Noise

Table 1

Freq. Tol. Code	Tol. $\times 10^{-6}$	Operating Temperature Range (°C)	Note
0	± 50	0 to +70	Standard specifications
S	± 30		
U	± 25		
F	± 100	-40 to +85	Please contact us for available frequencies.
G	± 50		
6	± 50	-40 to +105	

How to Order

KC3225L 125.000 P □ □ J 00
 ① ② ③ ④ ⑤ ⑥ ⑦

- ① Series
- ② Output Frequency
- ③ Output Type (LV-PECL)
- ④ Supply Voltage (3 : 3.3V or 2 : 2.5V)
- ⑤ Frequency Tolerance (See Table 1)
- ⑥ Symmetry/ INH Function
J : 45/ 55%, Stand-by
- ⑦ Individual Specification (STD Specification is "00")

Packaging (Tape & Reel 2000 pcs./ reel)

Specifications

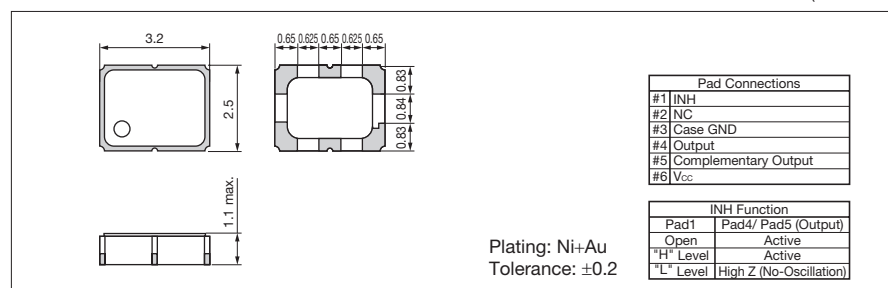
Item	Symbol	Conditions	Specifications		Units	
			KC3225L-P2	KC3225L-P3		
Output Frequency Range ^{Note1}	fo		25 to 175		MHz	
Frequency Tolerance	f _{tol}	Initial tolerance, Operating temperature range, Rated power supply voltage change, Load change, Aging (1 year @25°C), Shock and vibration	$\pm 50/ -40$ to +105°C		ppm	
			$\pm 100/ -40$ to +85°C			
			$\pm 50/ -40$ to +85°C			
			$\pm 50/ 0$ to +70°C			
			$\pm 30/ 0$ to +70°C			
Storage Temperature Range	T _{stg}		-55 to +125		°C	
Operating Temperature Range	T _{use}	Standard Specifications	0 to +70/ -40 to +85		°C	
		Extend (Option)	-40 to +105			
Max. Supply Voltage	—		-0.5 to +5.0		V	
Supply Voltage	V _{CC}		+2.375 to +2.625	+2.97 to +3.63	V	
Current Consumption	I _{CC}	$\leq 156.25\text{MHz}$	60 max.		mA	
		156.25MHz<	70 max.			
Stand-by Current	I _{std}		20 max.		μA	
Symmetry	SYM	50ohm @crossing point	50±5		%	
Rise/ Fall Time (20% V _{CC} to 80% V _{CC} Maximum Loaded)	tr/ tf	50ohm	0.6 max.		ns	
Low Level Output Voltage ^{Note2}	V _{OL}		V _{CC} -1.810 to V _{CC} -1.620		V	
High Level Output Voltage ^{Note2}	V _{OH}		V _{CC} -1.025 to V _{CC} -0.880		V	
Output Load	RL		50		ohm	
Input Voltage Range	V _{IN}		0 to V _{CC}			
Low Level Input Voltage	V _{IL}		30% V _{CC} max.		V	
High Level Input Voltage	V _{IH}		70% V _{CC} min.		V	
Disable Time	t _{dis}		150 max.		ns	
Enable Time	t _{ena}		10 max.		ms	
Start-up Time	t _{str}	@Minimum operating voltage to be 0 sec.	10 max.		ms	
Deterministic Jitter	DJ		2 max.		ps	
1 Sigma Jitter	J _{sigma}	Measured with Wavecrest SIA-3000	4 max.		ps	
Peak to Peak Jitter	J _{PK-PK}		30 max.		ps	
Phase Jitter	J _{Phase}	@156.25MHz V _{CC} = 3.3V	BW : 12kHz to 20MHz	0.3max.		ps
		@156.25MHz V _{CC} = 3.3V				
Phase Noise	—	@156.25MHz V _{CC} = 3.3V	@10Hz offset	Typ. -77		dBc/ Hz
			@100Hz offset	Typ. -103		
			@1kHz offset	Typ. -133		
			@10kHz offset	Typ. -143		
			@100kHz offset	Typ. -149		
			@1MHz offset	Typ. -149		
			@10MHz offset	Typ. -154		

Note : All electrical characteristics are defined at the maximum load and operating temperature range.

Note1: Please contact us for inquiry about operating temperature range, available frequencies and other conditions. Note2: DC characteristic

Dimensions

(Unit: mm)



Recommended Land Pattern

(Unit: mm)

