EV8125EF-00A

550mA, 8-14V Input, LNB Power Supply and Control Voltage Regulator Evaluation Board

The Future of Analog IC Technology

DESCRIPTION

The EV8125EF-00A is an evaluation board for MP8125, which is LNB power supply and control voltage regulator. The board provides efficient, low noise power to the Satellite receiver's RF LNB (Low Noise Block) converter.

The board accepts the supply voltage from 8V to 14V and the load current can be up to 550mA. The current limit can be adjustable with an external resistor.

The board provides a number of features described in the European EUTELSAT specification (DiSEqC) including: voltage selection of horizontal or vertical polarization directions of LNB and a selectable V_{OUT} compensation for voltage drop on the long coaxial cable. In accordance with DiSEqC standard, a tone signal of 22kHz is generated by an internal oscillator and can be activated or deactivated onto output by EXTM pin.

FEATURES

- DiSEqC 1.x Compatibility
- 8V to 14V Input Voltage
- Up to 550mA Output Current
- Programmable Current Limit
- 1V Line Drop Compensation
- Adjustable Soft-start Time
- POK Indicator
- Short Circuit Protection
- Over Temperature Protection

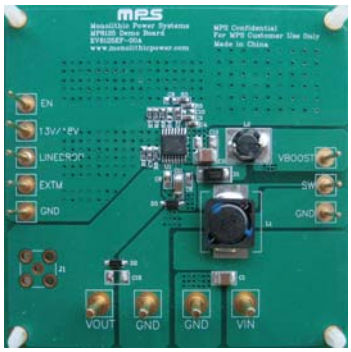
APPLICATIONS

- LNB Power Supply and Control for Satellite Set Top Boxes

ELECTRICAL SPECIFICATION

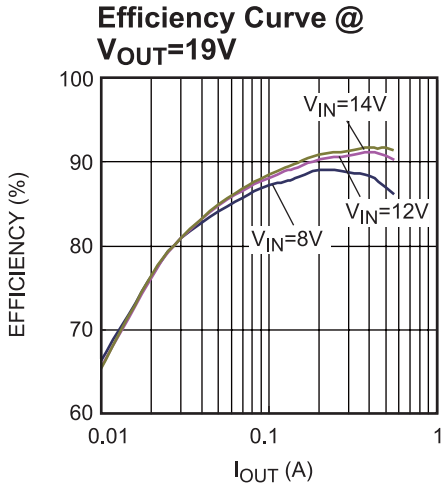
Parameter	Symbol	Value	Units
Input Voltage	V_{IN}	8-14	V
Output Voltage	V_{OUT}	19	V
Output Current	I_{OUT}	0-0.55	A

EV8125EF-00A EVALUATION BOARD

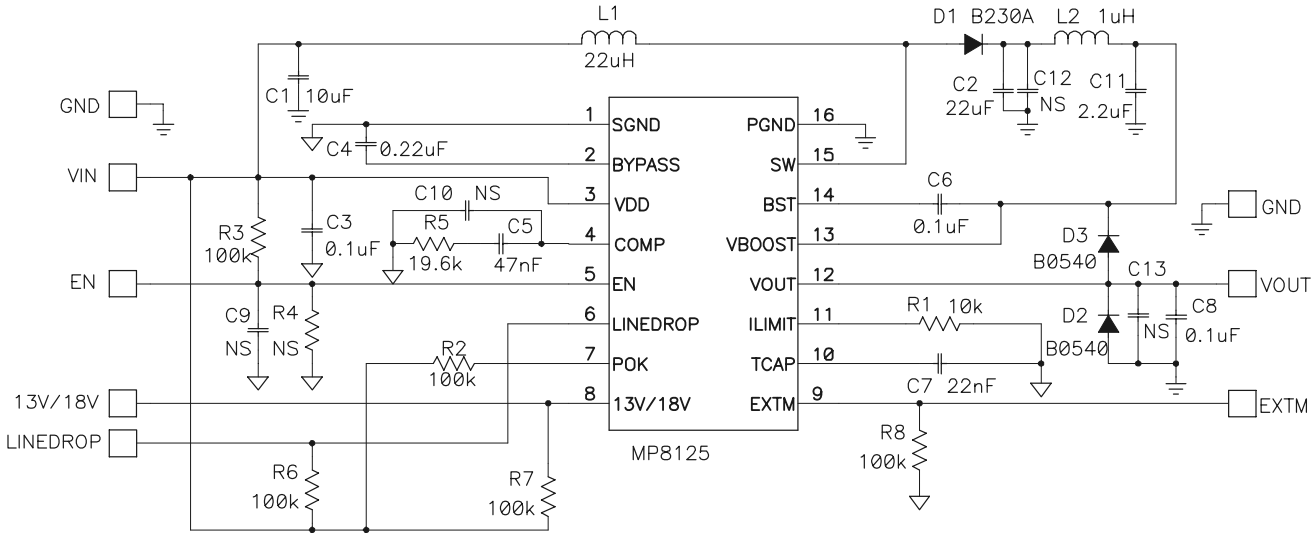


(L x W x H) 2.9" x 2.9" x 0.4"
7.3cm x 7.3cm x 1cm

Board Number	MPS IC Number
EV8125EF-00A	MP8125EF



EVALUATION BOARD SCHEMATIC



EV8125EF-00A BILL OF MATERIALS

Qty	Ref Des	Value	Description	Package	Manufacturer	Manufacturer P/N
1	C1	10uF	Ceramic Cap, X7R, 25V	1210	muRata	GRM32DR71E106KA12
1	C2	22uF	Ceramic Cap, X7R, 25V	1210	muRata	GRM32ER71E226KE15L
3	C3, C6, C8	0.1uF	Ceramic Cap, X7R, 100V	0603	muRata	GRM188R71H104KA93D
1	C4	0.22uF	Ceramic Cap, X7R, 16V	0603	muRata	GRM188R71C224KA01A
1	C5	47nF	Ceramic Cap, X7R, 50V	0603	muRata	GRM188R71H473KA61D
1	C7	22nF	Ceramic Cap, X7R, 50V	0603	muRata	GRM188R71223KA01D
0	C9, C10	NS	Do Not Stuff	0603		
1	C11	2.2uF	Ceramic Cap, X7R, 50V	1206	muRata	GRM31CR71H225KA88L
0	C12, C13	NS	Do Not Stuff	1210		
1	R1	10k	Film Res, 1%	0603	Royalohm	0603F1002T5E
5	R2, R3, R6, R7, R8	100k	Film Res, 5%	0603	Royalohm	0603J0104T5E
0	R4	NS	Do Not Stuff	0603		
1	R5	19.6k	Film Res, 1%	0603	Yageo	RC0603FR-0719K6L
1	D1		30V, 2A	SMA	Diodes	B230A
1	D2		40V, 0.5A	SOD-123	Diodes	B0540
1	D3		40V, 0.5A	SOD-123	Diodes	B0540
1	L1	22uH	SMD, 3A, 77.6mohm	SMD	TOKO	D104C-919AS-220M
1	L2	1uH	SMD, 3.7A, 17mohm	SMD	TOKO	D62LCB-#A918CY-1R0M
1	U1			TSSOP16	MPS	MP8125EF

PRINTED CIRCUIT BOARD LAYOUT

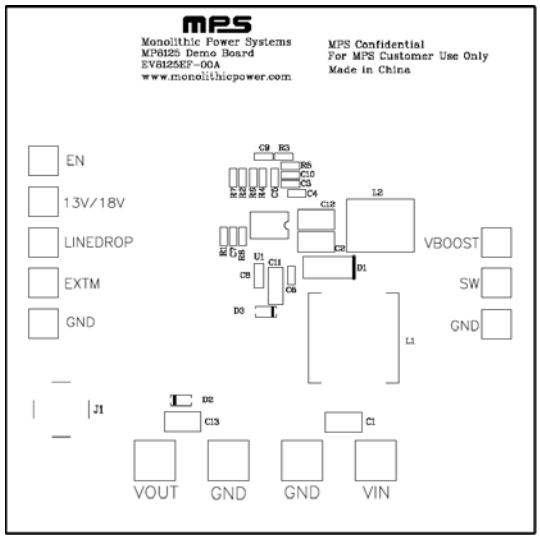


Figure 1—Top Silk Layer

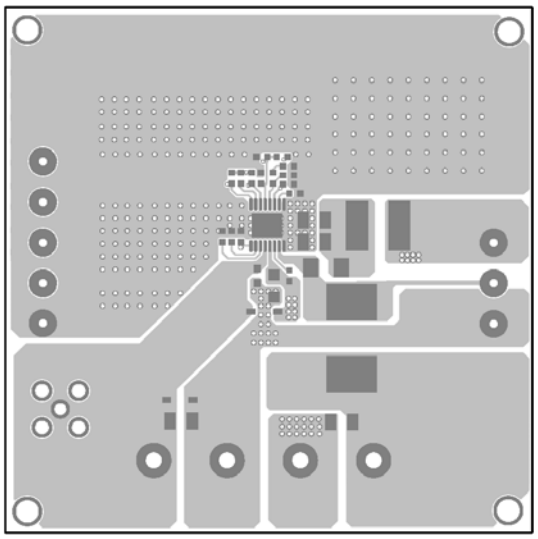


Figure 2—Top Layer

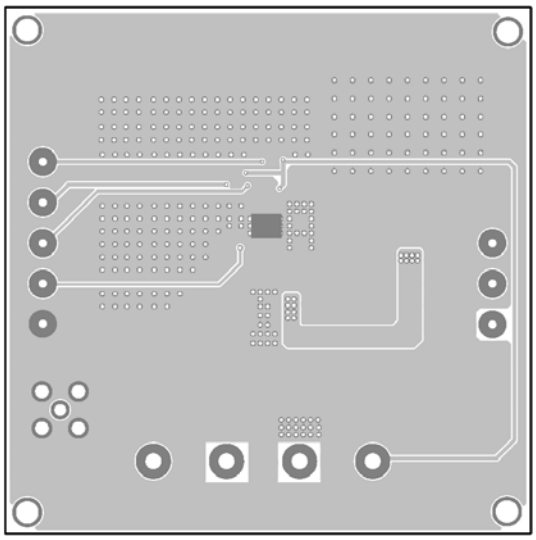


Figure 3—Bottom Layer

QUICK START GUIDE

1. Connect the positive terminal of the load to VOUT pins, and the negative terminal of the load to GND pins.
2. Preset the power supply output to 8-14V and turn off the power supply.
3. Connect the positive terminal of the power supply output to the VIN pin and the negative terminal of the power supply output to the GND pin.
4. Turn on the power supply. The board will automatically start up.
5. To use the Enable function, apply a logic input to the EN pin. Set EN higher than 2V to turn on the regulator or lower than 0.8V to turn it off.
6. To generate tone signal on output, apply a logic input to the EXTM pin. Set EXTM higher than 2V to activate the function or lower than 0.8V to deactivate it.
7. The output voltage of this board is set to 19V. To adjust the output voltage, apply a logic input on 13V/18V or LINEDROP pin. The output voltage under different conditions is shown in below table. “High” represents a voltage higher than 2V, “Low” represents a voltage lower than 0.8V.

13V/18V	LINEDROP	VOUT
High	High	19V
High	Low	18V
Low	High	14V
Low	Low	13V

8. The current limit can be adjusted by R1 and the soft start time be adjusted by C7. For further information, please refer to MP8125 datasheet.