

isc N-Channel MOSFET Transistor TK14E65W5, ITK14E65W5

• FEATURES

- Low drain-source on-resistance: $R_{DS(on)} \leq 0.3\Omega$.
- Enhancement mode: $V_{TH} = 3$ to $4.5V$ ($V_{DS} = 10 V$, $I_D = 0.69mA$)
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

• DESCRIPTION

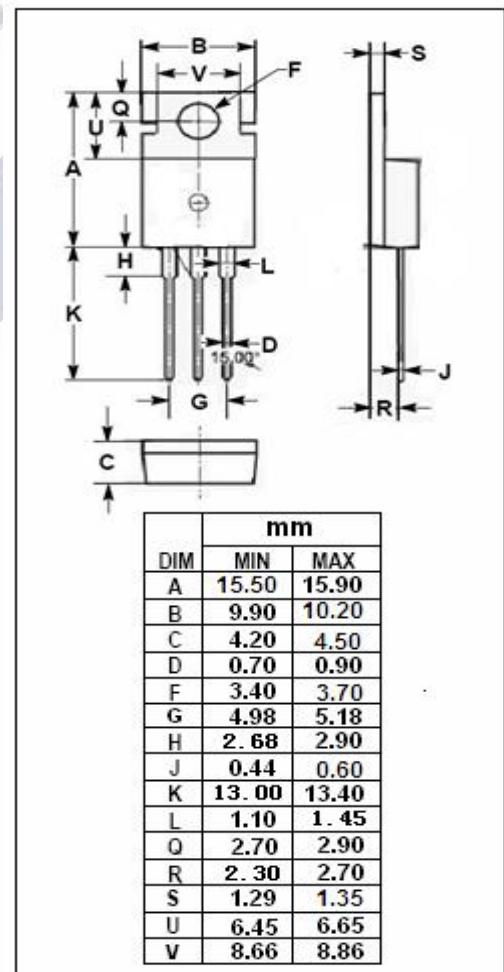
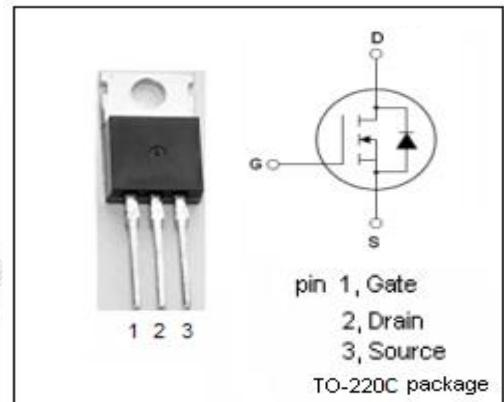
- Switching Voltage Regulators

• ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DSS}	Drain-Source Voltage	650	V
V_{GS}	Gate-Source Voltage	± 30	V
I_D	Drain Current-Continuous	13.7	A
I_{DM}	Drain Current-Single Pulsed	54.8	A
P_D	Total Dissipation @ $T_c=25^\circ C$	130	W
T_j	Max. Operating Junction Temperature	150	$^\circ C$
T_{stg}	Storage Temperature	-55~150	$^\circ C$

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	0.962	$^\circ C/W$
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	83.3	$^\circ C/W$



isc N-Channel MOSFET Transistor TK14E65W5, ITK14E65W5**ELECTRICAL CHARACTERISTICS** $T_c=25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}; I_D=10\text{mA}$	650			V
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=10\text{V}; I_D=0.69\text{mA}$	3		4.5	V
$R_{DS(\text{on})}$	Drain-Source On-Resistance	$V_{GS}=10\text{V}; I_D=6.9\text{A}$			0.3	$\text{m}\Omega$
I_{GSS}	Gate-Source Leakage Current	$V_{GS} = \pm 30\text{V}; V_{DS} = 0\text{V}$			± 1	μA
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=650\text{V}; V_{GS}=0\text{V}$			100	μA
$V_{SD(\text{F})}$	Diode forward voltage	$I_{DR}=13.7\text{A}, V_{GS}=0\text{V}$			1.7	V