



ACE14201T

P-Channel Enhancement Mode Power MOSFET

Description

The ACE14201T uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

Features

- V_{DS} (V) = -20V I_D = -14A
- $R_{DS(ON)} \leq 8.5\text{m}\Omega$ ($V_{GS} = -4.5\text{V}$)
- $R_{DS(ON)} \leq 12\text{m}\Omega$ ($V_{GS} = -2.5\text{V}$)
- $R_{DS(ON)} \leq 17\text{m}\Omega$ ($V_{GS} = -1.8\text{V}$)

Absolute Maximum Ratings @ $T_A=25^\circ\text{C}$ unless otherwise noted

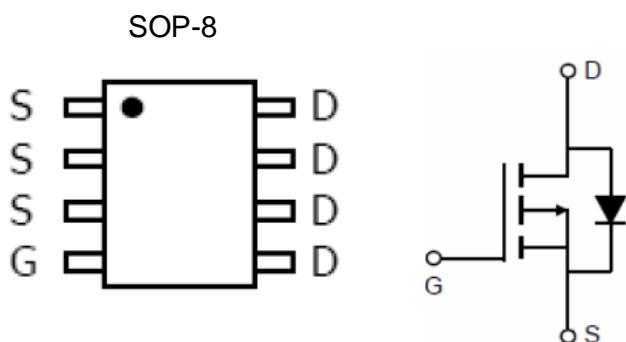
Parameter	Symbol	Max	Unit
Drain-Source Voltage	V_{DSS}	-20	V
Gate-Source Voltage	V_{GSS}	± 12	V
Drain Current (Continuous)*AC	I_D	-14	A
$T_A=100^\circ\text{C}$		-8.8	
Drain Current (Pulsed)*B	I_{DM}	-56	A
Avalanche energy L=0.1mH	E_{AS}, E_{AR}	22	mJ
Power Dissipation	P_D	2	W
Operating temperature / storage temperature	T_J/T_{STG}	-55~150	$^\circ\text{C}$
Thermal Resistance ,Junction-to-Ambient*	$R_{\theta JA}$	62	$^\circ\text{C}/\text{W}$

A: The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$. The value in any given application depends on the user's specific board design.

B: Repetitive rating, pulse width limited by junction temperature.

C: The current rating is based on the $t \leq 10\text{s}$ junction to ambient thermal resistance rating.

Packaging Type

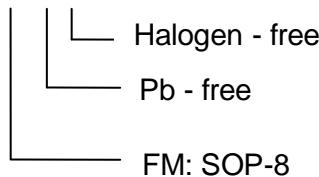




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Ordering information

ACE14201T XX + H



Electrical Characteristics $T_A=25^\circ\text{C}$, unless otherwise specified.

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-20			V
Zero gate voltage drain current	I_{DSS}	$V_{\text{DS}}=-20\text{V}, V_{\text{GS}}=0\text{V}$			-1	μA
Gate threshold voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=-250\mu\text{A}$	-0.3	-0.6		V
Gate leakage current	I_{GSS}	$V_{\text{GS}}=\pm 12\text{V}, V_{\text{DS}}=0\text{V}$			± 100	nA
Drain-source on-state resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-8\text{A}$		6.5		$\text{m}\Omega$
		$V_{\text{GS}}=-2.5\text{V}, I_{\text{D}}=-5\text{A}$		9		
		$V_{\text{GS}}=-1.8\text{V}, I_{\text{D}}=-3\text{A}$		12		
Forward trans conductance	g_{FS}	$V_{\text{DS}}=-10\text{V}, I_{\text{D}}=-5\text{A}$		20		S
Diode forward voltage	V_{SD}	$I_{\text{SD}}=-1\text{A}, V_{\text{GS}}=0\text{V}$			-1.2	V
Switching						
Total gate charge	Q_g	$V_{\text{GS}}=-4.5\text{V}, V_{\text{DS}}=-10\text{V}, I_{\text{D}}=-5\text{A}$		44.4		nC
Gate-source charge	Q_{gs}			7.2		
Gate-drain charge	Q_{gd}			10.2		
Turn-on delay time	$t_{\text{d}(\text{on})}$	$V_{\text{GS}}=-10\text{V}, V_{\text{DD}}=-15\text{V}, I_{\text{D}}=-1\text{A}, R_{\text{GEN}} = 6\Omega$		13.2		ns
Turn-on rise time	Tr			68		
Turn-off delay time	$t_{\text{d}(\text{off})}$			160		
Turn-off fall time	Tf			154		
Dynamic						
Input capacitance	C_{iss}	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=-15\text{V}, f=1.0\text{MHz}$		4060		pF
Output capacitance	C_{oss}			520		
Reverse transfer capacitance	C_{rss}			400		



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Typical Performance Characteristics

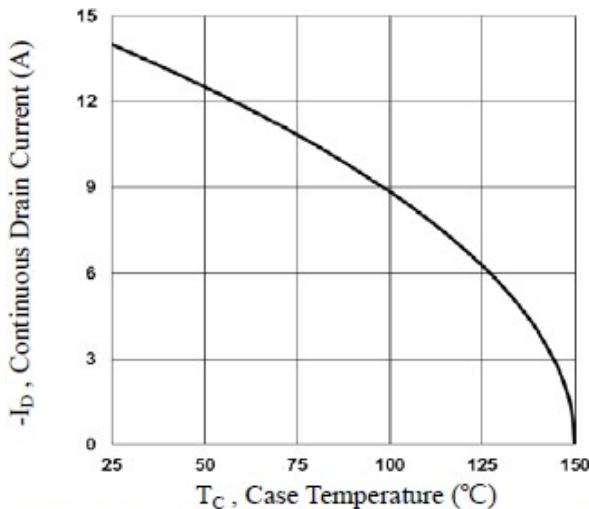


Fig.1 Continuous Drain Current vs. T_C

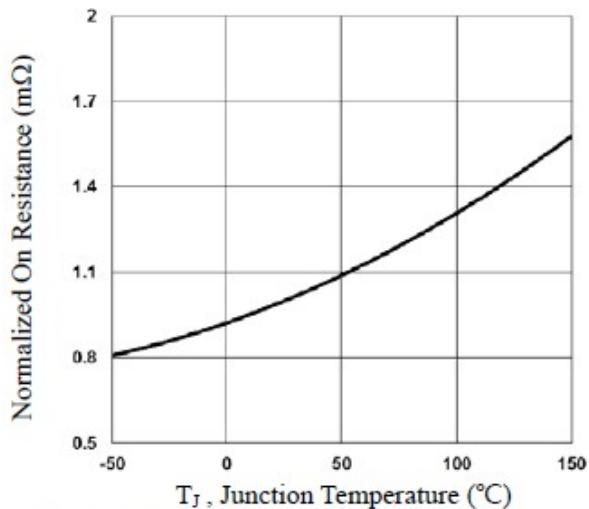


Fig.2 Normalized $R_{DS(on)}$ vs. T_J

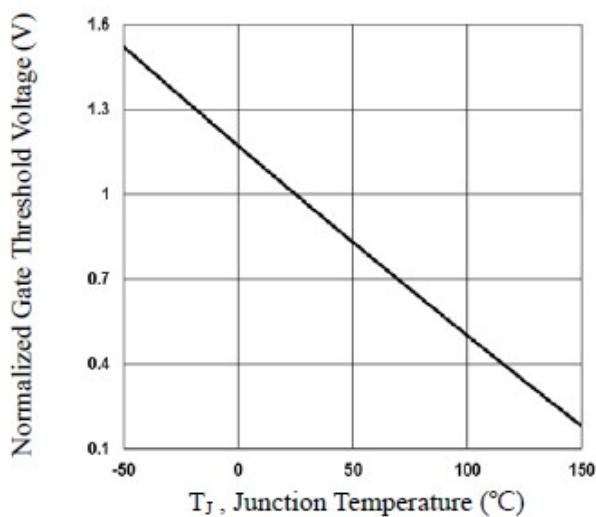


Fig.3 Normalized V_{th} vs. T_J

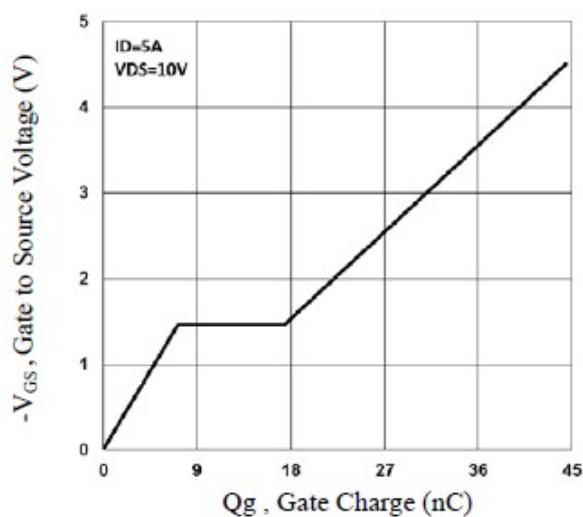


Fig.4 Gate Charge Waveform

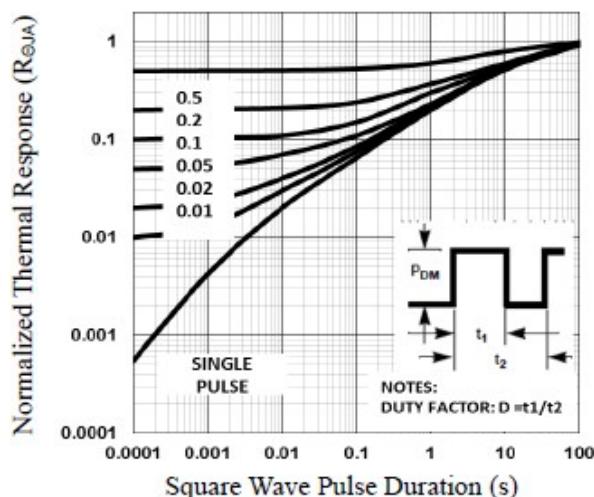


Fig.5 Normalized Transient Response

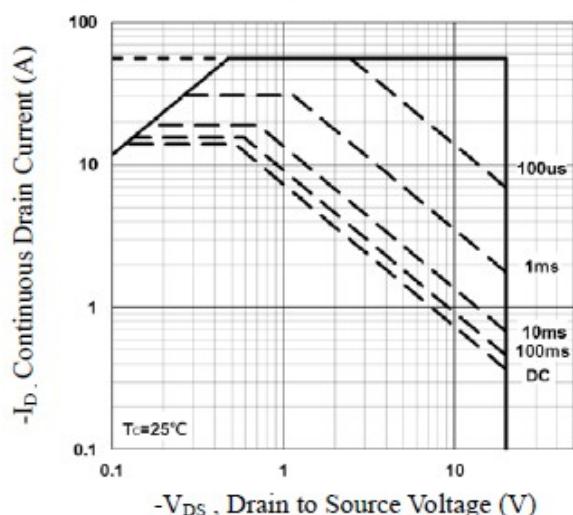


Fig.6 Maximum Safe Operation Area



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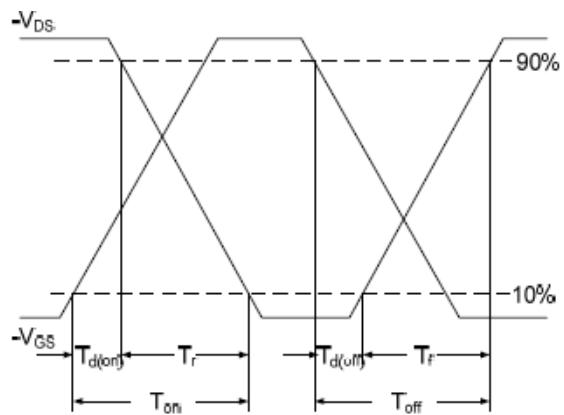


Fig.7 Switching Time Waveform

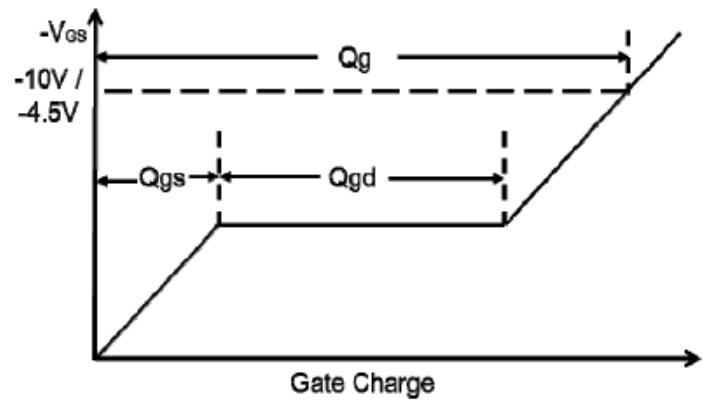


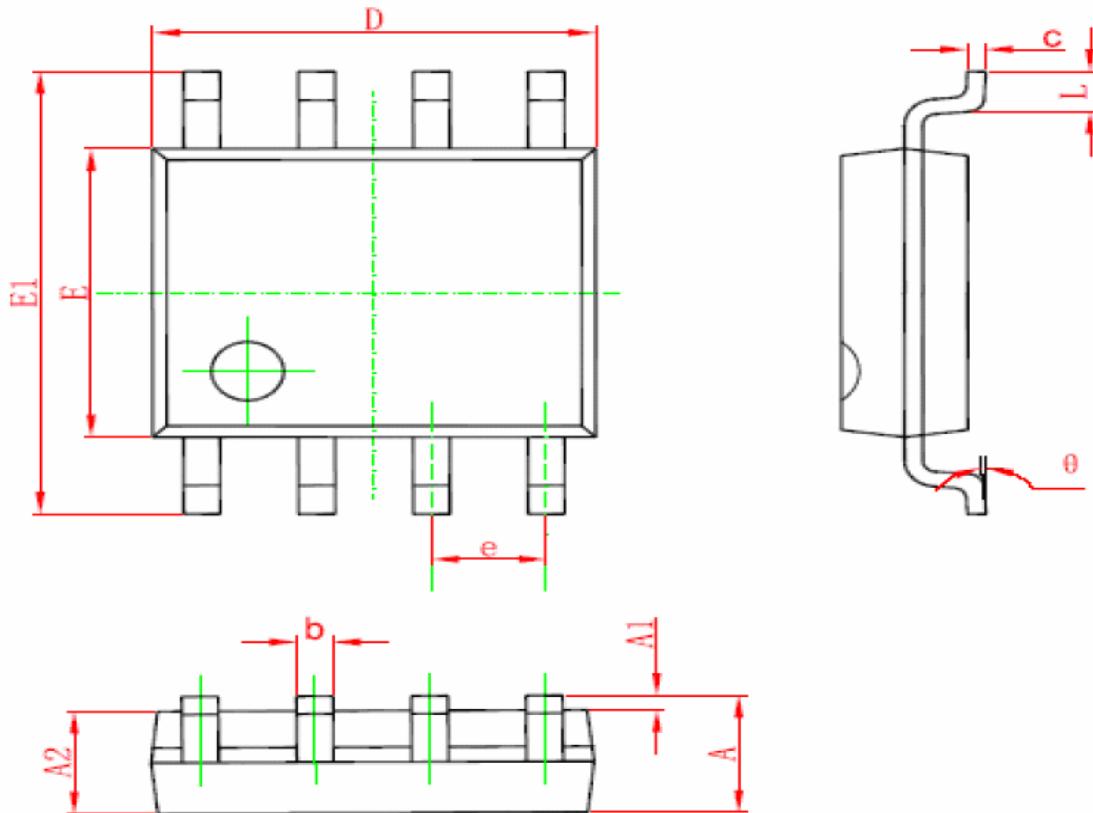
Fig.8 Gate Charge Waveform



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Packing Information

SOP-8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



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Notes

ACE does not assume any responsibility for use as critical components in life support devices or systems without the express written approval of the president and general counsel of ACE Electronics Co., LTD. As sued herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and shows failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.