Description

The 74AHCT05 provides provides six independent inverters with open drain outputs. The device is designed for operation with a power supply range of 4.5V to 5.5V.

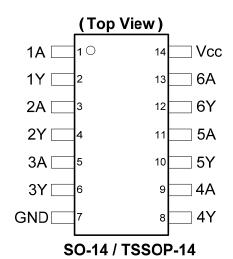
The gates perform the Boolean function:

$$Y = \overline{A}$$

Features

- Wide Supply Voltage Range from 4.5V to 5.5V
- Inputs Are TTL Voltage Level Compatible
- Outputs Sink or Source 8mA at V_{CC} = 4.5V
- **CMOS Low Power Consumption**
- Schmitt Trigger Action at All Inputs
- ESD Protection Exceeds JESD 22
 - 200-V Machine Model (A115-A)
 - 2000-V Human Body Model (A114-A)
 - Exceeds 1000-V Charged Device Model (C101C)
- Latch-Up Exceeds 250mA per JESD 78, Class II
- Range of Package Options SO-14 and TSSOP-14
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Pin Assignments



Applications

- General Purpose Logic
- Wide array of products such as:
 - PCs, Networking, Notebooks, Netbooks
 - Computer Peripherals, Hard Drives, CD/DVD ROM
 - TV, DVD, DVR, Set Top Box

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

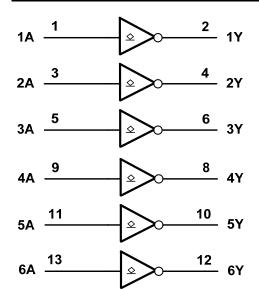
Click here for ordering information, located at the end of datasheet



Pin Descriptions

Pin Number	Pin Name	Function
1	1A	Data Input
2	1Y	Data Output
3	2A	Data Input
4	2Y	Data Output
5	3A	Data Input
6	3Y	Data Output
7	GND	Ground
8	4Y	Data Output
9	4A	Data Input
10	5Y	Data Output
11	5A	Data Input
12	6Y	Data Output
13	6A	Data Input
14	V _{CC}	Supply Voltage

Logic Diagram



Function Table

Input	Output
Α	Υ
L	Z
Н	L

Absolute Maximum Ratings (Note 4) (@T_A = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD CDM	Charged Device Model ESD Protection	1	KV
ESD MM	Machine Model ESD Protection	200	V
V _{CC}	Supply Voltage Range	-0.5 to +7.0	V
VI	Input Voltage Range	-0.5 to +7.0	V
I _{IK}	Input Clamp Current V _I < -0.5V	-20	mA
I _{OK}	Output Clamp Current V _O < 0V	-20	mA
I _{OK}	Output Clamp Current V _O > V _{CC}	20	mA
Io	Continuous Output Current 0V < V _O < V _{CC}	+/- 25	mA
Icc	Continuous Current Through V _{CC}	50	mA
I _{GND}	Continuous Current Through GND	-50	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C
Ртот	Total Power Dissipation	500	mW

Note: 4. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.



Recommended Operating Conditions (Note 5) (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Min	Max	Unit
Vcc	Supply Voltage	4.5	5.5	V
VI	Input Voltage	0	5.5	V
Vo	Output Voltage	0	V _{CC}	V
Δt/ΔV	Input Transition Rise or Fall Rate		20	ns/V
TA	Operating Free-Air Temperature	-40	+125	°C

Note:

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Test Conditions	V	T _A = -40°C to +85°C		T _A = -40°C to +125°C		Unit	
Symbol	Parameter	rest Conditions	V _{CC}	Min	Max	Min	Max		
V _{IH}	High-Level Input Voltage		4.5V to 5.5V	2.0		2.0		V	
V _{IL} Low-Level Input Voltage			4.5V to 5.5V		0.8		0.8	V	
.,	Low-Level Output	I _{OL} = 50μA	4.5V		0.1		0.1	V	
V_{OL}	Voltage	I _{OL} = 8mA	4.5V		0.44		0.55	٧	
l _{OZ}	Z State Leakage Current	V _O = 0 to 5.5V	5.5V		±2.5		±10	μΑ	
I _I	Input Current	V _I = GND to 5.5V	3.6V		±1		±2	μΑ	
Icc	Supply Current	$V_I = GND \text{ or } V_{CC}, I_O = 0$	3.6V		20		40	μΑ	
ΔI _{CC}	Additional Supply Current	One input at V _{CC} -2.1V Other pins at V _{CC} or GND	5.5V		1.35		5	mA	

Operating Characteristics

	Parameter	Test Conditions	V _{CC} = 5.5V Typ	Unit
C _{pd}	Power Dissipation Capacitance per Gate	f = 1MHz	5	pF
C _i	Input Capacitance	$V_i = V_{CC} - or$ GND	4.0	pF

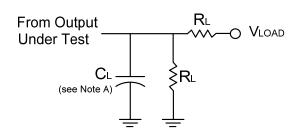
Switching Characteristics

Comple at	Dama ma atau	Test	V	7	Γ _A = +25°C	;	-40°C to	+85°C	-40°C to	+125°C	11
Symbol	Symbol Parameter Conditi	Conditions	V _{cc}	Min	Тур	Max	Min	Max	Min	Max	Unit
	Propagation	Figure 1 C _L = 15pF	4.5V to 5.5V	0.5	3.4	6.9	0.5	8.0	0.5	9.0	
t _{PD}	Delay A _N to Y _N	Figure 1 C _L = 50pF	4.5V to 5.5V	0.5	4.9	10.0	0.5	10.0	0.5	11.0	ns

^{5.} Unused inputs should be held at $V_{\text{\footnotesize CC}}$ or Ground.



Parameter Measurement Information



TEST	Condition
t _{PLZ} (see Notes D and E)	Vload
t _{PZL} (see Notes D and F)	Vload

V	Inputs		V _M	V _M	v		В	V/A
V _{CC}	VI	t _r /t _f	Inputs	Outputs	VLOAD	CL CL	KL	V Δ
4.5V to 5.5V	3 V	≤3ns	1.5V	V _{CC} /2	2 X V _{CC}	15pF, 50pF	2K	0.3V

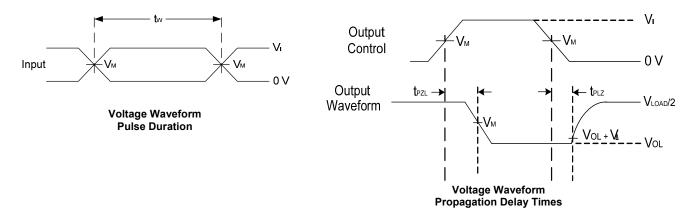


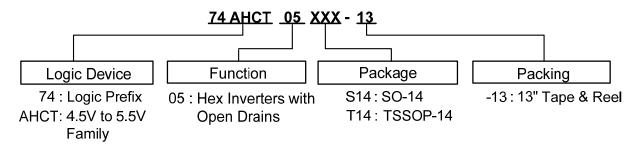
Figure 1 Load Circuit and Voltage Waveforms

Notes: A. Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 1 MHz.
- C. The inputs are measured one at a time with one transition per measurement.
- D. For the open drain device t_{PLZ} and t_{PZL} are the same as t_{PD} .
- $\text{E. } t_{PZL} \text{ is measured at } V_{M}.$
- D. t_{PLZ} is measured at V_{OL} +V $_{\!\Delta}.$



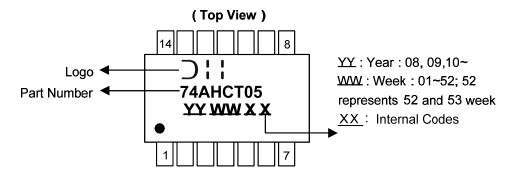
Ordering Information



	Part Number	Package Code	Dookoging	7" Tape	and Reel
	Part Number	Package Code	Packaging	Quantity	Part Number Suffix
Physic Green	74AHCT05S14-13	S14	SO-14	2500/Tape & Reel	-13
Pb.	74AHCT05T14-13	T14	TSSOP-14	2500/Tape & Reel	-13

Marking Information

(1) SO-14, TSSOP-14



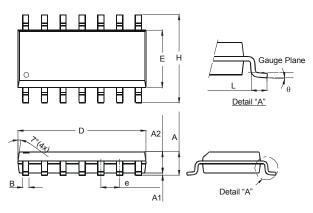
Part Number	Package
74AHCT05S14	SO-14
74AHCT05T14	TSSOP-14



Package Outline Dimensions (All dimensions in mm.)

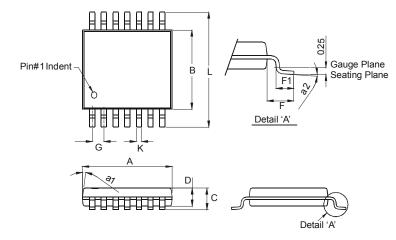
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

Package Type: SO-14



	SO-14	
Dim	Min	Max
Α	1.47	1.73
A1	0.10	0.25
A2	1.45	Тур
В	0.33	0.51
D	8.53	8.74
Е	3.80	3.99
е	1.27	Тур
Н	5.80	6.20
L	0.38	1.27
θ	0°	8°
All Din	nensions	in mm

Package Type: TSSOP-14



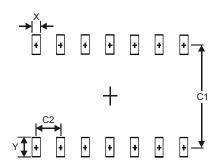
TSSOP-14			
Dim	Min	Max	
a1	7° (4X)		
a2	0°	8°	
Α	4.9	5.10	
В	4.30	4.50	
C	_	1.2	
D	0.8	1.05	
F	1.00 Typ		
F1	0.45	0.75	
G	0.65 Typ		
K	0.19	0.30	
L	6.40 Typ		
All Dimensions in mm			



Suggested Pad Layout

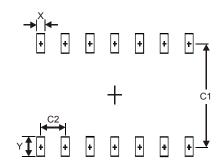
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

Package Type: SO-14



Dimensions	Value (in mm)
Х	0.60
Υ	1.50
C1	5.4
C2	1.27

Package Type: TSSOP-14



Dimensions	Value (in mm)	
X	0.45	
Υ	1.45	
C1	5.9	
C2	0.65	



IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.

Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
 - 1. are intended to implant into the body, or
 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2013, Diodes Incorporated

www.diodes.com