RT1P130X SERIES

Transistor With Resistor For Switching Application Silicon PNP Epitaxial Type

DESCRIPTION OUTLINE DRAWING UNIT: mm RT1P130X is a one chip transistor RT1P130U RT1P130C with built-in bias resistor,NPN type is RT1N130X. 1.6 2.5 0.4 0.8 0.5 1.5 0.4 0.5 50 90 2.9 1.6 1.0 **APPLICATION** Inverted circuit, switching circuit, interface circuit, driver circuit. 0.7 0.55 0.8 Equivalent circuit 0 ~ 0.1 0 ~ 0.1 С JEITA: -JEITA: SC-59 (OUT) R1 JEDEC: -JEDEC: Similar to TO-236 В -MM 0 **Terminal Connector Terminal Connector** (IN) : Base : Base : Emitter : Emitter : Collector : Collector Е (GND) RT1P130M **RT1P130S** 4.0 2.1 1.25 0.425 0.425 3.0 0.65 0.1 2.0 14.0 1.3 8 .45 1.27 1.27 6.0 0 ~ 0.1 JEITA: SC-70 JEITA: -JEDEC: -JEDEC: -**Terminal Connector Terminal Connector** : Base : Emitter : Emitter : Collector : Collector : Base

ISAHAYA ELECTRONICS CORPORATION

FEATURE

•Built-in bias resistor (R1=1k).

Transistor

RT1P130X SERIES

MAXIMUM RATING (Ta=25)

SYMBOL	PARAMETER	RATING				UNIT
		RT1P130U	RT1P130M	RT1P130C	RT1P130S	
V _{CBO}	Collector to Base voltage	-50				V
V _{EBO}	Emitter to Base voltage	-6				V
V _{CEO}	Collector to Emitter voltage	-50				V
I _c	Collector current	-100				mA
I _{CM}	Peak Collector current	-200				mA
Pc	Collector dissipation(Ta=25)	150	2	00	450	mW
Tj	Junction temperature	+150 +150				
Tstg	Storage temperature	-55 ~ +150	+150 -55 ~ +150			

ELECTRICAL CHARACTERISTICS (Ta=25)

SY	SYMBOL	PARAMETER	TEST CONDITION	LIMIT			UNIT
	STMBOL		TEST CONDITION	MIN	TYP	MAX	UNIT
	$V_{(BR)CEO}$	C to E break down voltage	Ι _c =-100 μ A , R _{BE} =	-50			V
	I _{CBO}	Collector cut off current	V _{CB} =-50V , I _E =0			-0.1	μA
	h _{FE}	DC forward current gain	V _{CE} =-5V , I _C =-1mA	100			-
Ī	$V_{CE(sat)}$	C to E saturation voltage	I _c =-10mA , I _B =-0.5mA			-0.3	V
Ī	R ₁	Input resistance		0.7	1.0	1.3	k
	f _T	Gain band width product	V_{ce} =-6V , I _e =10mA		150		MHz



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