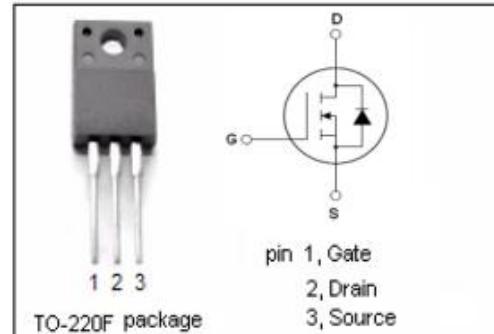


Isc N-Channel MOSFET Transistor

TK50A04K3, ITK50A04K3

• FEATURES

- Low drain-source on-resistance:
 $R_{DS(ON)} = 3.5\text{m}\Omega$ ($V_{GS} = 10\text{ V}$)
- Enhancement mode:
 $V_{th} = 3.0$ to 4.0V ($V_{DS} = 10\text{ V}$, $I_D=1\text{mA}$)
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

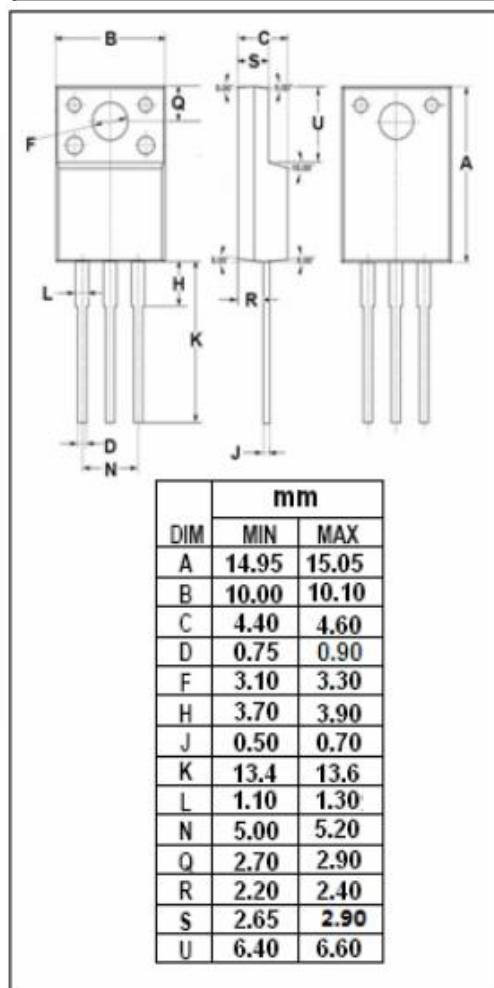


• DESCRIPTION

- Switching Voltage Regulators

• ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DSS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current-Continuous	50	A
I_{DM}	Drain Current-Single Pulsed	200	A
P_D	Total Dissipation @ $T_c=25^\circ\text{C}$	42	W
T_j	Max. Operating Junction Temperature	175	$^\circ\text{C}$
T_{stg}	Storage Temperature	-55~175	$^\circ\text{C}$



• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	3.57	$^\circ\text{C}/\text{W}$
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	62.5	$^\circ\text{C}/\text{W}$

Isc N-Channel MOSFET Transistor**TK50A04K3, ITK50A04K3****ELECTRICAL CHARACTERISTICS** $T_c=25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}; \text{I}_D= 10\text{mA}$	40			V
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}= 10\text{V}; \text{I}_D=1\text{mA}$	3.0		4.0	V
$\text{R}_{\text{DS(on)}}$	Drain-Source On-Resistance	$\text{V}_{\text{GS}}=10\text{V}; \text{I}_D=25\text{A}$			3.5	$\text{m}\Omega$
I_{GSS}	Gate-Source Leakage Current	$\text{V}_{\text{GS}}= \pm 20\text{V}; \text{V}_{\text{DS}}= 0\text{V}$			± 0.1	μA
I_{DSS}	Drain-Source Leakage Current	$\text{V}_{\text{DS}}= 40\text{V}; \text{V}_{\text{GS}}= 0\text{V}$			10	μA
V_{SDF}	Diode forward voltage	$\text{I}_{\text{DR}} = 50\text{A}, \text{V}_{\text{GS}} = 0 \text{ V}$			1.2	V