1200 V SiC MPS™ Diode

Silicon Carbide Power Schottky Diode



V_{RRM}	=	1200 V		
I _{F (Tc = 135°C)}	=	36 A*		
Q_c	=	92 nC*		

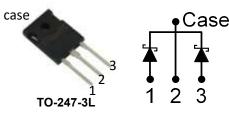
Features

- High Avalanche (UIS) Capability
- Enhanced Surge Current Capability
- 175 °C Maximum Operating Temperature
- Temperature Independent Switching Behavior
- Positive Temperature Coefficient Of V_F
- Extremely Fast Switching Speeds
- Superior Figure of Merit Q_C/I_F

Advantages

- Low Standby Power Losses
- Improved Circuit Efficiency (Lower Overall Cost)
- Low Switching Losses
- Ease of Paralleling Devices without Thermal Runaway
- Smaller Heat Sink Requirements
- Low Reverse Recovery Current
- Low Device Capacitance
- Low Reverse Leakage Current at Operating Temperature

Package







Applications

- Power Factor Correction (PFC)
- Switched-Mode Power Supply (SMPS)
- Solar Inverters
- Wind Turbine Inverters
- Motor Drives
- Induction Heating
- Uninterruptible Power Supply (UPS)
- High Voltage Multipliers

Absolute Maximum Ratings

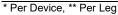
Parameter	Symbol	Conditions	Values	Unit	
Repetitive Peak Reverse Voltage (Per Leg)	V_{RRM}		1200	V	
Continuous Forward Current (Per Leg/Per Device)	l _F	$T_C = 25 ^{\circ}\text{C}, D = 1$ $T_C = 135 ^{\circ}\text{C}, D = 1$ $T_C = 166 ^{\circ}\text{C}, D = 1$	36/72 18/36 8/16	Α	
Non-Repetitive Peak Forward Surge Current, Half Sine Wave (Per Leg)	I _{F,SM}	$T_C = 25 ^{\circ}\text{C}, t_P = 10 \text{ms}$ $T_C = 150 ^{\circ}\text{C}, t_P = 10 \text{ms}$	75 56	А	
Repetitive Peak Forward Surge Current, Half Sine Wave (Per Leg)	$I_{F,RM}$	T_C = 25 °C, t_P = 10 ms T_C = 150 °C, t_P = 10 ms	41 24	Α	
Non-Repetitive Peak Forward Surge Current (Per Leg)	I _{F,max}	T _C = 25 °C, t _P = 10 μs	700	Α	
I ² t Value (Per Leg)	∫i² dt	$T_{\rm C}$ = 25 °C, $t_{\rm P}$ = 10 ms	25	A ² s	
Non-Repetitive Avalanche Energy (Per Leg)	E _{AS}	$L = 5 \text{ mH}, I_{AV} = 9 \text{ A}, V_{DD} = 60 \text{ V}$	110	mJ	
Diode Ruggedness (Per Leg)	dV/dt	$V_R = 0 \sim 960 \text{ V}$	100	V/µs	
Power Dissipation (Per Leg/Per Device)	P_{tot}	T _C = 25 °C	272/544	W	
Operating and Storage Temperature	T_{j} , T_{stg}		-55 to 175	°C	

Electrical Characteristics (Per Leg)

Parameter	Cumph of	Conditions min.			Values		l lm!4
	Symbol			min.	typ.	max.	Unit
Diode Forward Voltage	V _F	$I_F = 8 A, T_j = 2$			1.5	1.8	V
	- 1	I _F = 8 A, T _j = 175 °C		2.3	2.7	<u> </u>	
Reverse Current	1	V _R = 1200 V, T _i = 25 °C		0.8	11	μΑ	
	I _R	V _R = 1200 V, T _j = 175 °C		6	76		
Total Capacitive Charge		V _R = 400 V			31		nC
	Q _C		V _R = 800 V		46		IIC
Conitation of Times		dI _F /dt = 200 A/μs Τ _i = 175 °C	V _R = 400 V		- 10		
Switching Time	t _s		V _R = 800 V		< 10	ns	
Total Capacitance		V _R = 1 V, f = 1 MHz,	T _i = 25 °C		509		
	С	$V_R = 800 \text{ V}, f = 1 \text{ MHz}$	z, T _i = 25 °C		34		pF

Thermal / Mechanical Characteristics

Thermal Resistance, Junction - Case (Per Leg	I I I I I I I I I I I I I I I I I I I	0.55	°C/W





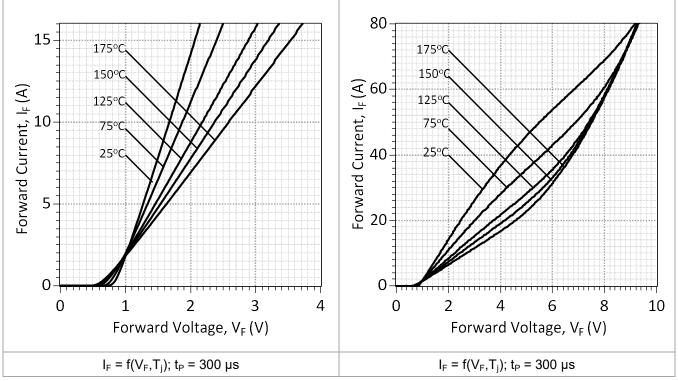


Figure 1: Typical Forward Characteristics (Per Leg)

350 300 175°C Power Dissipated (W) 100-Reverse Current, I_R (A) 125°C 25°C 10^{-7} 50 10^{-8} 50 75 100 125 150 175 200 25 200 400 600 800 1000 1200 Reverse Voltage, V_R (V) Case Temperature, T_C (°C) $I_R = f(V_R, T_j)$ $P_{tot} = f(T_j)$

Figure 2: Typical High Current Forward Characteristics (Per Leg)

Figure 4: Power Derating Curve (Per Leg)

Figure 3: Typical Reverse Characteristics (Per Leg)

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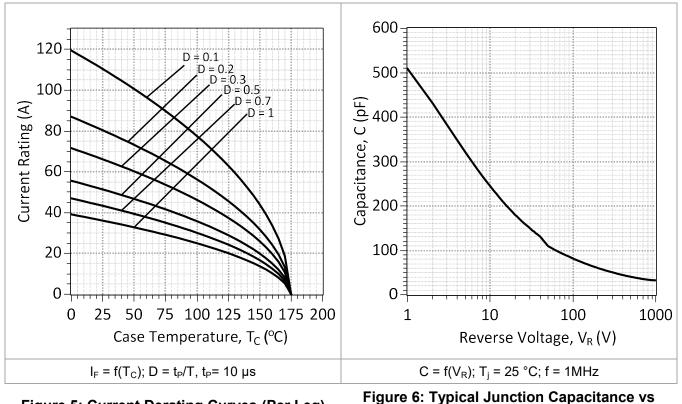


Figure 5: Current Derating Curves (Per Leg)

60 20 18 50 Capacitive Charge, $Q_C(nC)$ 16 Stored Energy, E $_{
m C}$ (μ J) 14 12 10 8 10 2 0 200 400 600 800 200 400 600 800 0 1000 0 1000 Reverse Voltage, V_R (V) Reverse Voltage, V_R (V) $Q_c = f(V_R)$; $T_j = 25$ °C; f = 1MHz $E_C = f(V_R); T_j = 25 \, ^{\circ}C; f = 1MHz$

Figure 7: Typical Capacitive Charge vs. **Reverse Voltage Characteristics (Per Leg)**

Reverse Voltage Characteristics (Per Leg)

Figure 8: Typical Capacitive Energy vs. Reverse Voltage Characteristics (Per Leg)



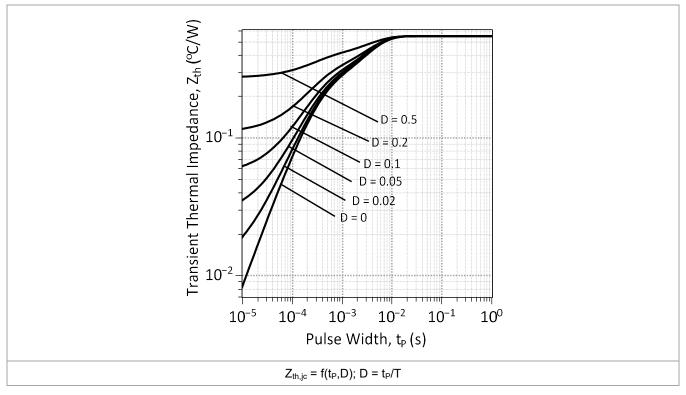


Figure 9: Transient Thermal Impedance (Per Leg)

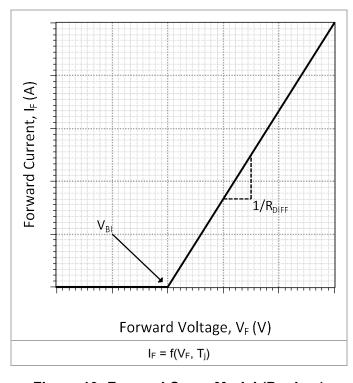


Figure 10: Forward Curve Model (Per Leg)

$$I_F = (V_F - V_{BI})/R_{DIFF}$$
Built-In Voltage (V_{BI}):
$$V_{BI}(T_j) = m^*T_j + b,$$

Differential Resistance (RDIFF):

m = -1.34e-03, b = 0.915

$$R_{DIFF}(T_j) = a^*T_j^2 + b^*T_j + c (\Omega);$$

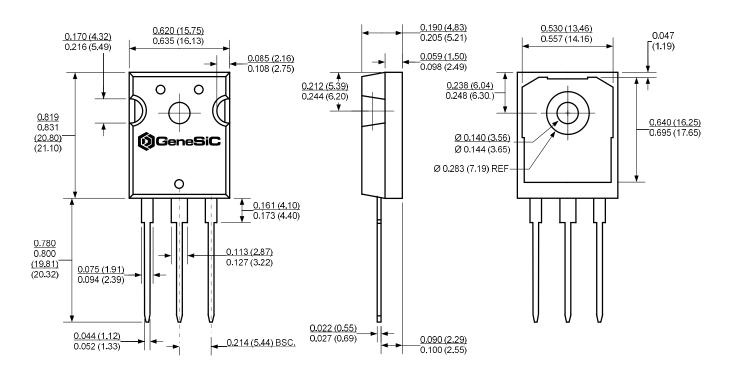
a = 5.94e-05, b = 1.05e-02, c = 2.07

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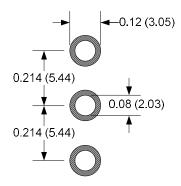
Package Dimensions:

TO-247-3L

PACKAGE OUTLINE



Recommended Solder Pad Layout



NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
- $2. \ \mathsf{DIMENSIONS} \ \mathsf{DO} \ \mathsf{NOT} \ \mathsf{INCLUDE} \ \mathsf{END} \ \mathsf{FLASH}, \ \mathsf{MOLD} \ \mathsf{FLASH}, \ \mathsf{MATERIAL} \ \mathsf{PROTRUSIONS}$



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RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented January 2, 2013. RoHS Declarations for this product can be obtained from your GeneSiC representative.

REACH Compliance

REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.

This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, or air traffic control systems.

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Related Links

- Soldering Document: http://www.genesicsemi.com/quality/quality-manual/
- Tin-whisker Report: http://www.genesicsemi.com/quality/compliance/
- Reliability Report: http://www.genesicsemi.com/quality/reliability/



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SPICE Model Parameters

This is a secure document. Please copy this code from the SPICE model PDF file on our website (http://www.genesicsemi.com/sic rectifiers diodes/merged pin schottky/GC2X8MPS12-247 SPICE.pdf) into LTSPICE (version 4) software for simulation of the GC2X8MPS12-247. All the simulations are per Leg.

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GeneSiC Semiconductor SiC MPS<sup>™</sup> Rectifier
    Revision: 1.1
    Date: February-2018
********************
        TO-247-3 package
******************
.SUBCKT GC2X8MPS12 A K Case
L anode
                    6.5n
      A
              AD
D1
        ΑD
                    GC08MPS12
              Case
L cathode K
              Case
                    6.5n
.ends
*****************
.SUBCKT GC2X8MPS12 ANODE KATHODE
D1 ANODE KATHODE GC08MPS12 SCHOTTKY
.MODEL GC08MPS12 SCHOTTKY D
+ IS
        6.57E-15
                     RS
                              0.0776
                              500
+ N
                     IKF
+ EG
        1.2
                     XTI
                              2
+ TRS1
        0.005434
                              2.717E-05
                     TRS2
+ CJO
        7.09E-10
                     VJ
                              0.879
        0.438
                     FC
                              0.5
+ M
+ TT
        1.00E-10
                     ΒV
                              1600
        0.8E-06
                              1200
+ IBV
                     VPK
                              SiC\ MPS^{TM}
+ IAVE
                     TYPE
+ MFG
        GeneSiC Semi
.ENDS
* End of GC2X8MPS12-247 SPICE Model
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* This model is provided "AS IS, WHERE IS, AND WITH NO WARRANTY OF ANY KIND
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^{*} EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO ANY IMPLIED

^{*} WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE."