1200 V SiC MPS™ Diode

Silicon Carbide Power **Schottky Diode**



V _{RRM}	=	1200 V
I _{F (Tc = 135°C)}	=	13 A
Q_{c}	=	31 nC

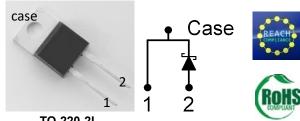
Features

- High Avalanche (UIS) Capability
- Enhanced Surge Current Capability
- 175 °C Maximum Operating Temperature
- Temperature Independent Switching Behavior
- Positive Temperature Coefficient Of V_F
- Extremely Fast Switching Speeds
- Superior Figure of Merit Q_C/I_F

Advantages

- Low Standby Power Losses
- Improved Circuit Efficiency (Lower Overall Cost)
- Low Switching Losses
- Ease of Paralleling Devices without Thermal Runaway
- Smaller Heat Sink Requirements
- Low Reverse Recovery Current
- Low Device Capacitance
- Low Reverse Leakage Current at Operating Temperature

Package



TO-220-2L

Applications

- Power Factor Correction (PFC)
- Switched-Mode Power Supply (SMPS)
- Solar Inverters
- Wind Turbine Inverters
- Motor Drives
- · Induction Heating
- Uninterruptible Power Supply (UPS)
- High Voltage Multipliers

Absolute Maximum Ratings

Parameter	Symbol	Conditions	Values	Unit	
Repetitive Peak Reverse Voltage	V_{RRM}		1200	V	
		T _C = 25 °C, D = 1	25		
Continuous Forward Current	I_{F}	$T_C = 135 ^{\circ}C, D = 1$	13	Α	
		$T_C = 167 ^{\circ}C, D = 1$	5		
Non-Repetitive Peak Forward Surge Current,	1	$T_C = 25 ^{\circ}\text{C}, t_P = 10 \text{ms}$	48	٨	
Half Sine Wave	I _{F,SM}	$T_C = 150 ^{\circ}C, t_P = 10 \text{ms}$	34	Α	
Repetitive Peak Forward Surge Current, Half	1	$T_C = 25 ^{\circ}\text{C}, t_P = 10 \text{ms}$	25	۸	
Sine Wave	I _{F,RM}	$T_C = 150 ^{\circ}\text{C}, t_P = 10 \text{ms}$	15	Α	
Non-Repetitive Peak Forward Surge Current	$I_{F,max}$	T_C = 25 °C, t_P = 10 μ s	450	Α	
I ² t Value	∫i² dt	T_{C} = 25 °C, t_{P} = 10 ms	12	A ² s	
Non-Repetitive Avalanche Energy	E _{AS}	$L = 12 \text{ mH}, I_{AV} = 5 \text{ A}, V_{DD} = 60 \text{ V}$	75	mJ	
Diode Ruggedness	dV/dt	V _R = 0 ~ 960 V	100	V/µs	
Power Dissipation	P _{tot}	T _C = 25 °C	211	W	
Operating and Storage Temperature	T_{j} , T_{stg}		-55 to 175	°C	

Electrical Characteristics

Doromotor	Cumphal	Conditions min.			Values		Unit
Parameter	Symbol			typ.	max.		
Diode Forward Voltage	V_{F}	I _F = 5 A, T _j = 25 °C		1.5	1.8	V	
	٧F	I _F = 5 A, T _j = 175 °C		2.3	2.7		
Reverse Current	ı	V _R = 1200 V, T _j = 25 °C		0.5	7	μA	
	I _R	$V_R = 1200 \text{ V}, T_j = 175 ^{\circ}\text{C}$		4	47		
Total Capacitive Charge			V _R = 400 V		21		nC
	Q _C	$I_F \le I_{F,MAX}$ - $dI_F/dt = 200 A/\mu s$	V _R = 800 V		31		IIC
Switching Time	+	$T_i = 175 ^{\circ}\text{C}$	V _R = 400 V		< 10		ns
	ts	.,	V _R = 800 V				
Total Capacitance	С	$V_R = 1 \text{ V, } f = 1 \text{ MHz, } T_j = 25 ^{\circ}\text{C}$		343		"r	
	C	$V_R = 800 \text{ V}, f = 1 \text{ MHz}$	z, T _i = 25 °C		23		pF

Thermal / Mechanical Characteristics

Thermal Resistance, Junction - Case	R_{thJC}	0.71	°C/W

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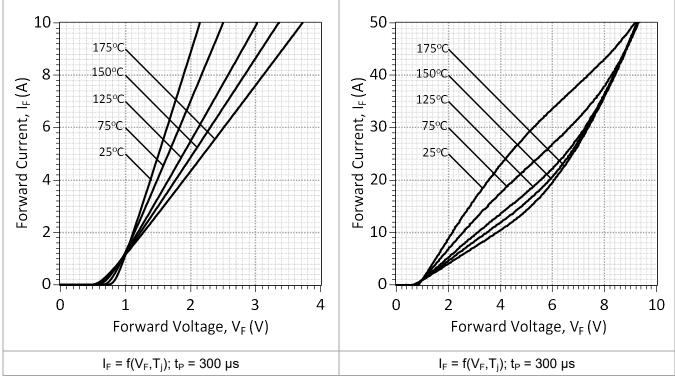


Figure 1: Typical Forward Characteristics

250 200 175°C Reverse Current, I_R (A) Power Dissipated (W) 10^{-6} 125°C 150 75°C 25°C 100 10^{-7} 50 10^{-8} 25 50 75 100 125 150 175 200 200 400 600 800 1000 1200 Reverse Voltage, V_R (V) Case Temperature, T_C (°C) $I_R = f(V_R, T_j)$ $P_{tot} = f(T_j)$

Figure 3: Typical Reverse Characteristics

Figure 4: Power Derating Curve

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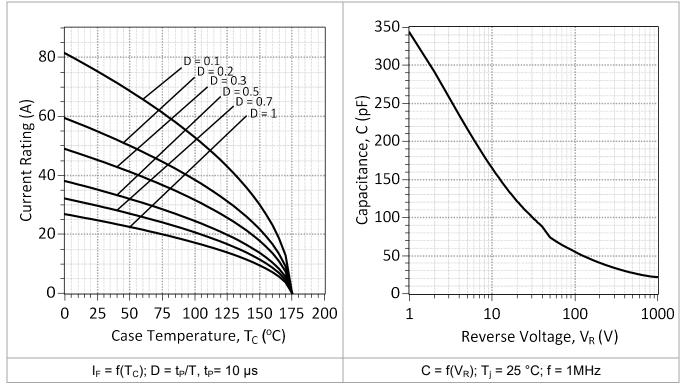


Figure 5: Current Derating Curves

40 35 Capacitive Charge, $\mathsf{Q}_{\scriptscriptstyle{\mathbb{C}}}$ (nC) 30 25 20 15 10 5 0 200 400 600 800 0 1000 Reverse Voltage, V_R (V) $Q_c = f(V_R); T_j = 25 \, ^{\circ}C; f = 1MHz$

Figure 7: Typical Capacitive Charge vs. Reverse Voltage Characteristics

Figure 6: Typical Junction Capacitance vs Reverse Voltage Characteristics

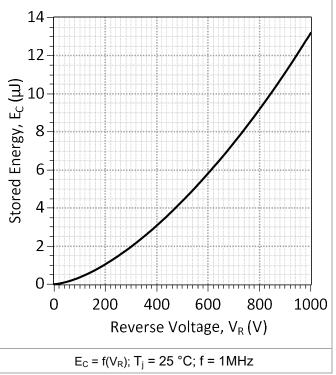


Figure 8: Typical Capacitive Energy vs. Reverse Voltage Characteristics



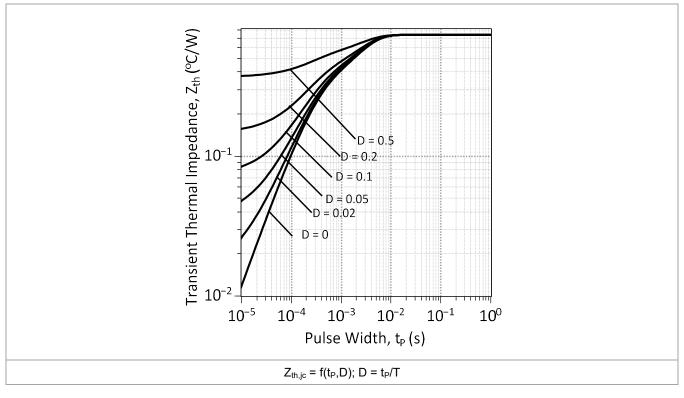


Figure 9: Transient Thermal Impedance

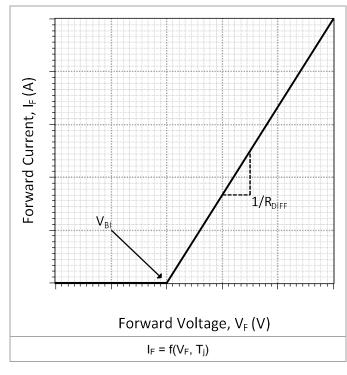


Figure 10: Forward Curve Model

$$I_F = (V_F - V_{BI})/R_{DIFF}$$

Built-In Voltage (V_{BI}):
$$V_{BI}(T_j) = m^*T_j + b,$$

$$m = -1.29e-03, b = 0.91$$

Differential Resistance (RDIFF):

$$R_{DIFF}(T_j) = a^*T_j^2 + b^*T_j + c(\Omega);$$

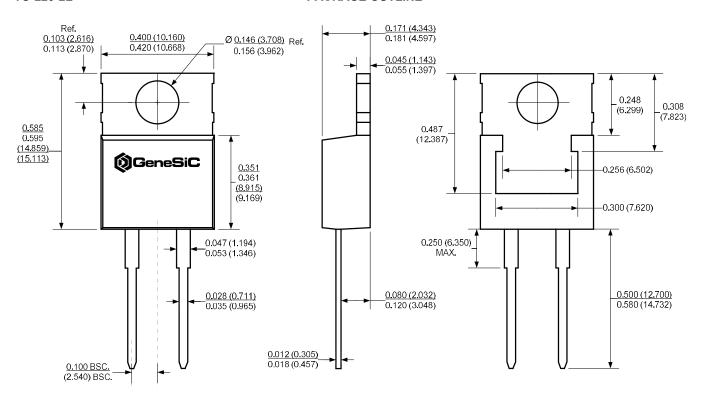
 $a = 6.97e-05, b = 1.00e-02, c = 2.28$

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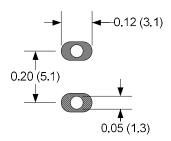
Package Dimensions:

TO-220-2L

PACKAGE OUTLINE



Recommended Solder Pad Layout



NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
- 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS



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RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented January 2, 2013. RoHS Declarations for this product can be obtained from your GeneSiC representative.

REACH Compliance

REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.

This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, or air traffic control systems.

GeneSiC disclaims all and any warranty and liability arising out of use or application of any product. No license, express or implied to any intellectual property rights is granted by this document.

Related Links

- Soldering Document: http://www.genesicsemi.com/quality/quality-manual/
- Tin-whisker Report: http://www.genesicsemi.com/quality/compliance/
- Reliability Report: http://www.genesicsemi.com/quality/reliability/



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SPICE Model Parameters

This is a secure document. Please copy this code from the SPICE model PDF file on our website (http://www.genesicsemi.com/sic rectifiers diodes/merged pin schottky/GC05MPS12-220 SPICE.pdf) into LTSPICE (version 4) software for simulation of the GC05MPS12-220.

```
GeneSiC Semiconductor SiC MPS™ Rectifier
    Revision: 1.1
    Date: February-2018
******************
        TO-220-2 package
****************
.SUBCKT GC05MPS12 A K Case
       Α
             ΑD
                    6.5n
D1
        ΑD
             Case
                   GC05MPS12
                    6.5n
L cathode K
             Case
.ends
******************
.SUBCKT GC05MPS12 ANODE KATHODE
D1 ANODE KATHODE GC05MPS12 SCHOTTKY
.MODEL GC05MPS12 SCHOTTKY D
        4.55E-15
+ IS
                             0.1242
                    RS
+ N
        1
                     IKF
                             500
+ EG
        1.2
                    XTI
+ TRS1
        0.005434
                             2.717E-05
                    TRS2
+ CJO
        4.78E-10
                    VJ
                             0.879
        0.438
+ M
                    FC
                             0.5
+ TT
        1.00E-10
                    BV
                             1600
+ IBV
        0.5E-06
                    VPK
                             1200
                             SiC MPS<sup>TM</sup>
+ TAVE
        5
                     TYPE
+ MFG
        GeneSiC Semi
.ENDS
* End of GC05MPS12-220 SPICE Model
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^{*} This model is provided "AS IS, WHERE IS, AND WITH NO WARRANTY OF ANY KIND

^{*} EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO ANY IMPLIED

^{*} WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE."