

N-channel SiC power MOSFET

V_{DSS}	400V
$R_{DS(on)}(Typ.)$	120m $Ω$
I _D	20A
P_D	132W

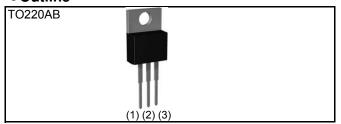
Features

- 1) Low on-resistance
- 2) Fast switching speed
- 3) Fast reverse recovery
- 4) Easy to parallel
- 5) Simple to drive
- 6) Pb-free lead plating; RoHS compliant

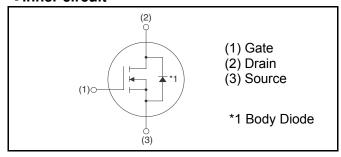
Application

Audio

Outline



•Inner circuit



Packaging specifications

	Packing	Tube
Re	Reel size (mm)	-
Type	Tape width (mm)	-
Туре	Basic ordering unit (pcs)	50
	Packing code	С
	Marking	SCTMU001F

● Absolute maximum ratings (T_a = 25°C)

Parameter		Symbol	Value	Unit
Drain - Source voltage		V_{DSS}	400	V
Continuous drain current $T_c = 25^{\circ}C$		l _D *1	20	А
Pulsed drain current		l _{D,pulse} *2	60	А
Gate - Source voltage		V_{GSS}	-6 to 22	V
Power dissipation (T _c = 25°C)		P_{D}	132	W
Junction temperature		T_j	150	°C
Range of storage temperature		T_{stg}	-55 to +150	°C

●Thermal resistance

Parameter	Symbol	Values			Unit
- Farameter	Symbol	Min.	Тур.	Max.	Offic
Thermal resistance, junction - case	R_{thJC}	-	0.72	0.95	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C

• Electrical characteristics ($T_a = 25^{\circ}C$)

Parameter	Symbol Conditions -		Values			Unit
r ai ai i i e te i	Syllibol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V$, $I_D = 1mA$	400	ı	ı	V
		$V_{DS} = 400V, V_{GS} = 0V$				
Zero gate voltage drain current	I_{DSS}	T _j = 25°C	-	0.1	1	μΑ
		T _j = 150°C	-	0.5	-	
Gate - Source leakage current	I _{GSS+}	$V_{GS} = +22V, V_{DS} = 0V$	-	ı	100	nA
Gate - Source leakage current	I _{GSS-}	$V_{GS} = -6V, V_{DS} = 0V$	-	-	-100	nA
Gate threshold voltage	V _{GS (th)}	$V_{DS} = V_{GS}, I_{D} = 3.3 \text{mA}$	1.6	-	4.0	V
		$V_{GS} = 18V, I_D = 10A$				
Static drain - source on - state resistance	R _{DS(on)} *3	T _j = 25°C	-	120	156	mΩ
		T _j = 100°C	-	137	-	
Gate input resistance	R_{G}	f = 1MHz, open drain	-	14	-	Ω

^{*1} Limited only by maximum temperature allowed.

^{*2} PW \leq 10 μ s, Duty cycle \leq 1%

^{*3} Pulsed

•Electrical characteristics ($T_a = 25^{\circ}C$)

Parameter	Symbol	Symbol Conditions		Values		
Parameter	Syllibol	Conditions	Min.	Тур.	Max.	Unit
Transconductance	g _{fs} *3	$V_{DS} = 10V, I_{D} = 10A$	-	2.7	-	S
Input capacitance	C _{iss}	V _{GS} = 0V	-	1218	-	
Output capacitance	C _{oss}	V _{DS} = 200V	-	102	-	pF
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	14	ı	
Turn - on delay time	t _{d(on)} *3	$V_{DD} = 300V, I_{D} = 5A$	-	22	ı	
Rise time	t _r *3	V _{GS} = 18V/0V	-	23	ı	ne
Turn - off delay time	t _{d(off)} *3	$R_L = 60\Omega$	_	67	-	ns
Fall time	t _f *3	$R_G = 0\Omega$	-	30	-	

•Gate Charge characteristics ($T_a = 25^{\circ}C$)

Parameter	Symbol	Conditions	Values			Linit
	Syllibol		Min.	Тур.	Max.	Unit
Total gate charge	Q_g^{*3}	V _{DD} = 200V	-	59	-	-
Gate - Source charge	Q _{gs} *3	I _D = 5A	-	13	-	nC
Gate - Drain charge	Q _{gd} *3	V _{GS} = 18V	-	18	-	

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	mbol Conditions -	Values			Unit
	Syllibol		Min.	Тур.	Max.	Offic
Inverse diode continuous, forward current	I _S *1	T _c = 25°C	-	-	20	А
Inverse diode direct current, pulsed	I _{SM} *2		-	-	60	А
Forward voltage	V_{SD}^{*3}	$V_{GS} = 0V, I_{S} = 10A$	-	4.3	-	V
Reverse recovery time	t _{rr} *3	1 - 404 \/ - 400\/	-	29	-	ns
Reverse recovery charge	Q _{rr} *3	I _F = 10A, V _R = 400V di/dt = 165A/μs	-	53	-	nC
Peak reverse recovery current	I _{rrm} *3		-	3.1	-	Α

Fig.1 Power Dissipation Derating Curve

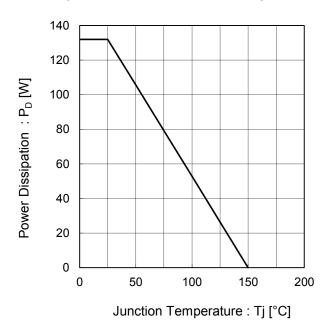
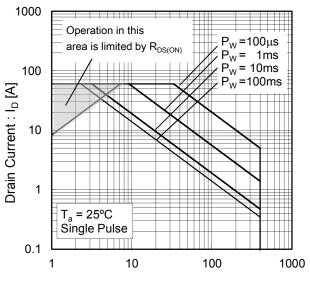


Fig.2 Maximum Safe Operating Area



Drain - Source Voltage : V_{DS} [V]

Fig.3 Typical Transient Thermal Resistance vs. Pulse Width

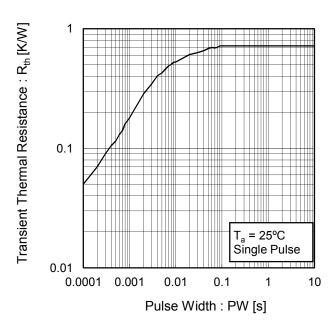


Fig.4 Typical Output Characteristics

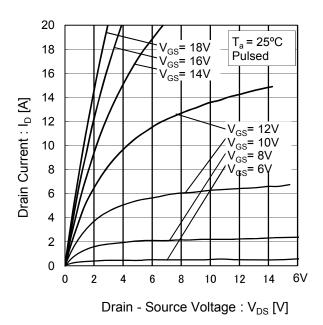


Fig.5 Tj = 150° C Typical Output Characteristics

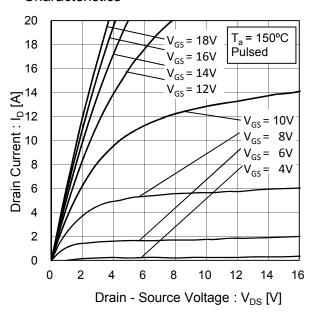
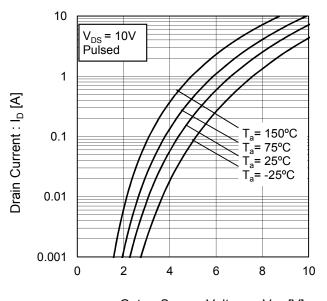
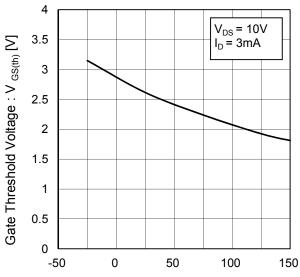


Fig.6 Typical Transfer Characteristics



Gate - Source Voltage : V_{GS} [V]

Fig.7 Gate Threshold Voltage vs. Junction Temperature



Junction Temperature : T_i [°C]

Resistance vs. Gate - Source Voltage Static Drain - Source On-State Resistance 0.5 $T_a = 25^{\circ}C$ Pulsed 0.4 $:R_{\text{DS(on)}}\left[\Omega \right]$ 0.3 0.2 $I_{D} = 10A$ 0.1 0 22 6 8 10 12 14 16 18 20 Gate - Source Voltage : V_{GS} [V]

Fig.8 Static Drain - Source On - State

Fig.9 Static Drain - Source On - State Resistance vs. Junction Temperature

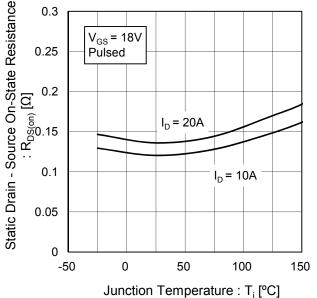


Fig.10 Static Drain - Source On - State Resistance vs. Drain Current

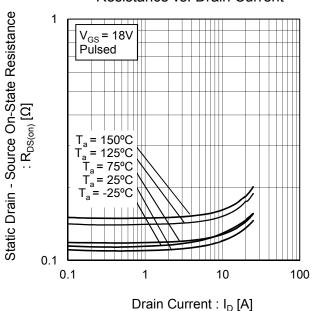


Fig.11 Transconductance vs. Drain Curren

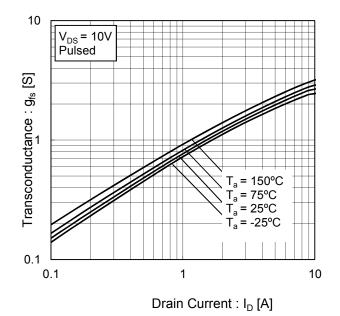


Fig.12 Typical Capacitance vs. Drain - Source Voltage

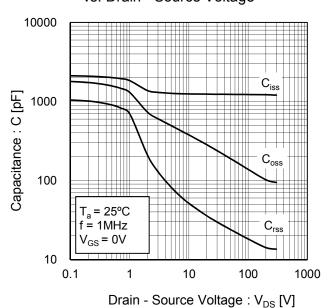


Fig.13 Dynamic Input Characteristics

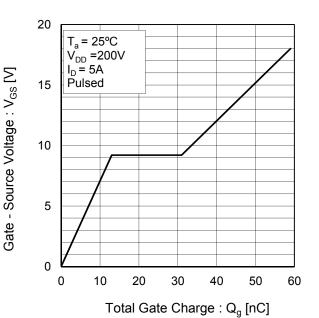
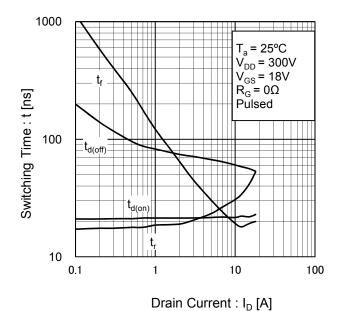


Fig.14 Switching Characteristics



Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

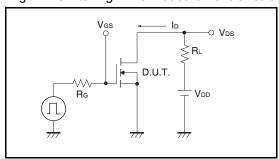


Fig.2-1 Gate Charge Measurement Circuit

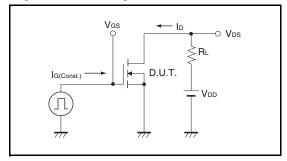


Fig.1-2 Switching Waveforms

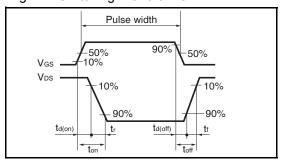
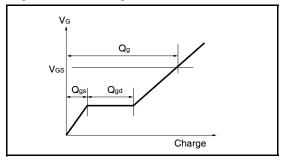
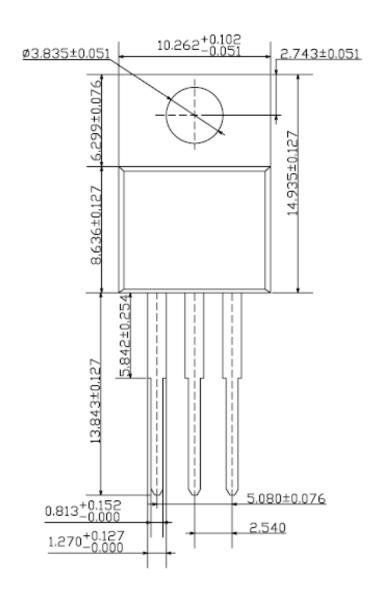


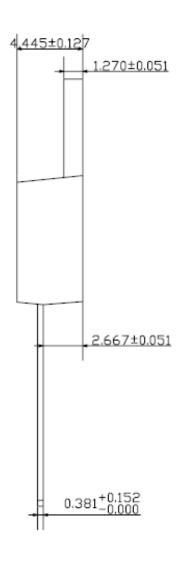
Fig.2-2 Gate Charge Waveform



● **Dimensions** (Unit: mm)

TO-220AB







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