



DIM800ACM45-TS001

IGBT Chopper Module

DS6186-2 June 2016 (LN33535)

FEATURES

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- High Current Density Enhanced DMOS SPT
- Isolated AISiC Base With AIN Substrates
- Lead Free Construction

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Drives
- Choppers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM800ACM45-TS001 is a 4500V, soft punch through n-channel enhancement mode, insulated gate bipolar transistor (IGBT) chopper module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

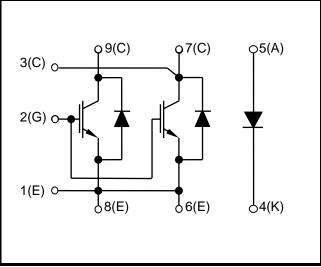
DIM800ACM45-TS001

Note: When ordering, please use the complete part number

KEY PARAMETERS

V _{CES}		4500V
V _{CE(sat)} *	ʻ (typ)	2.7V
l _c	(max)	800A
I _{С(РК)}	(max)	1600A

* Measured at the auxiliary terminals







Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures

ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V _{CES}	Collector-emitter voltage	$V_{GE} = 0V$	4500	V
V_{GES}	Gate-emitter voltage		±20	V
Ι _C	Continuous collector current	T _{case} = 90°C	800	А
I _{C(PK)}	Peak collector current	1ms, T _{case} = 115°C	1600	А
P _{max}	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_j = 125^{\circ}C$	8.3	kW
l ² t	Diode I ² t value	$V_R = 0, t_p = 10ms, T_j = 125^{\circ}C$	205	kA ² s
V _{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	10.2	kV
Q _{PD}	Partial discharge – per module	IEC1287, $V_1 = 6900V$, $V_2 = 5100V$, 50Hz RMS	10	рС

THERMAL AND MECHANICAL RATINGS

Internal insulation material:	AIN
Baseplate material:	AISiC
Creepage distance:	56mm
Clearance:	26mm
CTI (Comparative Tracking Index):	>600

Symbol	Parameter	Test Conditions	Min	Тур.	Мах	Units
$R_{th(j-c)}$	Thermal resistance – transistor	Continuous dissipation - junction to case	-	-	12	°C/kW
Р	Thermal resistance – diode (IGBT arm)	Continuous dissipation -	-	-	24	°C/kW
R _{th(j-c)}	Thermal resistance – diode (Diode arm)	junction to case	-	-	24	°C/kW
R _{th(c-h)}	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	6	°C/kW
Tj	Junction temperature	Transistor	-	-	125	°C
		Diode	-	-	125	°C
T _{stg}	Storage temperature range	-	-40	-	125	°C
	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm

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ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Тур	Мах	Units
	0	$V_{GE} = 0V, V_{CE} = V_{CES}$			1	mA
I _{CES}	Collector cut-off current	$V_{GE} = 0V, V_{CE} = V_{CES}, T_{case} = 125^{\circ}C$			40	mA
I _{GES}	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			1	μA
V _{GE(TH)}	Gate threshold voltage	$I_{C} = 80 \text{mA}, V_{GE} = V_{CE}$		5.8		V
N	Collector-emitter	V _{GE} = 15V, I _C = 800A		2.7		V
V _{CE(sat)}	saturation voltage	V _{GE} = 15V, I _C = 800A, T _j = 125°C		3.5		V
I _F	Diode forward current	DC		800		А
I _{FM}	Diode maximum forward current	t _p = 1ms		1600		А
		I _F = 800A		2.8		V
V _F	Diode forward voltage	I _F = 800A, T _j = 125°C		3.2		V
C _{ies}	Input capacitance	V_{CE} = 25V, V_{GE} = 0V, f = 1MHz		100		nF
Qg	Gate charge	±15V		15		μC
C _{res}	Reverse transfer capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz		TBC		nF
L _M	Module inductance			15		nH
R _{INT}	Internal transistor resistance			TBC		μΩ
SC _{Data}	Short circuit current, I _{SC}	$\begin{split} T_{j} &= 125^{\circ}C, \ V_{CC} &= 3400V \\ t_{p} &\leq 10 \mu s, \ V_{GE} &\leq 15V \\ V_{CE \ (max)} &= V_{CES} - L^{*} \ x \ dI/dt \\ IEC \ 60747-9 \end{split}$		2400		A

Note:

L is the circuit inductance + L_M

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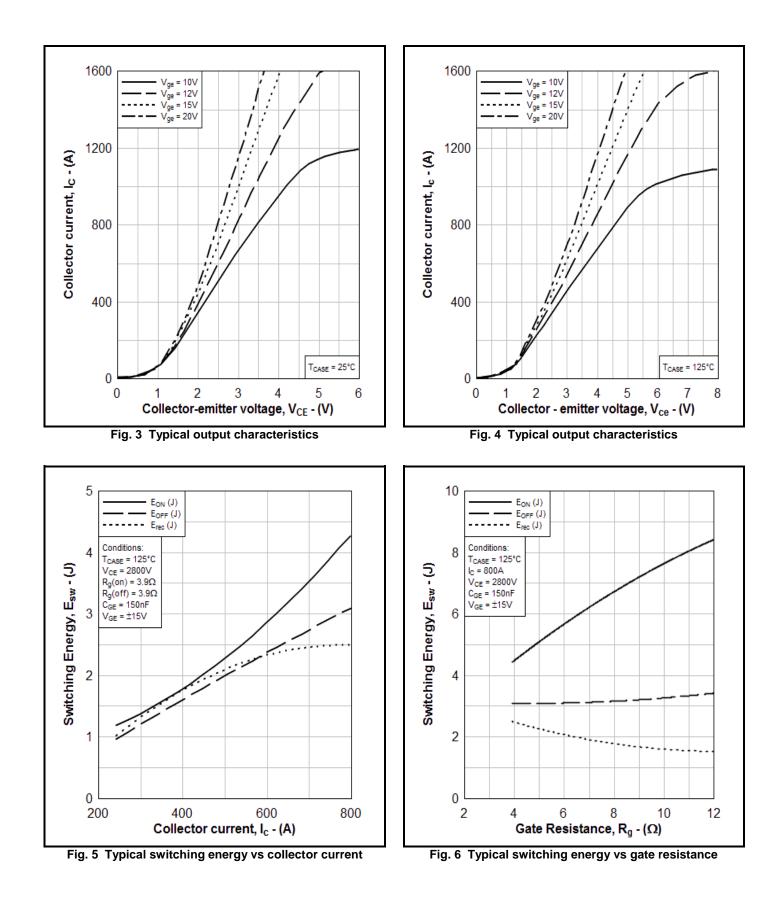
ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time			3000		ns
t _f	Fall time	I _C = 800A V _{GE} = ±15V		600		ns
E _{OFF}	Turn-off energy loss	V _{CE} = 2800V		3000		mJ
t _{d(on)}	Turn-on delay time	$\begin{array}{l} R_{G(ON)} = 3.9\Omega \\ R_{G(OFF)} = 3.9\Omega \end{array}$		900		ns
tr	Rise time	$C_{ge} = 150 nF$		350		ns
E _{ON}	Turn-on energy loss	L _s ~ 165nH		3200		mJ
Q _{rr}	Diode reverse recovery charge	I _F = 800A		880		μC
I _{rr}	Diode reverse recovery current	$V_{CE} = 2800V$		680		А
E _{rec}	Diode reverse recovery energy	dI _F /dt = 1900A/µs		1480		mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time			3100		ns
t _f	Fall time	I _C = 800A V _{GE} = ±15V		560		ns
E _{OFF}	Turn-off energy loss	V _{CE} = 2800V		3100		mJ
t _{d(on)}	Turn-on delay time	$\begin{array}{l} R_{G(ON)} = 3.9\Omega \\ R_{G(OFF)} = 3.9\Omega \end{array}$		900		ns
t _r	Rise time	$C_{ge} = 150 nF$		360		ns
E _{ON}	Turn-on energy loss	L _S ~ 165nH		4300		mJ
Q _{rr}	Diode reverse recovery charge	I _F = 800A		1450		μC
I _{rr}	Diode reverse recovery current	$V_{CE} = 2800V$		750		А
E _{rec}	Diode reverse recovery energy	dI _F /dt = 2000A/µs		2500		mJ



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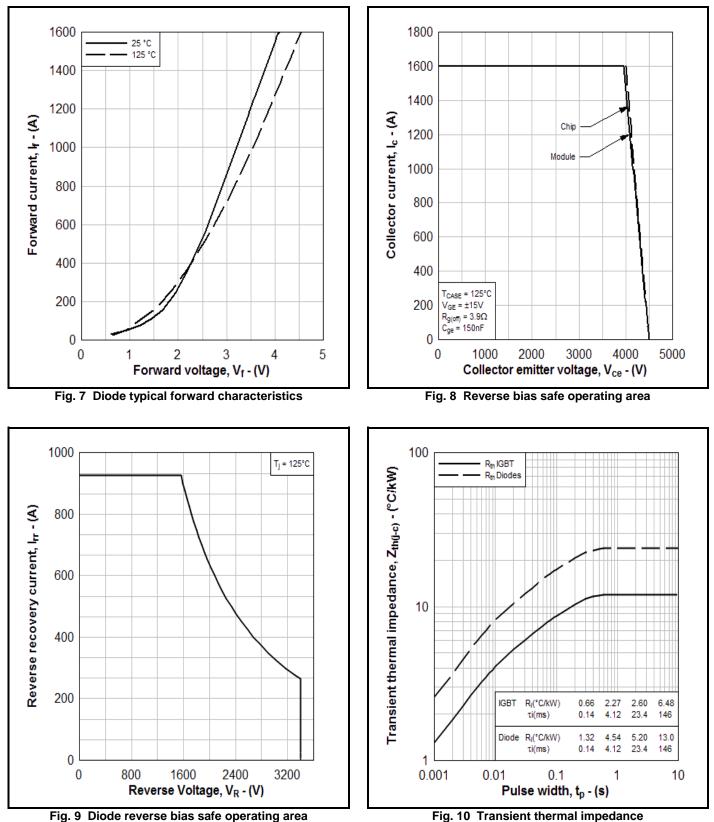


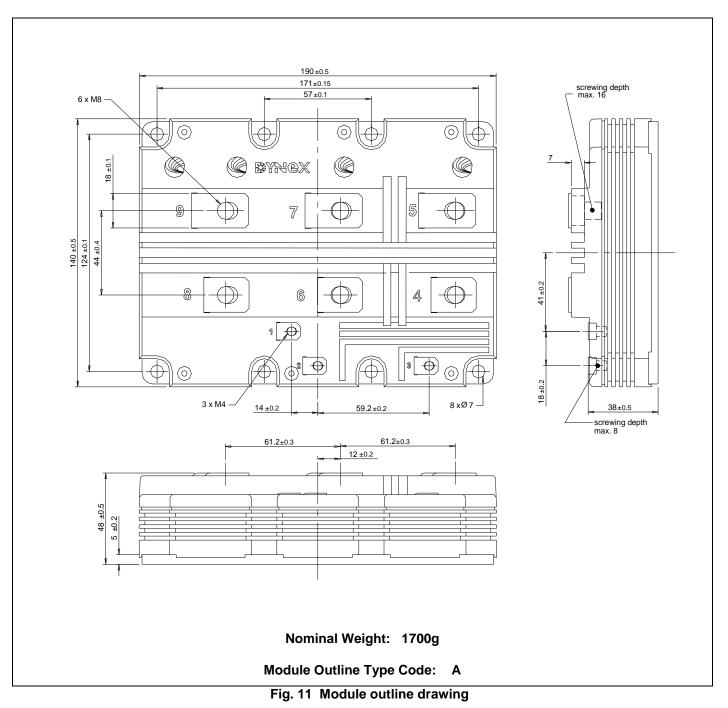
Fig. 10 Transient thermal impedance

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PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise. **DO NOT SCALE.**





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