## FEATURES

## DESCRIPTION

The EV20051DQ-00A evaluation board demonstrates the performance of MP20051, a low noise, low dropout and high PSRR linear regulator. It operates from a 2.5 V to 5.5 V input voltage and the output voltage can be set externally which ranges from 0.8 V to 5 V .

The EV20051DQ-00A can supply up to 1A of load current, and features current limiting, over temperature protection.

An internal PMOS pass element is used to allow a low $110 \mu \mathrm{~A}$ ground current, marking the MP20051 suitable for battery-power devices.

## ELECTRICAL SPECIFICATIONS

| Parameter | Symbol | Value | Units |
| :--- | :---: | :---: | :---: |
| Input Voltage | $\mathrm{V}_{\text {IN }}$ | $2.5-5.5$ | V |
| Output Voltage | $\mathrm{V}_{\text {OUT }}$ | 1.1 | V |
| Load Current | I OUT | 1 | A |

- Up to 1A Output Current
- Low 140 mV Dropout at 1 A
- Adjustable Output from 0.8 V to 5 V
- 63dB PSRR at 1 kHz
- $13 \mu \mathrm{VRMS}$ Low Noise Output
- Very Fast Transient Responses
- Current Limit and Thermal Protection


## APPLICATIONS

- Notebook Computers
- Cordless Telephones
- Cellular Phones
- Modems
- Hand-Held Instruments
- PDA and Palmtop Computers

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## EV20051DQ-00A EVALUATION BOARD


(L x W x H) 2.5" x 2.5" x 0.4"
$(6.35 \mathrm{~cm} \times 6.35 \mathrm{~cm} \times 1.1 \mathrm{~cm})$

| Board Number | MPS IC Number |
| :---: | :---: |
| EV20051DQ-00A | MP20051DQ |

## EVALUATION BOARD SCHEMATIC



## EV20051DQ-00A BILL OF MATERIALS

| Qty | Ref | Value | Description | Package | Manufacturer | Part Number |
| :---: | :---: | :---: | :--- | :---: | :---: | :--- |
| 1 | C1 | $2.2 \mu \mathrm{~F}$ | Ceramic <br> Capacitor,X5R,10V | 0603 | TDK | C1608X5R1A225K |
| 1 | C2 | $4.7 \mu \mathrm{~F}$ | Ceramic Capacitor, <br> X5R,10V | 0603 | TDK | C1608X5R1A475K |
| 0 | CIN,COUT, C3 | NS |  |  |  |  |
| 1 | R1 | $3.75 \mathrm{k} \Omega$ | Film Res, $1 \%$ | 0603 | Any |  |
| 1 | R2 | $10 \mathrm{k} \Omega$ | Film Res, $1 \%$ | 0603 | Any |  |
| 1 | R3 | $100 \mathrm{k} \Omega$ | Film Res, $5 \%$ | 0603 | Any |  |
| 1 | U1 |  | LDO Regulator | QFN8 <br> $(3 m m * 3 m m)$ | MPS | MP20051DQ |

## PRINTED CIRCUIT BOARD LAYOUT



Figure 1—Top Layer


Figure 2—Bottom Layer


Figure 3-Top Silk Layer

## QUICK START GUIDE

The output voltage of this board is set externally which ranges from 0.8 V to 5 V by operating from +2.5 V to +5.5 V input as the figure 4 . The default output voltage of this board is set to 1.1 V .
The board layout accommodates most commonly used resistors and capacitors.

1. Attach the positive and negative ends of the load to the VOUT and GND pins respectively.
2. Attach the Input Voltage ( $2.5 \mathrm{~V} \leq \mathrm{V}_{\mathbb{I N}} \leq 5.5 \mathrm{~V}$ ) and Input Ground to the VIN and GND pins respectively.
3. To enable the MP20051, apply a voltage, $1.5 \mathrm{~V} \leq \mathrm{V}_{E N} \leq 5.5 \mathrm{~V}$, to the EN pin. To disable the MP20051, apply a voltage, $\mathrm{V}_{\mathrm{EN}}<0.4 \mathrm{~V}$, to the EN pin. The EN pin can be connected to $\mathrm{V}_{\mathrm{IN}}$ with a $100 \mathrm{k} \Omega$ resistor for automatic startup.
4. The Output Voltage $V_{\text {OUt }}$ can be changed by varying R1. Calculate the new value by formula:

$$
R 1=R 2\left(\frac{V_{\text {OUT }}}{V_{\text {FB }}}-1\right)
$$

Where $\mathrm{V}_{\mathrm{FB}}=0.8 \mathrm{~V}$ and $\mathrm{R} 2=10 \mathrm{k} \Omega$.

## Example:

For $\mathrm{V}_{\text {out }}=1.1 \mathrm{~V}$ :

$$
\mathrm{R} 1=10 \mathrm{k} \Omega\left(\frac{1.1}{0.8}-1\right)=3.75 \mathrm{k} \Omega
$$

Therefore, use a $3.75 \mathrm{k} \Omega$ standard $1 \%$ value.


Figure 4

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