

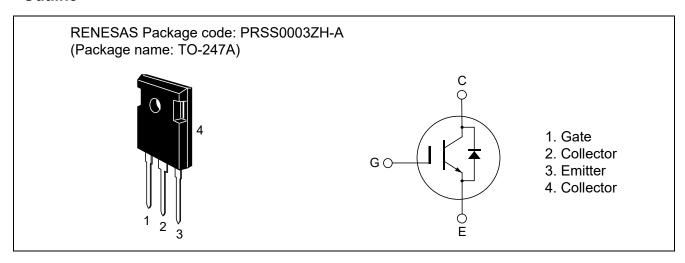
RBN75H65T1FPQ-A0

650V - 75A - IGBT Application: Uninterruptible Power Supply R07DS1383EJ0004 Rev.0.04 Dec 28, 2016

Features

- Low collector to emitter saturation voltage
 V_{CE(sat)} = 1.5 V typ. (at I_C = 75 A, V_{GE} = 15 V, Ta = 25°C)
- Built in fast recovery diode in one package
- Trench gate and thin wafer technology (G8H series)
- High speed switching
- Not guarantee short circuit withstand time

Outline



Absolute Maximum Ratings

 $(Tc = 25^{\circ}C)$

Item		Symbol	Ratings	Unit
Collector to emitter voltage		V _{CES} / V _R	650	V
Gate to emitter voltage		V _{GES}	±30	V
Collector current	Tc = 25 °C	Ic	150	Α
	Tc = 100 °C	Ic	75	Α
Collector peak current		I _C (peak) Note1	(300)	Α
Collector to emitter diode	Tc = 25 °C	I _{DF}	100	Α
Forward current	Tc = 100 °C	I _{DF}	50	Α
Collector to emitter diode forward peak current		I _{DF} (peak) Note1	(300)	Α
Collector dissipation		P _C Note 2	(348)	W
Junction to case thermal impedance (IGBT)		θј-с	(0.43)	°C/W
Junction to case thermal resistance (Diode)		θj-cd	(0.65)	°C/W
Junction temperature		Tj Note2	175	°C
Storage temperature		Tstg	-55 to +150	°C

Note: Continuous heavy condition (e.g. high temperature/voltage/current or high variation of temperature) may affect a reliability even if it are within the absolute maximum ratings. Please consider derating condition for appropriate reliability in reference Renesas Semiconductor Reliability Handbook (Recommendation for Handling and Usage of Semiconductor Devices) and individual reliability data.

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Electrical Characteristics

 $(Ta = 25^{\circ}C)$

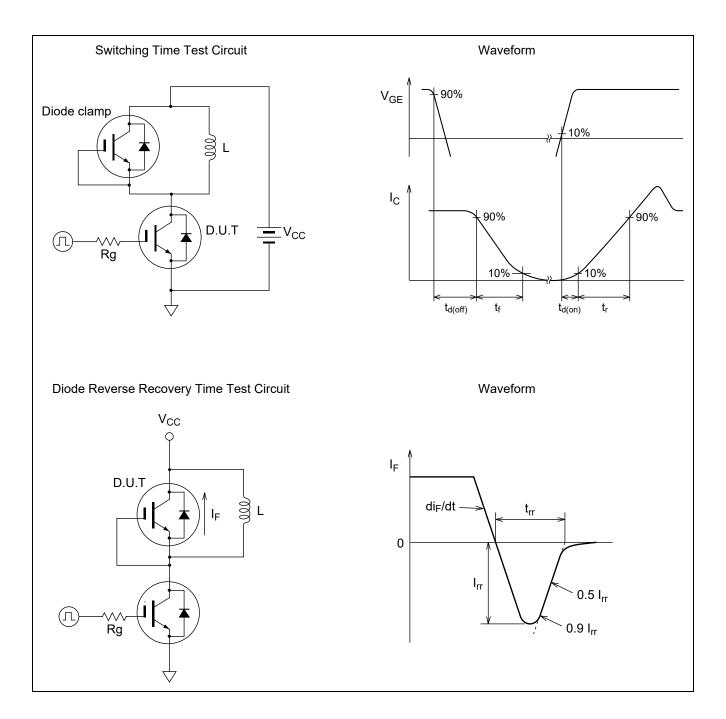
Item	Symbol	Min	Тур	Max	Unit	Test Conditions	
Zero gate voltage collector current	Ices / IR	_	_	(200)	μА	V _{CE} = 650 V, V _{GE} = 0	
/ Diode reverse current							
Gate to emitter leak current	I _{GES}		_	(±1)	μΑ	$V_{GE} = \pm 30 \text{ V}, V_{CE} = 0$	
Gate to emitter cutoff voltage	$V_{\text{GE(off)}}$	(4.1)	—	(5.9)	V	$V_{CE} = 10V, I_{C} = 1.5 \text{ mA}$	
Collector to emitter saturation	V _{CE(sat)}	_	(1.5)	(2.0)	V	$I_C = 75 \text{ A}, V_{GE} = 15 \text{V}^{\text{Note3}}$	
voltage							
Input capacitance	Cies	_	(1700)	_	pF	V _{CE} = 25 V	
Output capacitance	Coes	_	(225)	_	pF	V _{GE} = 0	
Reverse transfer capacitance	Cres	_	(16)	_	pF	f = 1 MHz	
Total gate charge	Qg	_	(64)	_	nC	VGE = 15 V	
Gate to emitter charge	Qge	_	(13)	_	nC	VCE = 400 V	
Gate to collector charge	Qgc	_	(28)	_	nC	IC = 75 A	
Turn-on delay time	t _{d(on)}	_	(29)	_	ns	$V_{CC} = 400 \text{ V}$ $V_{GE} = +15 \text{ V/-5V}$ $I_{C} = 75A$ $Rg = 16 \Omega$ $T_{C} = 25^{\circ}C$ Inductive load Note4	
Rise time	tr	_	(27)	_	ns		
Turn-off delay time	$t_{d(off)}$	_	(113)	_	ns		
Fall time	t _f	_	(37)	_	ns		
Turn-on loss energy	Eon	_	(1.6)	_	mJ		
Turn-off loss energy	E _{off}	_	(1.0)	_	mJ		
Total switching energy	E _{total}	_	(2.6)	_	mJ		
Turn-on delay time	t _{d(on)}	_	(27)	_	ns	V _{CC} = 400 V	
Rise time	tr	_	(24)	_	ns	V _{GE} = +15 V/-5V	
Turn-off delay time	$t_{d(off)}$	_	(137)	_	ns	$I_C = 75A$ $Rg = 16 \Omega$	
Fall time	t _f		(55)	_	ns		
Turn-on loss energy	Eon	_	(2.3)	_	mJ	T _C = 150°C Inductive load ^{Note4}	
Turn-off loss energy	Eoff		(1.5)	_	mJ		
Total switching energy	E _{total}		(3.8)	_	mJ		

FRD forward voltage	V_{F}	_	(1.7)	(2.2)	V	I _F = 50 A ^{Note3}
FRD reverse recovery time	t _{rr}	_	(72)	_	ns	I _F = 50 A, di _F /dt = 300 A/μs
FRD reverse recovery charge	Qrr	_	(0.3)	_	μC	
FRD peak reverse recovery current	Irr	_	(8)	_	Α	

Notes:

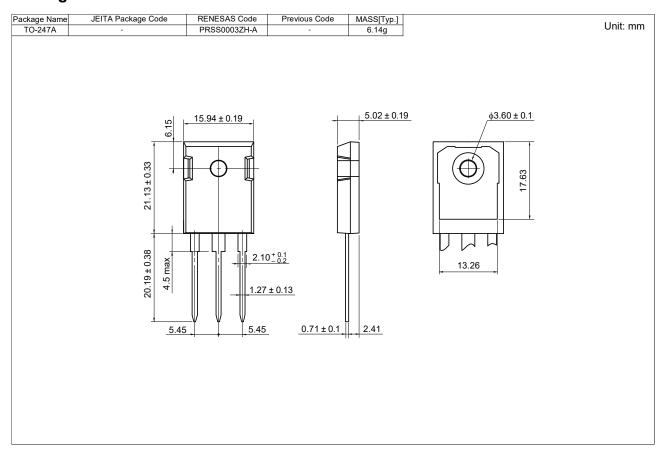
- 1. $PW \le 10 \mu s$, duty cycle $\le 1\%$
- 2. Please use this device in the thermal conditions which the junction temperature does not exceed 175°C. Renesas IGBT Application Note is disclosed about reliability test and application condition up to 175°C.
- 3. Pulse test
- 4. Switching time test circuit and waveform are shown below.

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Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RBN75H65T1FPQ-A0#CB0	240 pcs	Box (Tube)

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(Rev.3.0-1 November 2016)



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