

Super Low Distortion Multilayer Ceramic Capacitors (CFCAP(TM))

LMK212SD104KG-T



■ Features

- Item Summary  
10V, 0.1  $\mu$ F,  $\pm$ 10%, 0805
- Lifecycle Stage  
Mass Production
- Standard packaging quantity (minimum)  
Taping Embossed 3000pcs

■ Products characteristics table

CaseSize (EIA/JIS)	0805/2012
Rated Voltage (max)	10V
Capacitance	0.1 $\mu$ F
Capacitance Tolerance	$\pm$ 10%
Resistance	500 M $\Omega$ · $\mu$ F min.
Dielectric Loss Tangent	0.1%MAX
Temperature Range (EIA/JIS)	-55 to +125 $^{\circ}$ C/-55 to +125 $^{\circ}$ C
RoHS Compliance	Yes
Halogen Free	Yes
Soldering Method	Reflow

■ External Dimensions

L	2.0mm $\pm$ 0.10
W	1.25mm $\pm$ 0.10
T	1.25mm $\pm$ 0.10
e	0.5mm $\pm$ 0.25

2015.01.05

The data is reference only. Electrical characteristics vary depending on environment or measurement condition.  
 TAIYO YUDEN reserves the right to make change to the Date at any time without notice.  
 Before making final selection, please check product specification.

# Multi Layer Ceramic Capacitor

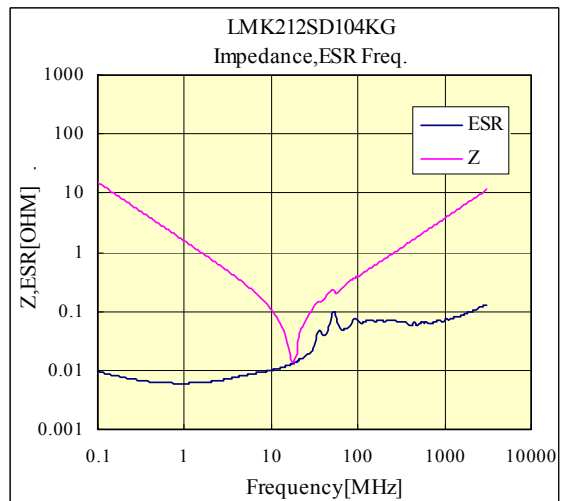
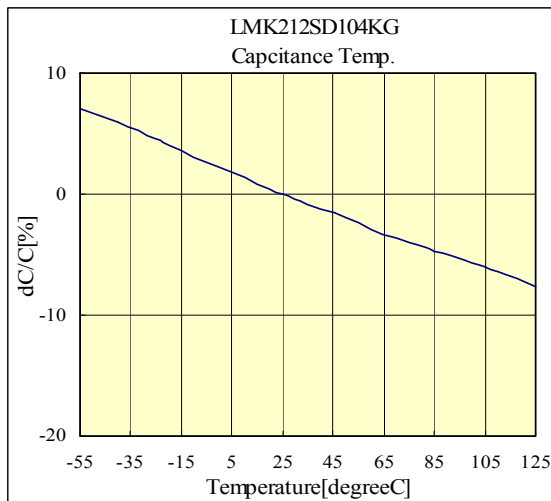
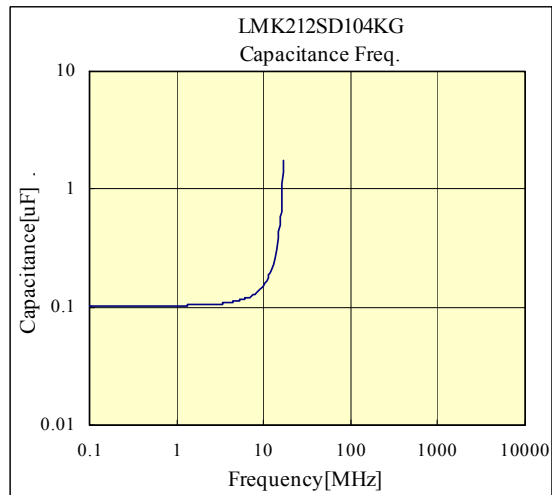
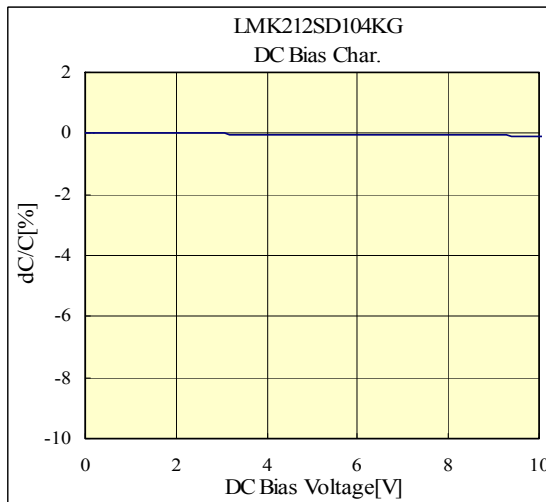
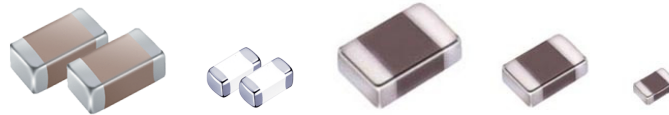
# TAIYO YUDEN

-Electrical Characteristics Data- Jun-2011

RoHS compliance product

## ○ LMK212SD104KG

10V 2012 0.1 $\mu$ F +/-10% t=1.35mmMAX  
10V 0805 0.1 $\mu$ F +/-10% t=0.053inchMAX



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