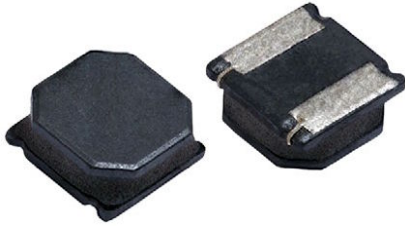


Low Profile, High Current Inductors



FEATURES

- Shielded construction
- Frequency range up to 5.0 MHz
- Handles high transient current spikes without saturation
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE

APPLICATIONS

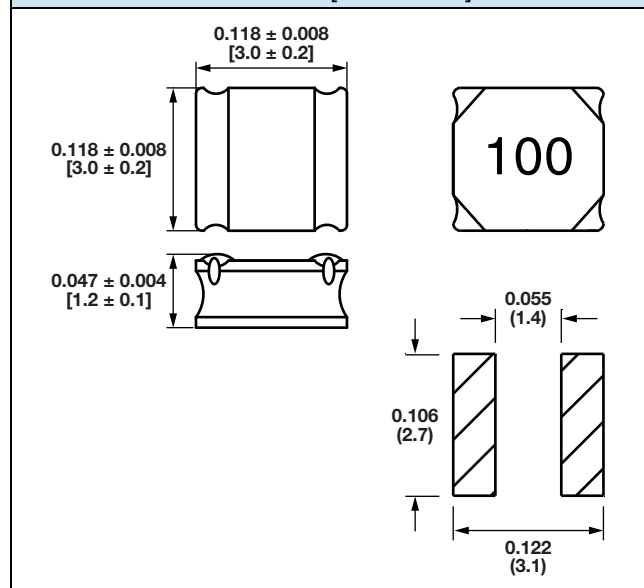
- PDA/notebook/desktop/server applications
- High current POL converters
- Low profile, high current power supplies
- Battery powered devices
- DC/DC converters in distributed power systems
- DC/DC converter for field programmable gate array (FPGA)

STANDARD ELECTRICAL SPECIFICATIONS						
L ₀ INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A (μH)	DCR 25 °C (mΩ)		HEAT RATING CURRENT DC I _{DC} (A) ⁽³⁾		SATURATION CURRENT DC I _{SAT} (A) ⁽⁴⁾	
	TYP.	MAX.	TYP.	MAX.	TYP.	MAX.
0.47	35	42	2.80	2.52	3.70	3.35
1.0	52	63	2.50	2.25	3.35	3.00
1.5	60	72	2.20	1.98	3.10	2.60
2.2	84	101	2.25	2.00	2.90	2.40
3.3	134	161	1.71	1.53	1.92	1.72
4.7	184	221	1.43	1.08	1.71	1.53
6.8	256	307	1.25	1.13	1.49	1.24
10.0	397	496	1.00	0.90	1.26	1.05
22.0	854	1025	0.60	0.54	0.86	0.72

Notes

- (1) All test data is referenced to 25 °C ambient
- (2) Operating temperature range -55 °C to +125 °C
- (3) DC current (A) that will cause an approximate ΔT of 40 °C
- (4) DC current (A) that will cause L₀ to drop approximately 30 %
- (5) The part temperature (ambient + temp. rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

DIMENSIONS in inches [millimeters]



DESCRIPTION

IFSC-1111AB-01	4.7 μH	± 20 %	ER	e3
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC® LEAD (Pb)-FREE STANDARD

GLOBAL PART NUMBER

I	F	S	C	1	1	1	1	A	B	E	R	4	R	7	M	0	1
PRODUCT FAMILY				SIZE					PACKAGE CODE		INDUCTANCE VALUE			TOL.	SERIES		



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