SA602A Double-balanced mixer and oscillator Rev. 3 — 27 May 2014

Product data sheet

1. General description

The SA602A is a low-power VHF monolithic double-balanced mixer with input amplifier, on-board oscillator, and voltage regulator. It is intended for high-performance, low-power communication systems. The guaranteed parameters of the SA602A make this device well-suited for cellular radio applications. The mixer is a 'Gilbert cell' multiplier configuration which typically provides 18 dB of gain at 45 MHz. The oscillator operates to 200 MHz. It can be configured as a crystal oscillator, a tuned tank oscillator, or a buffer for an external LO. For higher frequencies, the LO input may be externally driven. The noise figure at 45 MHz is typically less than 5 dB. The gain, intercept performance, low-power and noise characteristics make the SA602A a superior choice for high-performance battery operated equipment. It is available in an 8-lead SO (surface-mount miniature package).

2. Features and benefits

- Low current consumption: 2.4 mA typical
- Excellent noise figure: <4.7 dB typical at 45 MHz
- High operating frequency
- Excellent gain, intercept and sensitivity
- Low external parts count; suitable for crystal/ceramic filters
- SA602A meets cellular radio specifications

3. Applications

- Cellular radio mixer/oscillator
- Portable radio
- VHF transceivers
- RF data links
- HF/VHF frequency conversion
- Instrumentation frequency conversion
- Broadband LANs



Version SOT96-1

4. Ordering information

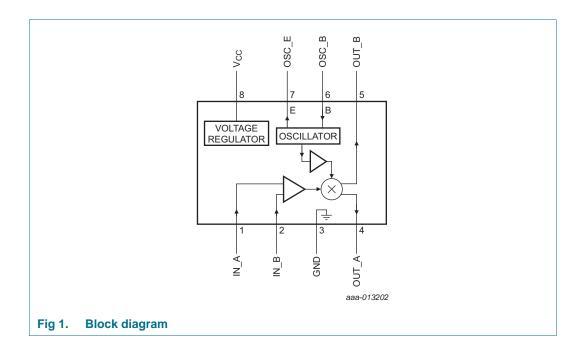
	Table 1. Ordering information					
	Type number	Package				
		Name	Description			
SA602AD/01 SO8 plastic small outline package; 8 leads; body width 3.9 mm		plastic small outline package; 8 leads; body width 3.9 mm				

4.1 Ordering options

Table 2. Ordering options

Type number	Orderable part number	Package	J	Minimum order quantity	Temperature
SA602AD/01	SA602AD/01,112	SO8	Standard marking * IC's tube - DSC bulk pack	2000	$T_{amb} = -40 \ ^{\circ}C \ to \ +85 \ ^{\circ}C$
	SA602AD/01,118	SO8	Reel 13" Q1/T1 Standard mark SMD	2500	$T_{amb} = -40 \ ^{\circ}C \ to \ +85 \ ^{\circ}C$

5. Block diagram

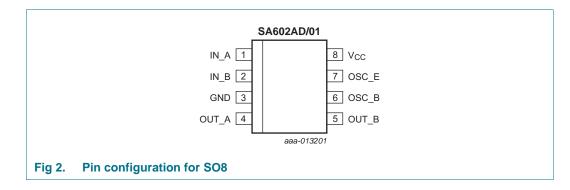


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6. Pinning information

6.1 Pinning

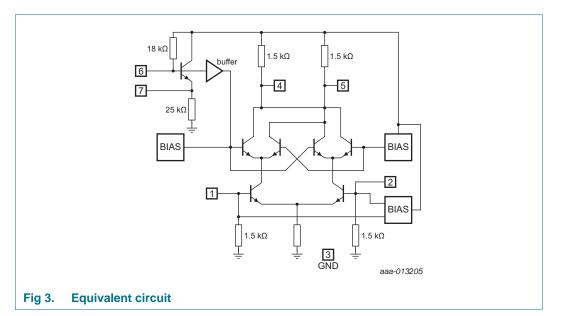


6.2 Pin description

Table 3.	Table 3. Pin description			
Symbol	Pin	Description		
IN_A	1	RF input A		
IN_B	2	RF input B		
GND	3	ground		
OUT_A	4	mixer output A		
OUT_B	5	mixer output B		
OSC_B	6	oscillator input (base)		
OSC_E	7	oscillator output (emitter)		
V _{CC}	8	supply voltage		

7. Functional description

The SA602A is a Gilbert cell, an oscillator/buffer, and a temperature-compensated bias network as shown in <u>Figure 3</u>. The Gilbert cell is a differential amplifier (IN_A and IN_B pins) that drives a balanced switching cell. The differential input stage provides gain and determines the noise figure and signal handling performance of the system.

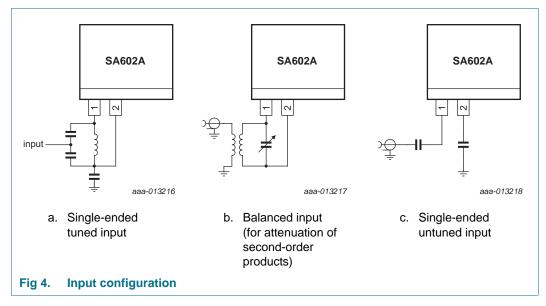


The SA602A is designed for optimum low-power performance. When used with the SA604A as a 45 MHz cellular radio second IF and demodulator, the SA602A is capable of receiving –119 dBm signals with a 12 dB S/N ratio. Third-order intercept is typically –13 dBm (that is approximately +5 dBm output intercept because of the RF gain). The system designer must be cognizant of this large signal limitation. When designing LANs or other closed systems where transmission levels are high, and small-signal or signal-to-noise issues are not critical, the input to the SA602A should be appropriately scaled.

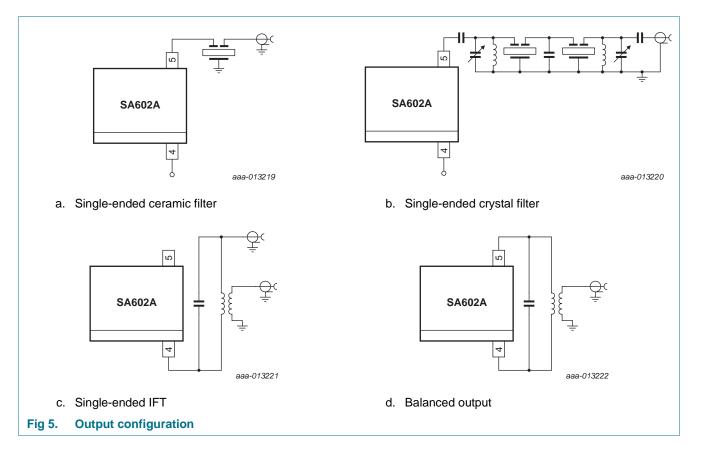
Besides excellent low-power performance well into VHF, the SA602A is designed to be flexible. The input, RF mixer output and oscillator ports support various configurations provided the designer understands certain constraints, which are explained here.

The RF inputs (IN_A and IN_B pins) are biased internally. They are symmetrical. The equivalent AC input impedance is approximately 1.5 k $\Omega \parallel$ 3 pF through 50 MHz. IN_A and IN_B pins can be used interchangeably, but they should not be DC biased externally. Figure 4 shows three typical input configurations.

Double-balanced mixer and oscillator



The mixer outputs (OUT_A and OUT_B pins) are also internally biased. Each output is connected to the internal positive supply by a 1.5 k Ω resistor. This permits direct output termination yet allows for balanced output as well. Figure 5 shows three single-ended output configurations and a balanced output.



The oscillator can sustain oscillation beyond 200 MHz in crystal or tuned tank configurations. The upper limit of operation is determined by tank 'Q' and required drive levels. The higher the 'Q' of the tank or the smaller the required drive, the higher the permissible oscillation frequency. If the required LO is beyond oscillation limits, or the system calls for an external LO, the external signal can be injected at OSC_B (pin 6) through a DC blocking capacitor. External LO should be at least 200 mV (peak-to-peak).

Figure 6 shows several proven oscillator circuits. Figure 6 a is appropriate for cellular radio. As shown, an overtone mode of operation is utilized. Capacitor C3 and inductor L1 suppress oscillation at the crystal fundamental frequency. In the fundamental mode, the suppression network is omitted.

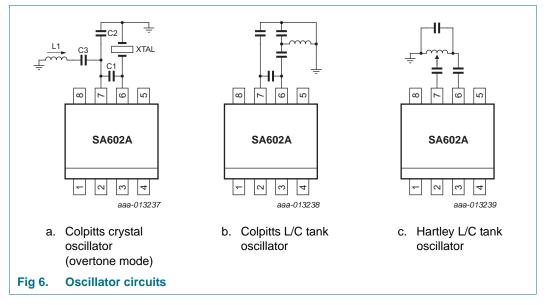
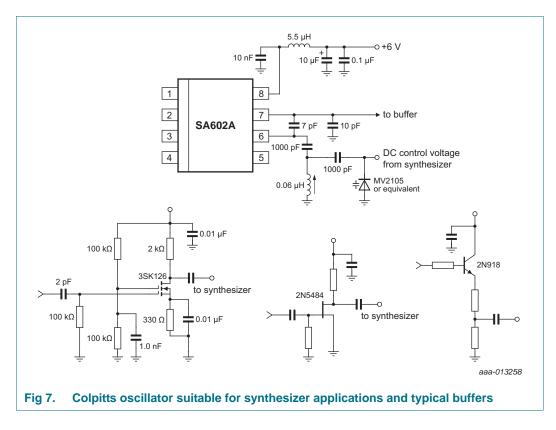


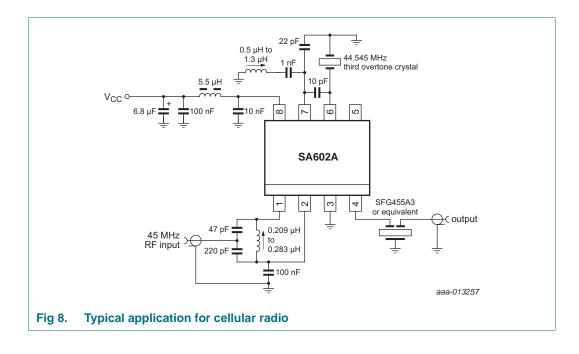
Figure 7 shows a Colpitts varactor tuned tank oscillator suitable for synthesizer-controlled applications. It is important to buffer the output of this circuit to assure that switching spikes from the first counter or prescaler do not end up in the oscillator spectrum. The dual-gate MOSFET provides optimum isolation with low current. The FET offers good isolation, simplicity, and low current, while the bipolar transistors provide the simple solution for non-critical applications. The resistive divider in the emitter-follower circuit should be chosen to provide the minimum input signal that assures correct system operation.

When operated above 100 MHz, the oscillator may not start if the Q of the tank is too low. A 22 k Ω resistor from OSC_E (pin 7) to ground increases the DC bias current of the oscillator transistor. This improves the AC operating characteristic of the transistor and should help the oscillator to start. A 22 k Ω resistor does not upset the other DC biasing internal to the device, but smaller resistance values should be avoided.

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8. Application design-in information



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Limiting values 9.

Table 4. Limiting values In accordance with the Absolute Maximum Rating System (IEC 60134).					
Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-	9	V
T _{stg}	storage temperature		-65	+150	°C
T _{amb}	ambient temperature	operating	-40	+85	°C

10. Thermal characteristics

Table 5.	Thermal characteristics				
Symbol	Parameter	Conditions	Min	Max	Unit
Z _{th(j-a)}	transient thermal impedance from junction to ambient		-	90	°C/W

11. Static characteristics

Table 6. **Static characteristics**

 V_{CC} = +6 V; T_{amb} = 25 °C; unless specified otherwise.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CC}	supply voltage		4.5	-	8.0	V
I _{CC}	supply current		-	2.4	2.8	mA

12. Dynamic characteristics

Dynamic characteristics Table 7.

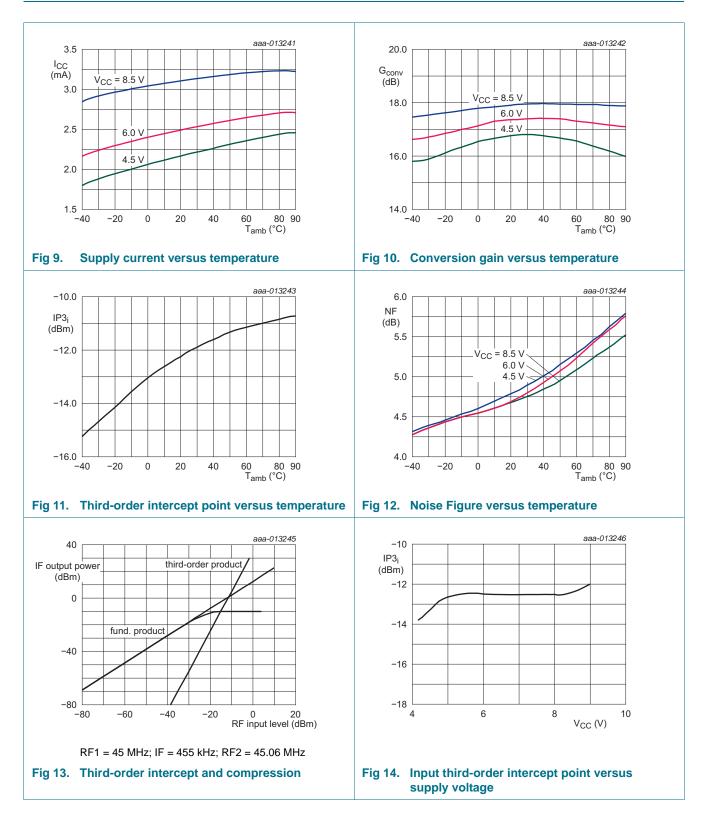
 $T_{amb} = 25 \text{ °C}; V_{CC} = +6 \text{ V}; unless specified otherwise.}$

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
f _i	input frequency		-	500	-	MHz
f _{osc}	oscillator frequency		-	200	-	MHz
NF	noise figure	at 45 MHz	-	5.0	5.5	dB
IP3 _i	input third-order intercept point	RF input = -45 dBm; RF1 = 45.0 MHz; RF2 = 45.06 MHz	-	-13	-15	dBm
G _{conv}	conversion gain	at 45 MHz	14	17	-	dB
R _{i(RF)}	RF input resistance		1.5	-	-	kΩ
C _{i(RF)}	RF input capacitance		-	3	3.5	pF
R _{o(mix)}	mixer output resistance	OUT_A, OUT_B pins	-	1.5	-	kΩ

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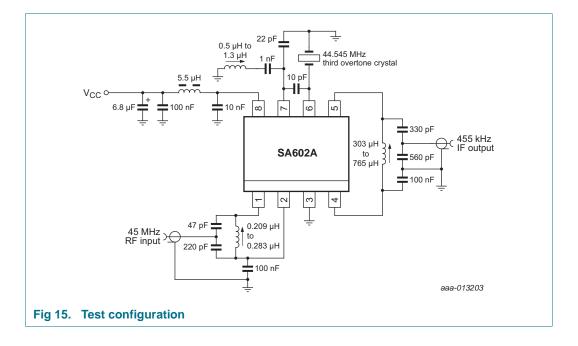
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13. Performance curves



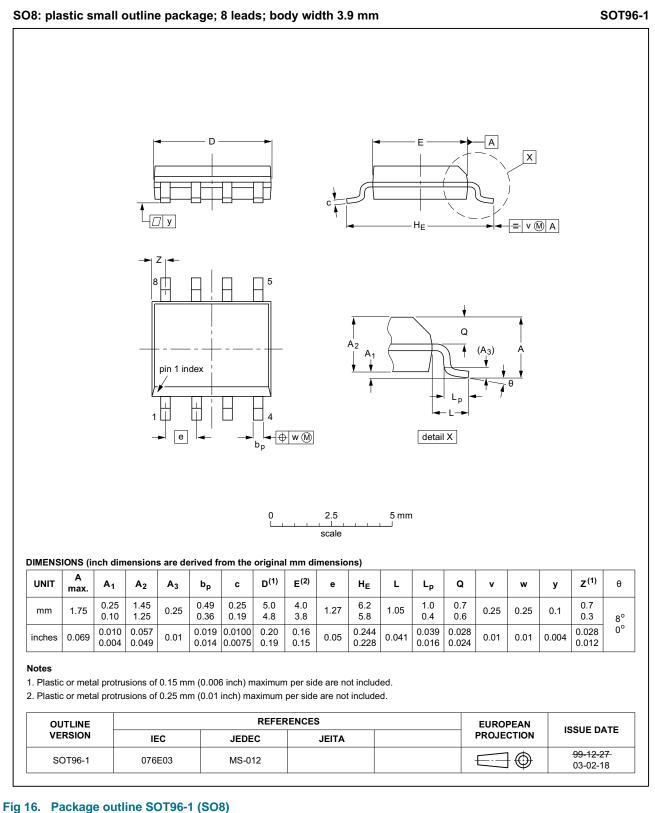
Product data sheet

14. Test information



Product data sheet

15. Package outline



16. Soldering of SMD packages

This text provides a very brief insight into a complex technology. A more in-depth account of soldering ICs can be found in Application Note *AN10365* "Surface mount reflow soldering description".

16.1 Introduction to soldering

Soldering is one of the most common methods through which packages are attached to Printed Circuit Boards (PCBs), to form electrical circuits. The soldered joint provides both the mechanical and the electrical connection. There is no single soldering method that is ideal for all IC packages. Wave soldering is often preferred when through-hole and Surface Mount Devices (SMDs) are mixed on one printed wiring board; however, it is not suitable for fine pitch SMDs. Reflow soldering is ideal for the small pitches and high densities that come with increased miniaturization.

16.2 Wave and reflow soldering

Wave soldering is a joining technology in which the joints are made by solder coming from a standing wave of liquid solder. The wave soldering process is suitable for the following:

- Through-hole components
- Leaded or leadless SMDs, which are glued to the surface of the printed circuit board

Not all SMDs can be wave soldered. Packages with solder balls, and some leadless packages which have solder lands underneath the body, cannot be wave soldered. Also, leaded SMDs with leads having a pitch smaller than ~0.6 mm cannot be wave soldered, due to an increased probability of bridging.

The reflow soldering process involves applying solder paste to a board, followed by component placement and exposure to a temperature profile. Leaded packages, packages with solder balls, and leadless packages are all reflow solderable.

Key characteristics in both wave and reflow soldering are:

- · Board specifications, including the board finish, solder masks and vias
- · Package footprints, including solder thieves and orientation
- · The moisture sensitivity level of the packages
- Package placement
- Inspection and repair
- Lead-free soldering versus SnPb soldering

16.3 Wave soldering

Key characteristics in wave soldering are:

- Process issues, such as application of adhesive and flux, clinching of leads, board transport, the solder wave parameters, and the time during which components are exposed to the wave
- Solder bath specifications, including temperature and impurities

16.4 Reflow soldering

Key characteristics in reflow soldering are:

- Lead-free versus SnPb soldering; note that a lead-free reflow process usually leads to higher minimum peak temperatures (see <u>Figure 17</u>) than a SnPb process, thus reducing the process window
- Solder paste printing issues including smearing, release, and adjusting the process window for a mix of large and small components on one board
- Reflow temperature profile; this profile includes preheat, reflow (in which the board is heated to the peak temperature) and cooling down. It is imperative that the peak temperature is high enough for the solder to make reliable solder joints (a solder paste characteristic). In addition, the peak temperature must be low enough that the packages and/or boards are not damaged. The peak temperature of the package depends on package thickness and volume and is classified in accordance with Table 8 and 9

Table 8. SnPb eutectic process (from J-STD-020D)

Package thickness (mm)	Package reflow temperature (°C	Package reflow temperature (°C)		
	Volume (mm ³)			
	< 350	≥ 350		
< 2.5	235	220		
≥ 2.5	220	220		

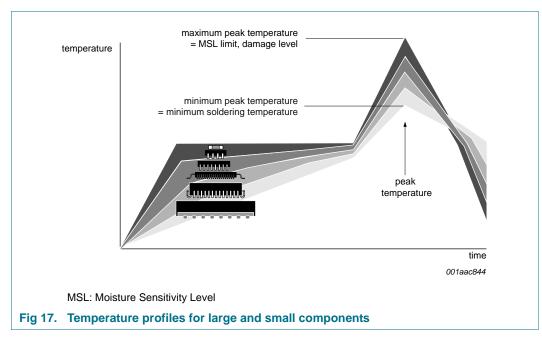
Table 9. Lead-free process (from J-STD-020D)

Package thickness (mm)	Package reflow temperature (°C)				
	Volume (mm ³)				
	< 350	350 to 2000	> 2000		
< 1.6	260	260	260		
1.6 to 2.5	260	250	245		
> 2.5	250	245	245		

Moisture sensitivity precautions, as indicated on the packing, must be respected at all times.

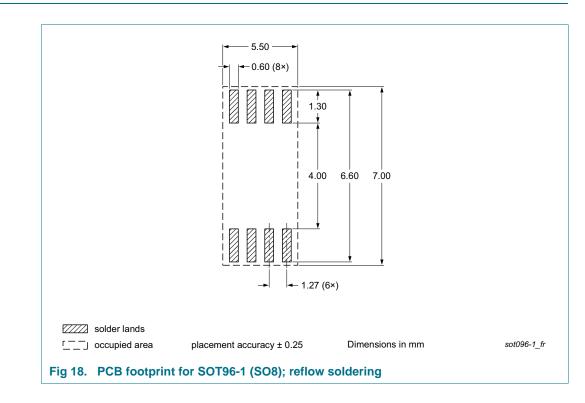
Studies have shown that small packages reach higher temperatures during reflow soldering, see Figure 17.

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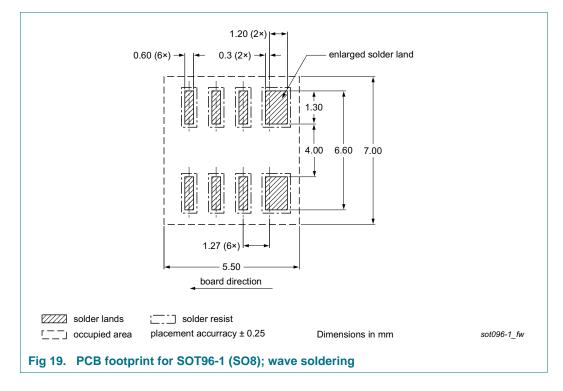


For further information on temperature profiles, refer to Application Note *AN10365* "Surface mount reflow soldering description".

17. Soldering: PCB footprints



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18. Abbreviations

Table 10. Abbreviations		
Acronym	Description	
FET	Field-Effect Transistor	
HF	High Frequency	
IF Intermediate Frequency		
LAN Local Area Network		
LO Local Oscillator		
MOSFET Metal-Oxide Semiconductor Field-Effect Transistor		
RF Radio Frequency		
VHF Very High Frequency		

19. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
SA602A v.3	20140527	Product data sheet	-	SA602A v.2		
Modifications:	Semiconductor Legal texts hav <u>Section 1 "Gen</u> <u>Table 1 "Orderin</u>	e been adapted to the new com eral description", last sentence: ng information": er SA602AN (DIP8 package, SO	pany name where ap deleted "8-lead dual i	propriate. n-line plastic package"		
	Added <u>Section</u>	er changed from "SA602AD" to " <u>4.1 "Ordering options"</u> <u>6.2 "Pin description"</u>	SA602AD/01"			
	 Figure 7 "Colpitts oscillator suitable for synthesizer applications and typical buffers": capacitor value corrected from "0.10 pF" to "10 nF" (above pin 8) Figure 8 "Typical application for cellular radio": component value corrected from "34.545 MHz third overtone crystal" to "44.545 MHz third overtone crystal" 					
	 <u>Table 5 "Thermal characteristics</u>": deleted characteristic values for "N package" (SA602AN) Old table "AC/DC electrical characteristics" split into <u>Table 6 "Static characteristics</u>" and <u>Table 7 "Dynamic characteristics</u>". <u>Table 7 "Dynamic characteristics</u>", Conditions for IP3_i, input third-order intercept point, corrected from "f₁" to "RF1" and from "f₂" to "RF2" 					
	 Figure 15 "Test configuration": component values corrected throughout this drawing Package outline SOT97-1 (DIP8) is deleted Added soldering information Added Section 17 "Soldering: PCB footprints" Added Section 18 "Abbreviations" 					
SA602A v.2	19971107	Product specification	853-1424 18662	NE/SA602A v.1		
NE/SA602A v.1	19900417	Product specification	853-1424 99374	-		

Table 11. Revision history

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