

HOME LAN NETWORKING BANDPASS & 10/100 BASE –T FILTER

P/N: HR003: APPLY TO AMD 79C978 Single-Chip

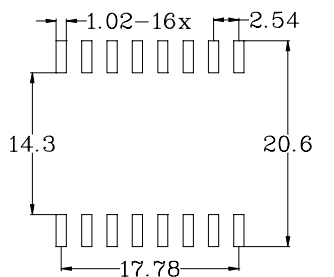
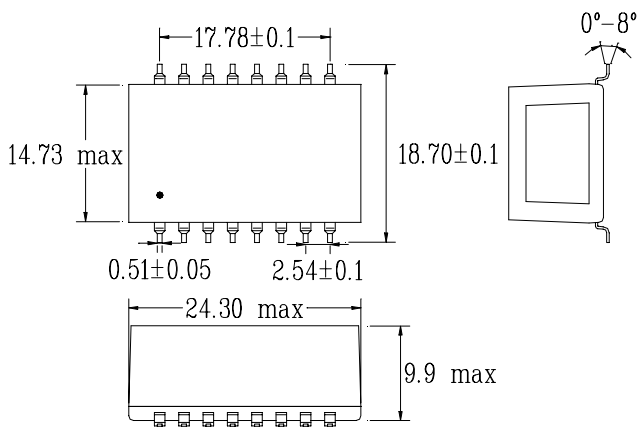
A.GENERAL SPECIFICATIONS:

- DESIGN FOR HOME LAN NETWORKING, PHONELINE & 10/100-TX ETHERNET WITH COMMON MODE CHOKE.
- SUPPORTS TUT SYSTEM HOME LAN TECHNOLOGY.
- 16 PIN SMD PACKAGE.

B.ELECTRICAL SPECIFICATIONS @ 25°C:

- ISOLATION HI-POT: 1500 VRMS (PRI. /SEC.)
- TURN RATIO (+/- 5%): TX=1CT: 1CT, RX=1:1CT
- INDUCTANCE (uH): TX& RX=550(MIN)
- LEAKAGE INDUCTANCE (.uH): 0.5 (MAX)
- INTERWINDING CAPACITANCE (PF): TX & RX=30(MAX)
- DC RESISTANCE (Ω): 1.0(MAX)
- TUT INSERTION@ 6MHz-9MHz (dB MAX):-4.0
- TUT ATTENUATION (dB): 1.0 MHz 27MHz
 -65 (MIN) -47 (MIN)
 -70 (NORMAL) -55 (NORMAL)
- TUT RETURN LOSS: 6.0-9.0 MHz (dB): -9.5 (MIN)

C. SCHEMATICS & DIMENSIONS & MARKING (unit mm):



suggest pad layout

