

700INV60-120-240-9G

DC/AC INVERTER



Bel Power Solutions' 700INV60-120-240-9G is a high-efficient DC/AC inverter that converts high-voltage DC power into split phase AC power 120/240 VAC required to drive AC accessory loads directly from the High Voltage DC Drive or Battery Bus.

Liquid cooled DC/AC inverter operates at input voltages from 400 to 850 VDC and power range up to 6000 W. The DC/AC inverter utilizes CAN communication to the vehicle controller which allows selection of operational modes and frequency selection.

The DC/AC inverter is built in a sealed aluminum enclosure, ready to be mounted to the vehicle chassis and is a commercial-off the-shelf solution for electric vehicle manufacturers and developers.

Key Features & Benefits

- 93 % typical efficiency
- Input voltage range: 400 – 850 VDC
- Power rating of 1 module 6 kW, possible parallel synchronized operation up to 6 modules with total power up to 32 or 36 kW
- Full galvanic insulation between input and output
- CAN bus serial interface
- Over temperature, output overvoltage and overcurrent protection
- Protection degree IP65 and IP67
- Liquid cooled
- Vibration immunity meets military level
- Three phase function : outputs of 3 modules can create 3 phase system 3 x 400 VAC

TECHNICAL DATA

Input Specifications

| PARAMETER | DESCRIPTION / CONDITION | MIN | NOM | MAX | UNIT |
|--|--|------|-----|-----|----------|
| Input Voltage | | 400 | 700 | 850 | VDC |
| Max. Input Current at nominal Power 6000 W | Vin = 400 Vmean | | 17 | 19 | ADC |
| Max. Input Current at peak Power 8000 W | Vin = 400 Vmean | | | 22 | ADC |
| Typical Efficiency | Maximum @ Vin = 700 VDC, Pout = 4 kW, T_coolant = 40°C | | 93 | | % |
| Internal Power Loss | @ Vin = 700 VDC, Pout = 6 kW, T_coolant = 40°C | | 430 | | W |
| | @ Vin = 700 VDC, Pout = 0 kW, T_coolant = 40°C | | 110 | | W |
| Input Line Interruption | Inverter shutdown | | | 400 | VDC |
| Input Capacitance | | | | 100 | μF |
| Insulation | Input-to-Chassis: Basic | 3000 | | | VDC |
| | Input-to-Output: Reinforced | 3000 | | | VDC |
| Insulation Resistance | Input to Chassis (Ground) | 10 | | | Mega Ohm |
| | Input to Output | 10 | | | Mega Ohm |
| Input Current Waveform | Remaining amplitude part of 50 / 60Hz component | | | 10 | % |

Output Specifications

| PARAMETER | DESCRIPTION / CONDITION | MIN | NOM | MAX | UNIT |
|-------------------------------|---|--------|---------|---------|------|
| Output Voltage | L1 to N, L2 to N | | 120 | | VAC |
| | L1 to L2 | | 240 | | VAC |
| Phase shift | L1 to L2 | | 180 | | Deg |
| Output Current | @ 6kW | | 25 | | A |
| Output Power | Continuous | | | 6 | kVA |
| | Output overload, (max. 10 sec.) Overload for motor load spin-up (max 3 sec) | 8 | | 8 | |
| Static Regulation | L1 = L2 = 120 VAC at load 0 to 25 A | -12 | | + 12 | VAC |
| In-factory Output Calibration | L1 to L2 = 240 VAC at load 25A, T_coolant = 30°C | 237.75 | | 237.85 | VAC |
| Frequency | | | 50 / 60 | | Hz |
| Periodic and Random Deviation | @ 120 VAC / Nominal load | | | | % |
| | - Differential Mode 20 MHz of Vo_max - Differential Mode 500 MHz | | 3 | 4 | Vp-p |
| Load Crest Factor | Vin = 400-850 Vmean, So1 = So2 = 3kVA, Ipk1 = Ipk2 = 75A, T_ambient = 85°C, T_coolant = 70°C | | | 3 | |
| Total Harmonic Distortion | Vin = 400-850 Vmean, So1 = So2 = (0.01kVA, 1kVA, 3kVA), cos(fi) = (0.5, 1), T_ambient = 25°C, T_coolant = 50°C 0.1 – 1 kVA linear load 1 – 3 kVA linear load | | | 4 10 | % |
| Turn-On Delay | after application of DC input or cycling enable signal | | | 5000 | ms |
| Load Power Factor | The output voltage of the inverter kept within the limits. | 0.3 | | 1 | |

Protection

| PARAMETER | DESCRIPTION / CONDITION | MIN | NOM | MAX | UNIT |
|---|---|------------|------------|---------------|------|
| Output Over-Current Protection shut-down time | Vin=400 - 850 Vmean, T_ambient=85°C, T_coolant=70°C, Io1=Io2=step from 25Arms to 27.5Arms Io1=Io2=step from 25Arms to 33Arms Io1=Io2=step from 25Arms to >33Arms | | | 65 12 3 | Sec |
| Short Circuit Survival shut-down time | Vin=700Vmean, T_ambient=85°C, T_coolant=70°C, Io1=Io2=from 25Arms/cos(F)=1 to short cut | 60 | | | ms |
| Output Over-Voltage Protection | Latch type | 200 | | 210 | Vpk |
| Output Under-Voltage Protection | UV duration 60ms | 95 | | 100 | Vrms |
| Over-Temperature Protection | Converter shutdown at T_coolant higher than | 70 | | | °C |
| Input Fuse Protection | DC Input: Single on positive pole | | 25 | | A |
| Input Low Line Protection | Turn ON threshold Turn OFF threshold | 390 380 | 395 385 | 400 390 | VDC |
| Input Overvoltage protection | Recovery: Vin re-cycling or ENABLE | 855 | | 865 | VDC |

Interface & Control Signals

| PARAMETER | DESCRIPTION / CONDITION |
|-----------------------------|---|
| CAN BUS SAE J1939 Interface | 250 kBit/s |
| ENABLE | Inverter turned ON signal is pulled-up 100 k Ohm to 5 V internally Inverter turned OFF pulled down to 0.6V or 100 Ohm to signal GND |
| FREQ_SELECT | Output frequency selected to 60 Hz signal is pulled up 100k Ohm to 5 V internally Output frequency selected to 50 Hz signal is pulled down to 0.6 V or 100 Ohm or less to signal GND |
| SYNC | Operation of multiple inverter modules in redundant parallel mode. SYNC signals of individual modules have to be connected together and twisted with signal ground |
| HVIL function | Internal short is provided on HVIL pins. |

Readback Accuracy on CAN

| PARAMETER | DESCRIPTION / CONDITION |
|----------------------------------|--|
| Output Current Readback Accuracy | Under all conditions for currents below 4 Arms read back is not working, i.e. read back value is 0 A For current higher than 4A this readback is valid: In case of crest factor 1 : ± 1.0 Arms In case of crest factor higher than 2.5 : ± 3.0 Arms |
| Input Voltage Readback Accuracy | ± 4.0 VDC |
| Output Voltage Readback Accuracy | ± 1.0 Vrms |

Safety, Regulatory and EMC Specifications

| PARAMETER | DESCRIPTION / CONDITION | CRITERION |
|--|--|--|
| Insulation | Basic Reinforced | Input-to-Protective Earth Input-to-Output |
| Radiated Emission | CISPR22, EN55022 | Class A |
| Conducted Emission | CISPR22, EN55022 | Class A |
| Electrostatic Discharge | IEC 61000-4-2 (10 V/m) | Performance Criterion B |
| Radiated Electromagnetic Field | IEC 61000-4-3, SAE J1113/21 | Performance Criterion B |
| Electrical Fast Transient (EFT) /Burst | IEC 61000-4-4, ISO 7637-2; Level 2 (+/- 2 kV, 2.5 kHz) | Performance Criterion B |
| RF Conducted Immunity | Level 3 (10V, 0.15...80 MHz, AM 80%, 1 KHz) | Performance Criterion A |
| RF Disturbances Immunity | SAE J1113-41 | Class A |

Environmental Specifications

| PARAMETER | DESCRIPTION / CONDITION | MIN | NOM | MAX | UNIT |
|-----------------------|---|-----|-----|-------|------|
| Altitude | Operating: 62 kPa absolute pressure | | | 3600 | m |
| | Non-Operating: 18.6 kPa absolute pressure | | | 12200 | |
| Operating Temperature | Liquid cooled: | | | | °C |
| | Tcoolant with no derating | -40 | | +70 | |
| | Tamb @ full load, with no power derating | -40 | | +85 | |
| Storage Temperature | | -40 | | +95 | °C |
| Humidity | SAE J1455 JAN 2011 | | | | |
| Shock | SAE J1455 | | | | |
| Vibration | SAE J1455, MIL-STD-202G | | | | |

Connectors on Inverter Module

| PARAMETER | DESCRIPTION / CONDITION | MANUFACTURER | MPN |
|------------------------------|---|-------------------------------|------------|
| Input Connector | High voltage connector with HVIL function | Tyco electronics HVA630 key A | 2141272-1 |
| Output Connector | High voltage unshielded connector | Delphi | 13879038 |
| CAN BUS and Signal Interface | Panel mounted | Molex MX150 | 47725-1310 |

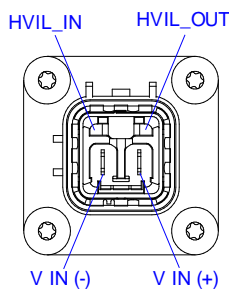
Connectors for Mating Wiring Harness

| PARAMETER | DESCRIPTION / CONDITION | MPN |
|------------------------------|---|--|
| DC Input Connector | Housing Tyco HVA630 (2pcs) | 9-2141256-1, 9-2141259-1 |
| | Shielding Tyco HVA630 (2pcs) | 9-2141262-2 |
| | Female terminal (4pcs) | 1-1241408-3, 963716-1 or 963716-2 |
| | Sealing (4pcs) | 9-2141265-4, 9-2141264-4, 9-2141263-4, 9-2141261-4 |
| AC Output Connector | Housing Delphi (1pc) | 13879046 |
| | Power terminal (4pcs), | 13783301 |
| | Signal terminal (2pcs, not used for contact) | 13711549 |
| CAN BUS and Signal Interface | Housing Molex (1pcs), | 33472-1206 |
| | Female terminal (12pcs, 11pcs used for contact) | 0330122002 |

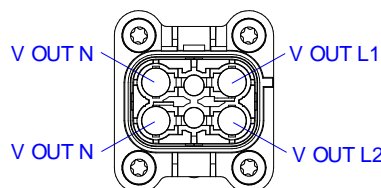
Note: Not all cavities are used for electrical function in signal and output connector. In order to maintain IP67 also non used cavities in mating connector must be assembled by dummy wires to get gasket into tightness.

Connector Pinouts

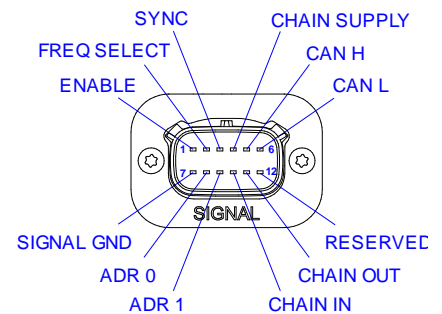
DC Input Connector



AC Output Connector



Signal Connector



Signal Connector Pin Description

| PIN | DESCRIPTION | FUNCTION |
|-----|--------------|--------------------------------|
| 1 | ENABLE | ENABLE |
| 2 | FREQ_SELECT | Frequency select |
| 3 | SYNC | Multi inverter synchronization |
| 4 | CHAIN_SUPPLY | Chain protection supply |
| 5 | CAN_H | CAN BUS H |
| 6 | CAN_L | CAN BUS L |
| 7 | SIGNAL_GND | Signal return |
| 8 | ADR_0 | Addressing pin |
| 9 | ADR_1 | Addressing pin |
| 10 | CHAIN_IN | Chain protection input |
| 11 | CHAIN_OUT | Chain protection output |
| 12 | Reserved | Reserved |

Parallel Function of Multiple Modules in Synchronized Mode

Purpose of running multiple modules in parallel is to get higher power. The maximum number of parallel modules is 6, see wiring diagram below.

| Number of modules | Max. Power 1 [kVA] | Max. Power 2 [kVA] |
|-------------------|--------------------------|--------------------------------------|
| | Full coolant temp. range | Reduced coolant temp range to 50DegC |
| 2 | 11.2 | 12 |
| 3 | 16.8 | 18 |
| 4 | 22.4 | 24 |
| 5 | 28.0 | 30 |
| 6 | 33.6 | 36 |

During parallel operation load current is shared evenly between modules. However because of production tolerance on the output voltage calibration and passive current sharing between modules (droop sharing) there may be a deviation of load current - up to 6% difference module to module is possible vs. the average value. The result of this deviation may cause some modules to deliver more current than others.

In the Table above, in the centre column (Max. power 1), is the available power when none of the paralleled modules override their nominal current of 25A. The right column specifies condition for n modules - n * 6 kVA. In the case of Max. Power 2, some of the modules may be operating over their nominal power. Functioning above the maximum power is however acceptable since the overcurrent protection only activates at an output current of ~27.5A, however this is only possible when operating at a lower coolant temperature range, so that the over-temperature protection doesn't trip off the overloaded module.

3-Phase Function

Modules connected in a 3 phase configuration, per the diagram below are able to produce a 3 x 400 VAC output. To achieve the 240VAC, each modules L1 is connected to L2.

The slaves function at 120° and 240° out of phase from the master by setting up the addressing per the table below.

Note: Each of the address pins are digital and have an internal pull up impedances, therefore a non-connected pin will result in a logic high.

| ADR_0 (PIN8) | ADR_1 (PIN9) | FUNCTION |
|--------------|--------------|---------------|
| 1 | 1 | Master |
| 0 | 1 | Slave 120 Deg |
| 1 | 0 | Slave 240 Deg |
| 0 | 0 | Reserved |

ADDITIONAL DETAILS

Multi-unit CAN Addressing: After input power is connected parallel modules (or units connected in a three phase configuration, here after called “paired” units) automatically allocate their addresses on the CAN line. The design allows up to 6 paired modules, addresses are always chosen from the range of B4 to B9. Every time the input power is re-cycled there may be different addresses allocated to units in comparison to the previous state, but they will be always inside of range of B4 to B9. If the user counts active devices, within this range, you know how many modules are actively connected. In case the CAN line is not required for paired modules, then it does not need to be connected. However in the case CAN is not used the sync BUS and chain circuit still need to be co connected.

Enable Control: Output enable control works either through the hardware enable pin or by sending CAN message “nv_b_cmd_Enable”. In parallel mode, hardware enable pins are required to be wired together, this ensures the same level is sent to all modules. The requirement CAN enable message is sent out to all active units. With the exception of the above, both enable controls are equivalent. By re-cycling the enable control (disable then re-enable) will clear an output latch condition that might have occurred because of a protection feature e.g.: output over current, over temperature, etc.

Paired Mode Sync: Modules use the “sync” signal to synchronize their outputs. This happens automatically and there is no user intervention required. After start up one of the paired units automatically establishes itself as the master, all other units in the paired configuration will default to slave operation. Slaves synchronize their outputs according to the reference waveform from master.

Read Back of Input / Output Values: In paired mode each module provides its own values. However in paired mode when input and/or output voltages are tied together there could be a read back inaccuracy of up to typical $\pm 2V$. Regarding output current, each module will provide its own value. To get the total output current the user must summarize read backs from all active modules.

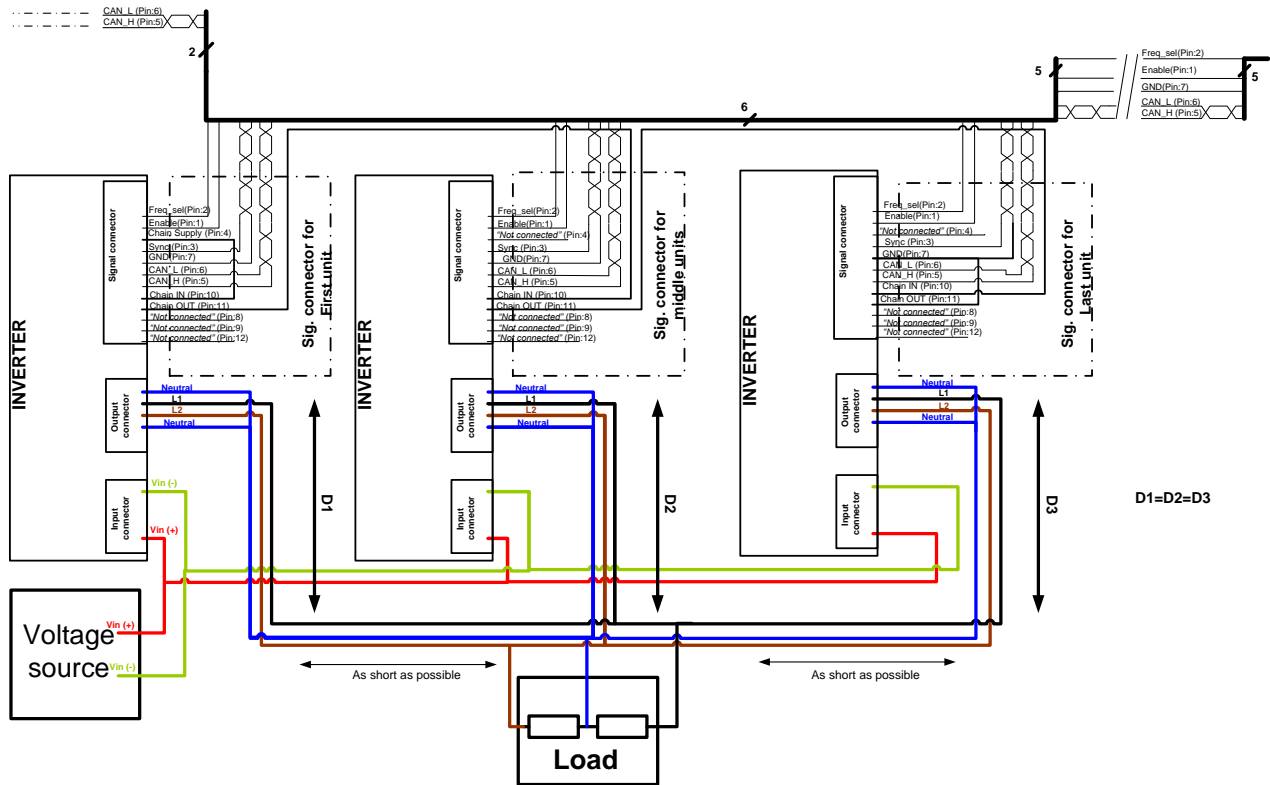
Paired Mode Protection Functions: In paired mode, the signal connector wiring is continuously monitored. This monitoring serves to protect modules against losing synchronization signal during service and is established via a chain circuit starting from the first module and ending with the last. The user needs to establish the chain circuit in the external wiring harness according to diagrams below. In case the chain line is broken all units in the chain will shut down and provide active warning bit on CAN Bit 29 – Inv_b_ft_SyncError. This may happen for example when signal connector of any unit is unplugged during service.

There is no redundancy provided for paired modules. This means in case a functional failure would occur in any of the paired modules, all modules would shut down. This is a requirement to protect output stages of modules against permanent damage.

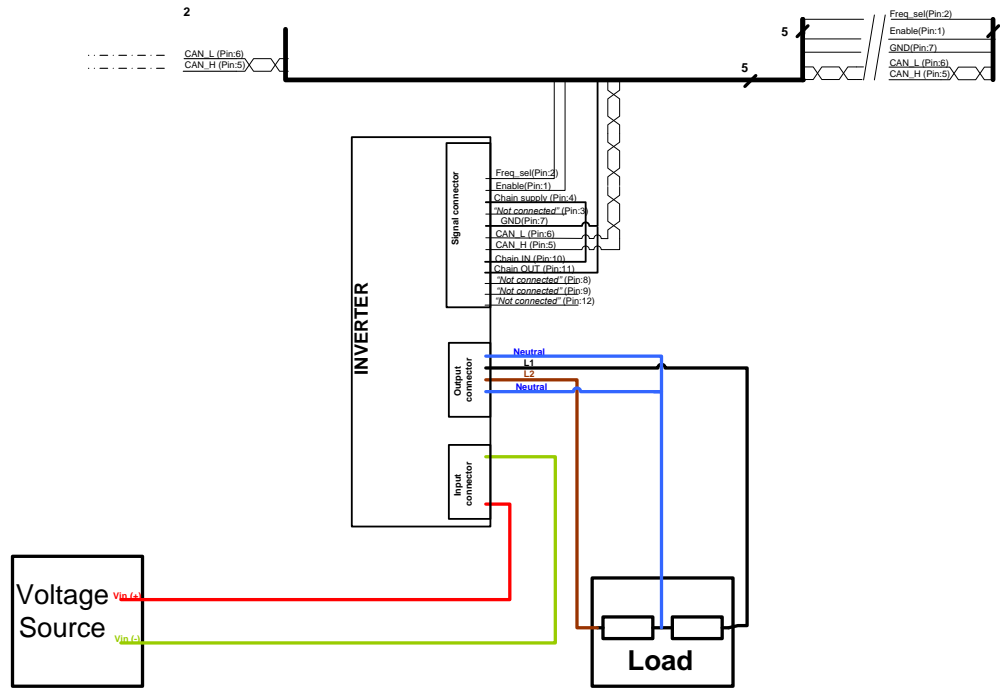
Paired Mode Chassis Grounding: During parallel operation it is required that the chassis of all paired modules are galvanic tied together and grounded to PE.

Wiring Setup

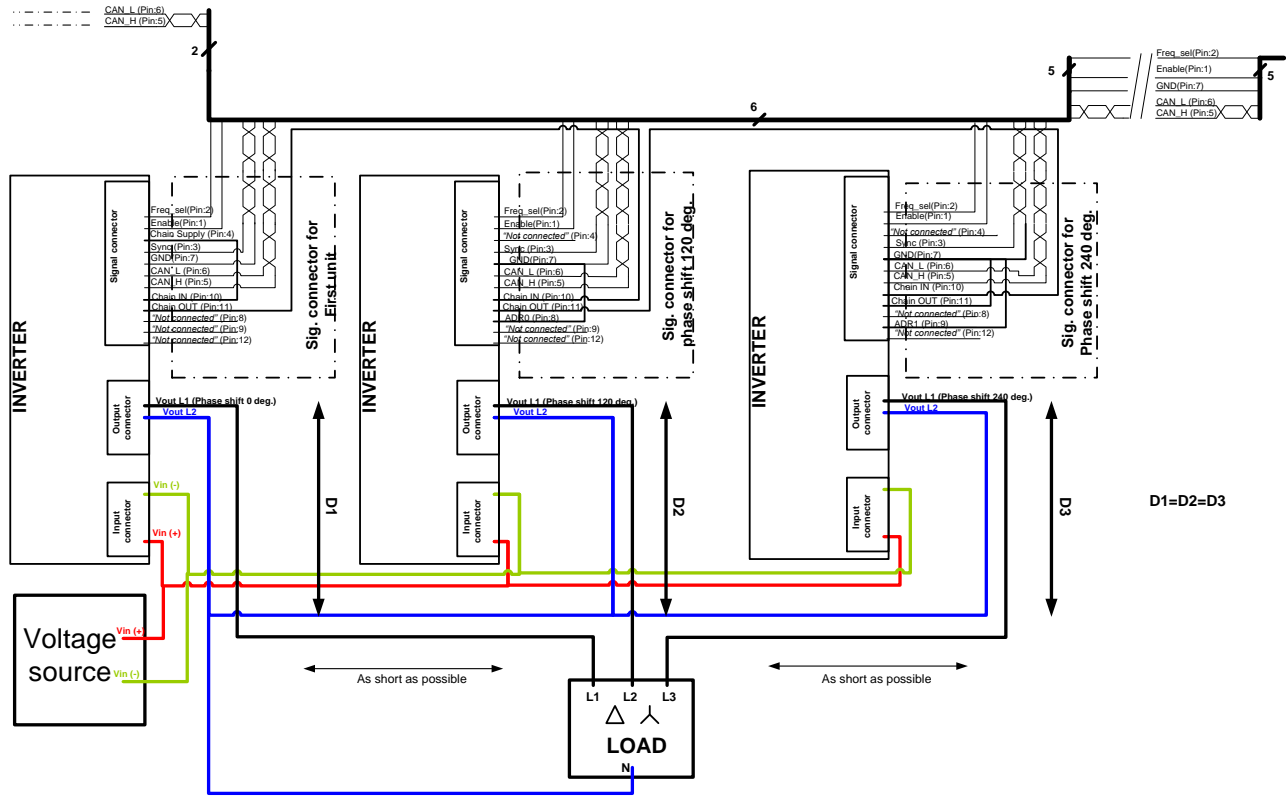
Wiring setup for parallel operation 700INV60-120-240-9G



Wiring setup for single operation 700INV60-120-240-9G



Wiring setup for 3-phase operation 700INV60-120-240-9G

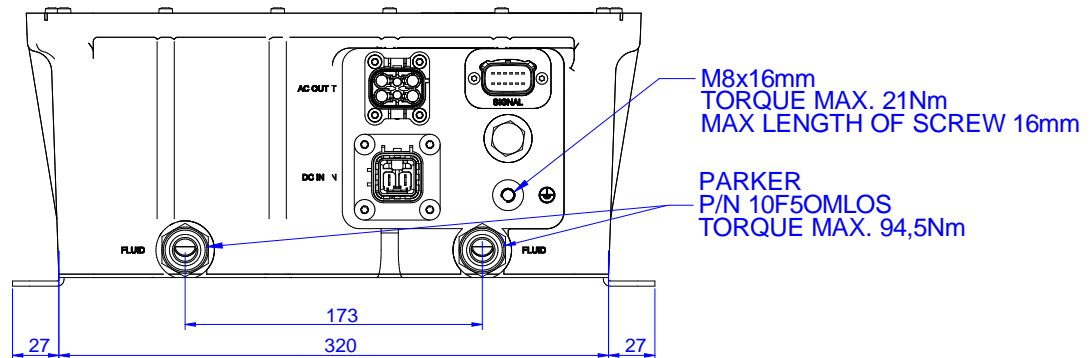


Mechanical Specifications

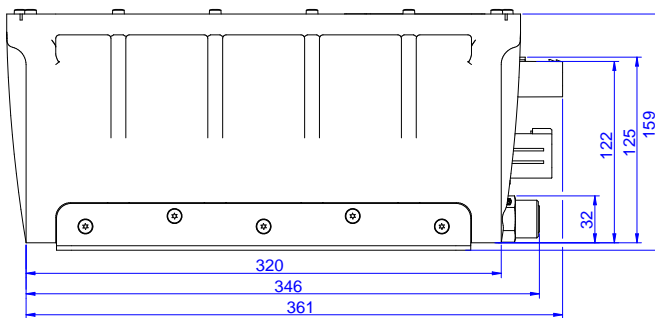
| PARAMETER | DESCRIPTION |
|--|--|
| Dimensions (W x H x D) | 374 x 154 x 374 mm |
| Weight | 25 kg |
| Enclosure | IP65 and IP67 |
| Maximum allowed pressure of cooling liquid | 20 PSI (1.4bar) |
| Recommended Coolant Medium/Mixture | 50/50 Propylene or Ethylene Glycol/Water |
| Recommended Coolant Flow | 0.0208 to 0.0505 l/s (0.33 to 0.8 GPM) |
| Inlet/Outlet Coolant Connection | 10F5OMLOS or equivalent fitting |

Mechanical Drawings

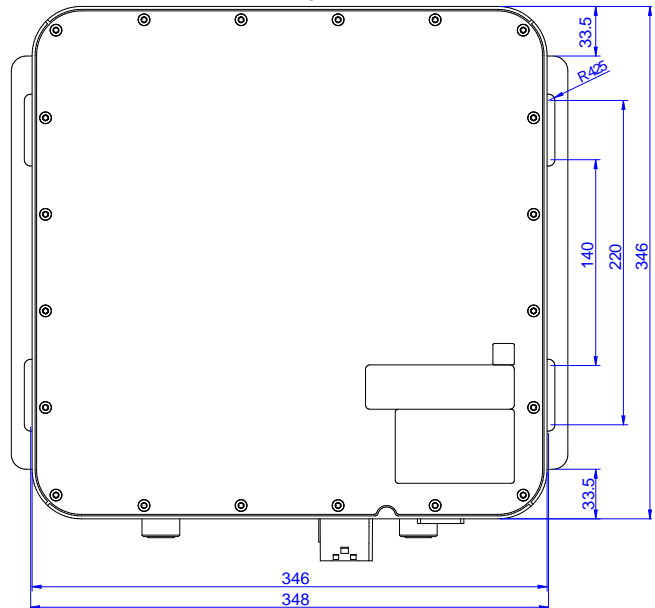
Front View



Side View



Top View



For more information on these products consult: tech.support@psbel.com

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