

■ Features

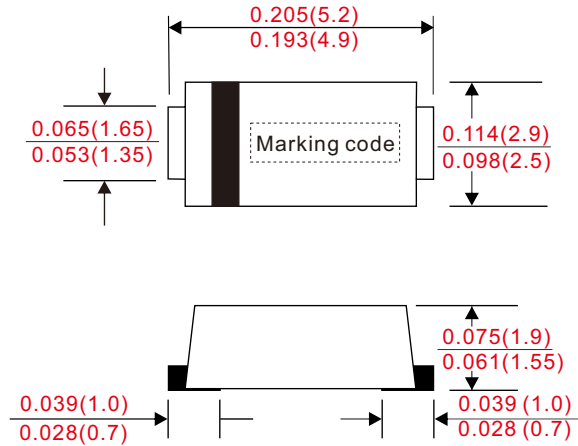
- Electrostatic discharge (ESD) test under IEC6100-4-2 standard >16KV(SK L32AS~SK L36AS). standard >10KV(SK L310AS~SK L320AS).
- Low profile surface mounted application in order to optimize board space.
- Low power loss, high efficiency.
- High current capability, low forward voltage drop.
- Ultra high-speed switching.
- Silicon epitaxial planar chip, metal silicon junction.
- Suffix "G" indicates Halogen-free part, ex. SK L32ASG.
- Lead-free parts meet environmental standards of MIL-STD-19500 /228

■ Mechanical data

- Epoxy: UL94-V0 rated flame retardant
- Case : Molded plastic, DO-214AC / SMAS
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026
- Polarity : Indicated by cathode band
- Weight : Approximated 0.08 gram

■ Outline

SMAS(DO-214AC)



Dimensions in inches and (millimeters)

■ Maximum ratings and electrical characteristics

Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Parameter	Conditions	Symbol	MIN.	TYP.	MAX.	UNIT
Forward rectified current	See Fig.1	I_O			3.0	A
Forward surge current	8.3ms single half sine-wave superimposed on rate load (JEDEC method)	I_{FSM}			125	A
Reverse current	$V_R = V_{RRM}$ $T_A = 25^\circ\text{C}$	I_R			0.5	mA
	$V_R = V_{RRM}$ $T_A = 100^\circ\text{C}$				20	
Diode junction capacitance	f=1MHz and applied 4V DC reverse voltage	C_J		250		pF
Thermal resistance	Junction to ambient	$R_{\theta JA}$		55		°C/W
Storage temperature		T_{STG}	-55		+150	°C

Symbol	Marking code	Max. repetitive peak reverse voltage V_{RRM} (V)	Max. RMS voltage V_{RMS} (V)	Max. DC blocking voltage V_R (V)	Max. forward voltage @3A, $T_A = 25^\circ\text{C}$ V_F (V)	Operating Junction temperature T_J (°C)
SKL32AS	KL32	20	14	20	0.40	-55 ~ +150
SKL34AS	KL34	40	28	40	0.45	
SKL36AS	KL36	60	42	60	0.55	
SKL310AS	KL310	100	70	100	0.75	
SKL315AS	KL315	150	105	150	0.82	-55 ~ +175
SKL320AS	KL320	200	140	200	0.85	

■ Rating and characteristic curves

FIG.1-TYPICAL FORWARD CURRENT DERATING CURVE

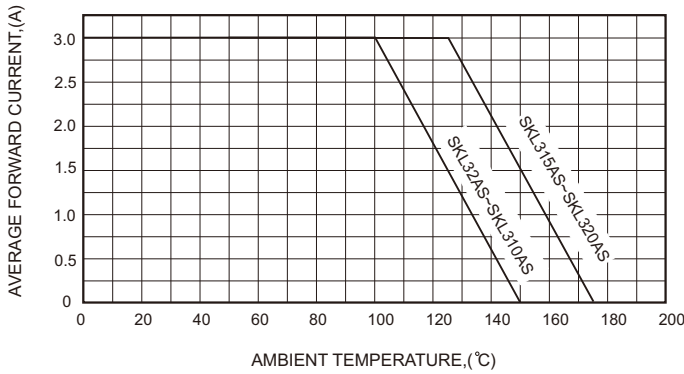


FIG.2-TYPICAL FORWARD CHARACTERISTICS

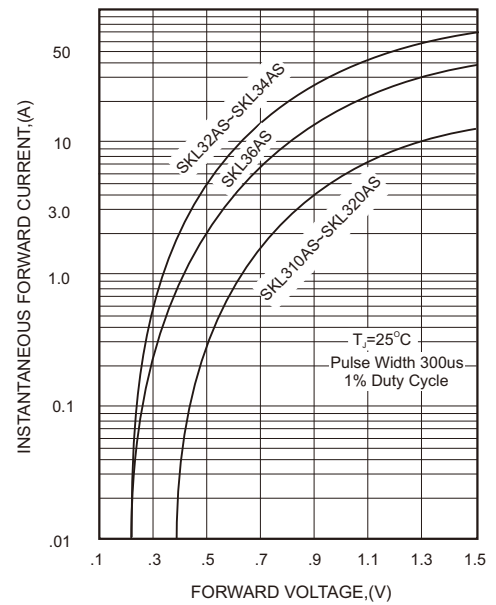


FIG.3-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

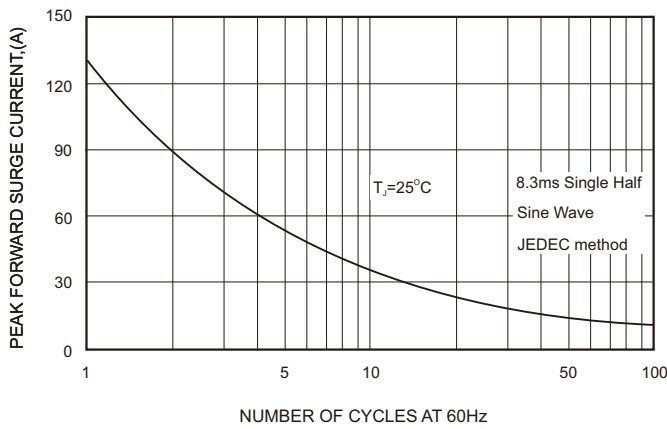


FIG.5 - TYPICAL REVERSE CHARACTERISTICS

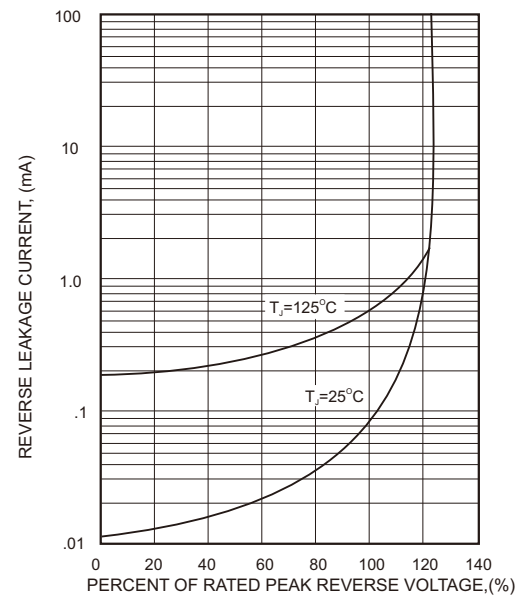
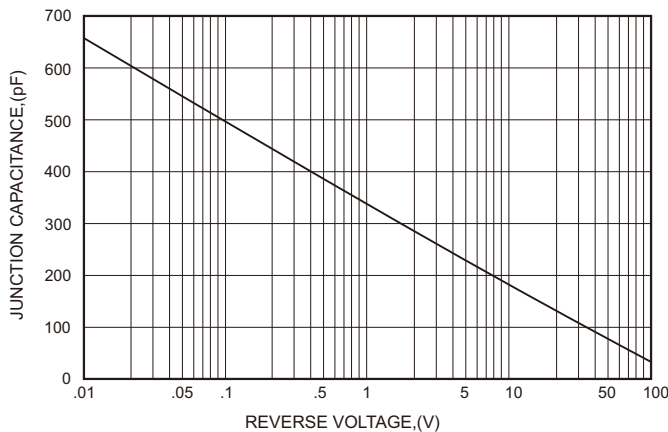
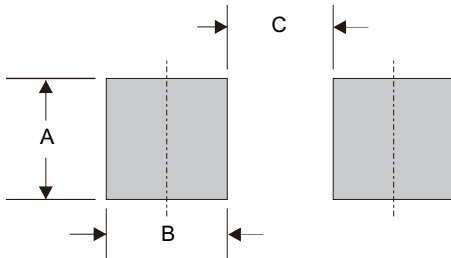


FIG.4-TYPICAL JUNCTION CAPACITANCE



■ SMAS foot print



A	B	C
0.063 (1.60)	0.059 (1.50)	0.110 (2.80)

Dimensions in inches and (millimeters)

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