

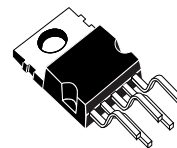
4A LINEAR DRIVER

- HIGH OUTPUT CURRENT (4A peak)
- HIGH CURRENT GAIN (10.000 typ.)
- OPERATION UP TO ± 20 V
- THERMAL PROTECTION
- SHORT CIRCUIT PROTECTION
- OPERATION WITHIN SOA
- HIGH SLEW-RATE (30 V/ms)

DESCRIPTION

The L149 is a general purpose power booster in Pentawatt[®] package consisting of a quasi-complementary darlington output stage with the associated biasing system and inhibit facility.

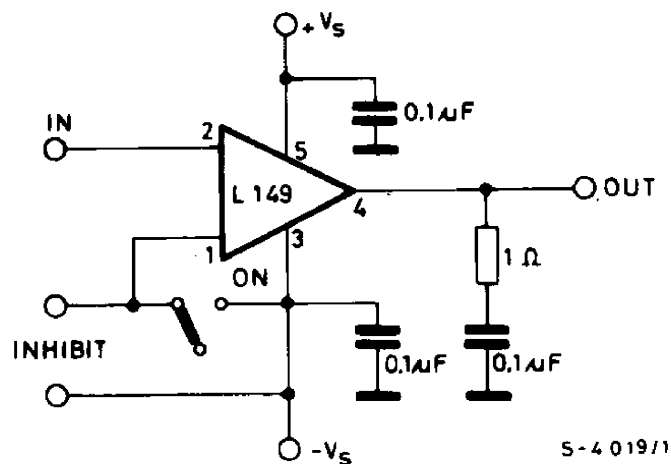
The device is particularly suited for use with an operational amplifier inside a closed loop configuration to increase output current.



Pentawatt V

ORDERING NUMBER: L149V

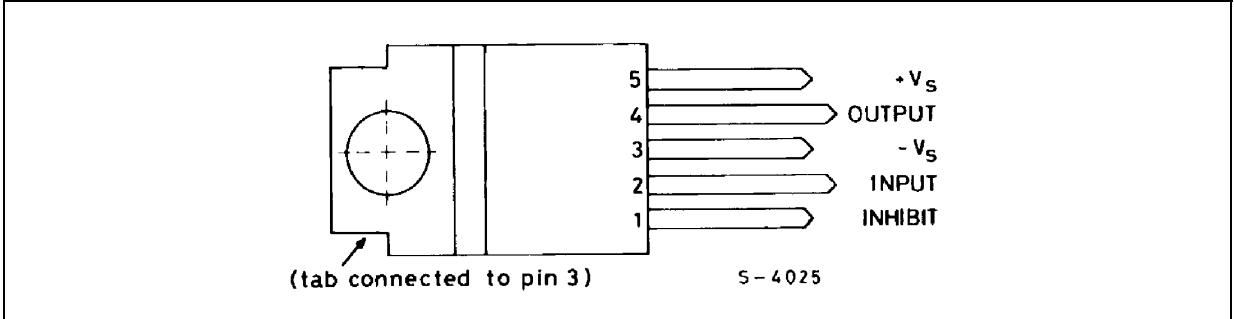
TEST CIRCUIT



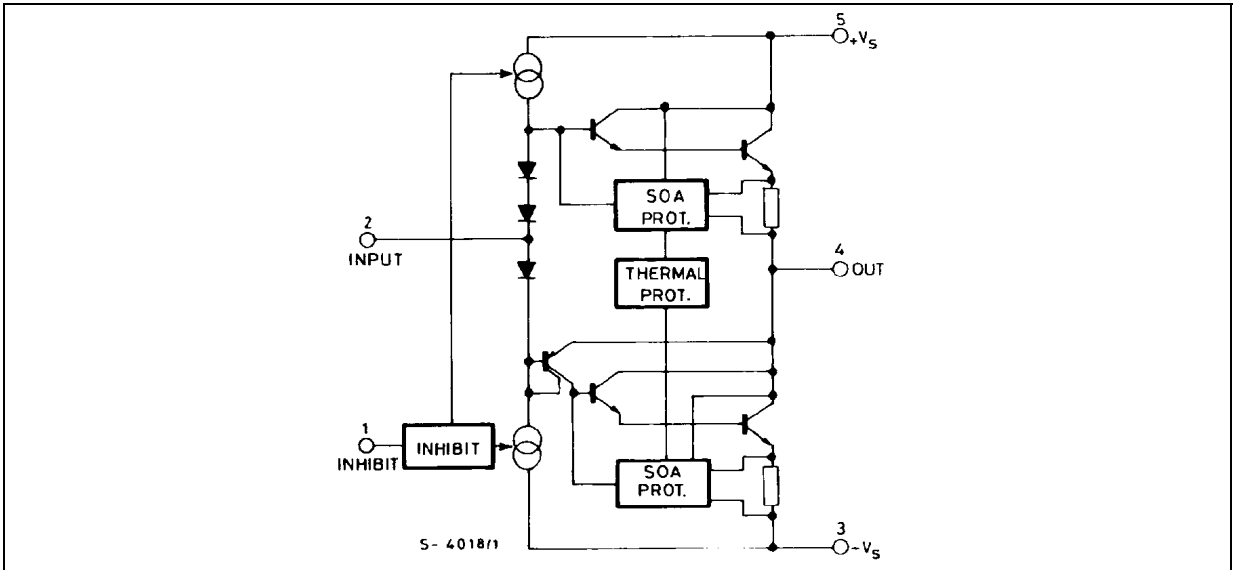
ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_S	Supply Voltage	± 20	V
V_i	Input Voltage		V_S
$V_5 - V_4$	Upper Power Transistor V_{CE}	40	V
$V_4 - V_3$	Lower Power Transistor V_{CE}	40	V
I_O	DC Output Current	3	A
I_O	Peak Output Current (internally limited)	4	A
V_{INH}	Input Inhibit Voltage	$-V_S + 5$	V
		$-V_S - 1.5$	V
P_{tot}	Total Power Dissipation at $T_{case} = 75\text{ }^{\circ}\text{C}$)	25	W

PIN CONNECTION (Top view)



SCHEMATIC DIAGRAM



THERMAL DATA

Symbol	Parameter	Value	Unit
$R_{th-j-case}$	Thermal resistance junction-case max	3	$^{\circ}\text{C/W}$

ELECTRICAL CHARACTERISTICS ($T_j = 25\text{ }^{\circ}\text{C}$, $V_S = \pm 16\text{V}$)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
V_S	Supply Voltage				± 20	V
I_d	Quiescent Drain Current	$V_S = \pm 16\text{V}$		30		mA
I_{in}	Input current	$V_S = \pm 16\text{V}$; $V_i = 0\text{V}$		200	400	μA
h_{FE}	DC current drain	$V_S = \pm 16\text{V}$; $I_O = 3\text{A}$	6000	10000		-
G_V	Voltage gain	$V_S = \pm 16\text{V}$; $I_O = 1.5\text{A}$		1		-
V_{CEsat}	Saturation voltage (for each transistor)	$I_O = 3\text{A}$			3.5	V
V_{OS}	Input offset voltage	$V_S = \pm 16\text{V}$			0.3	V
V_{INH}	Inhibit input voltage (pins 1-3)	ON condition			± 0.3	V
		OFF condition	± 1.8			V
R_{INH}	Inhibit input resistance			2.0		$\text{K}\Omega$
SR	Slew rate			30		$\text{V}/\mu\text{s}$
B	Power bandwidth	$V_O = \pm 10\text{V}$, $d = 1\%$, $R_L = 8\Omega$		200		KHz

APPLICATION INFORMATION

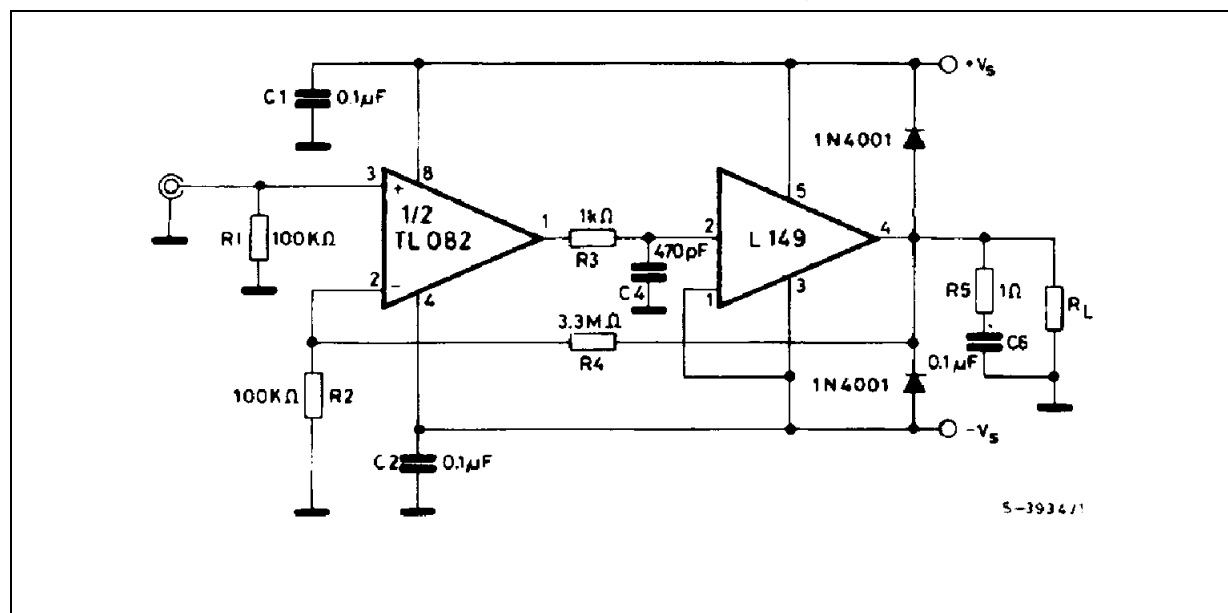
Figure 1. High slew-rate power operational amplifier ($\text{SR} = 13\text{V}/\mu\text{s}$)

Figure 2. Maximum saturation voltage vs. output current.

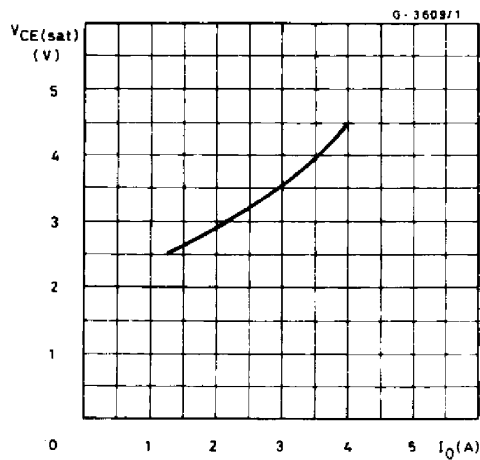


Figure 3. Current limiting characteristics.

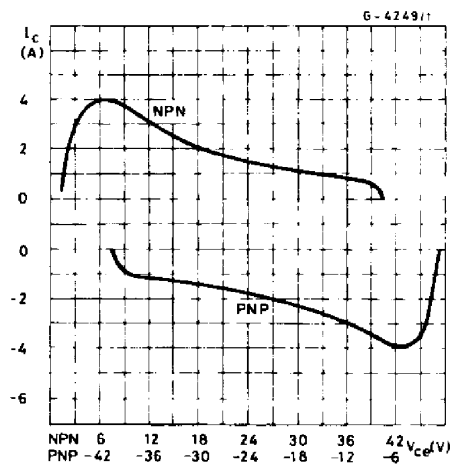


Figure 4. Supply voltage rejection vs. frequency.

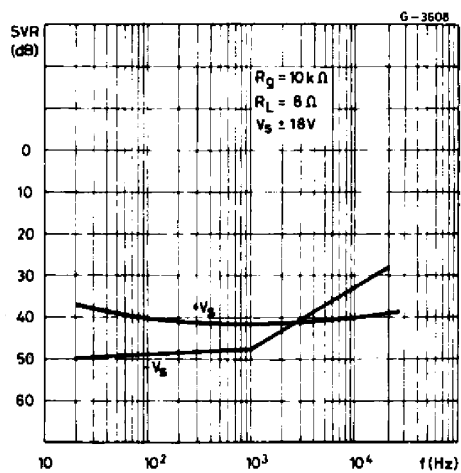
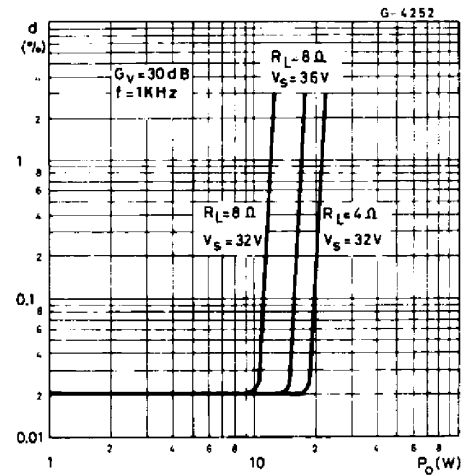
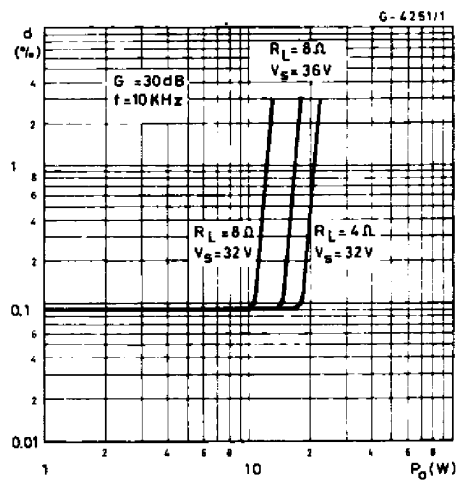
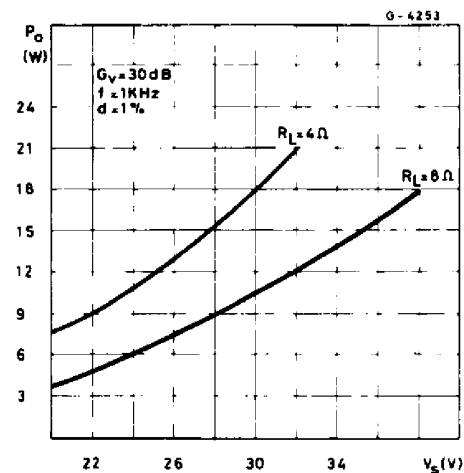
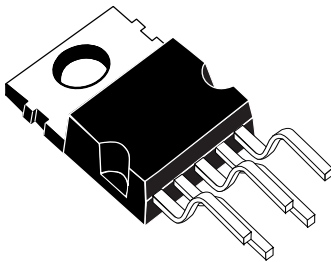
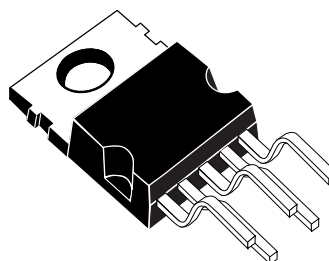
Figure 5. Distorsion vs. output power ($f = 1\text{ KHz}$).Figure 6. Distorsion vs. output power ($f = 1\text{ KHz}$).

Figure 7. Output power vs. supply voltage.

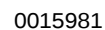


OUTLINE AND MECHANICAL DATA

Weight: 2.00gr

A 3D isometric drawing of a black, rectangular electronic component. The component has a circular hole on its top surface. Six pins extend from the bottom of the component, with three pins on each side. The drawing is shown from an isometric perspective, highlighting the component's three-dimensional shape.

Pentawatt V



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