



SUMMARY

CYTT21XXX
(28, 33, 35, 36 I/Os)

TrueTouch[®] Multi-Touch All-Points Touchscreen Controller Datasheet

Features

- Multi-touch capacitive touchscreen controller
 - 32-bit ARM[®] Cortex[™] CPU
 - Register-configurable
 - Noise-suppression technologies for battery charger and display
 - Effective 24-V drive for higher signal-to-noise ratio (SNR)
 - ChargerArmor[™] for charger noise immunity
 - External display synchronization
 - Water rejection and wet-finger tracking using DualSense[™]
 - Multi-touch glove with automatic mode switching
 - Ten fingers with thin glove (≤1-mm thick)
 - Two fingers with thick glove (≤5-mm thick)
 - Fingernail tracking
 - Grip suppression
 - Large object rejection
 - Automatic baseline tracking to environmental changes
 - Low-power look-for-touch mode
 - Field upgrades via bootloader
 - Android[™] driver support
 - Cypress Manufacturing Test Kit (MTK)
 - Touchscreen sensor self-test and Panel ID reporting
- System performance (configuration dependent)
 - Screen sizes up to 5.0-inch diagonal
 - 4.8-mm sensor pitch; 16:9 aspect ratio
 - Up to 36 sense pins
 - 308 intersections (22 × 14)
 - Reports up to ten fingers
 - Small finger support down to 4 mm
 - Large finger support up to 30 mm
 - Refresh rate up to 300 Hz; other rates configurable
 - TX frequency up to 500 kHz
 - Best-in-class charger noise immunity
 - Immunity up to 35-V peak-to-peak (V_{PP})
 - Immunity to AT&T[®] Zero charger
- Power (configuration-dependent)
 - 1.71- to 1.95-V or 2.0- to 5.5-V digital and I/O supply
 - 2.65- to 4.7-V analog supply
 - 4-mW average power
 - 5.7- μ W typical deep-sleep power
- Sensor and system design (configuration-dependent)
 - Supports a variety of touchscreen sensors and stackups
 - Manhattan, diamond, Single-Layer Independent Multi-touch (SLIM[®]), and Totem-pole patterns
 - Sensor-on-Lens (SOL)
 - Plastic (PET) and glass-sensor substrates
 - LCD, AMOLED, and IPS displays
 - Metal mesh
 - Single-layer flexible printed circuit (FPC) routing enabled by flexible TX/RX configurations
- Communication interface
 - I²C slave at 100 and 400 kbps
 - SPI slave bit rates up to 8 Mbps^[1]
- Package options
 - 44-pin 5 × 5 × 0.6-mm QFN (0.35-mm lead pitch)
 - 48-pin 6 × 6 × 0.6-mm QFN (0.4-mm lead pitch)

Note

1. SPI support is only available in 48-pin QFN package.

Ordering Information

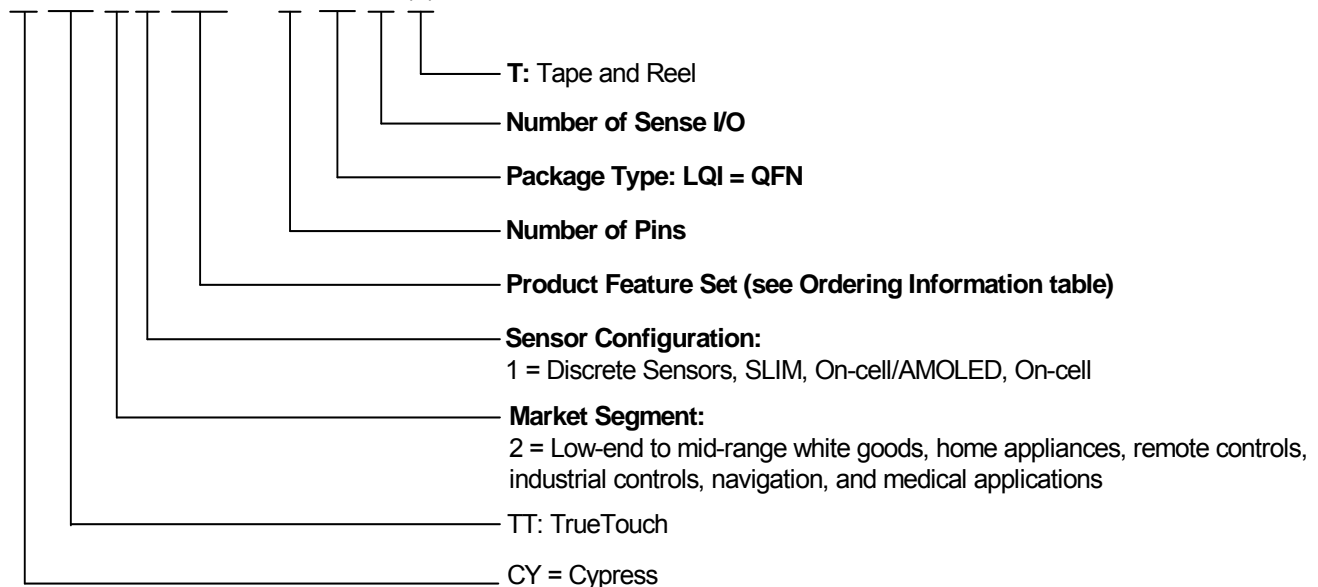
Table 1 lists the CYTT21XXX TrueTouch touchscreen controllers. For information on other TrueTouch families, visit <http://www.cypress.com/truetouch>.

Table 1. Device Ordering Information

Part Number	Top Marking	TrueTouch					Package
		Sense I/O	Base Features ^[2]	Advanced Wet-finger Tracking	Glove	Easy Wake	Type
CYTT21100-44LQI28(T)	CYTT21000-44LQI	28	✓	–	–	–	44-Pin QFN
CYTT21401-44LQI28(T)	CYTT21000-44LQI	28	✓	✓	✓	–	44-Pin QFN
CYTT21402-44LQI28(T)	CYTT21000-44LQI	28	✓	✓	✓	✓	44-Pin QFN
CYTT21100-44LQI33(T)	CYTT21000-44LQI	33	✓	–	–	–	44-Pin QFN
CYTT21401-44LQI33(T)	CYTT21000-44LQI	33	✓	✓	✓	–	44-Pin QFN
CYTT21402-44LQI33(T)	CYTT21000-44LQI	33	✓	✓	✓	✓	44-Pin QFN
CYTT21100-44LQI35(T)	CYTT21000-44LQI	35	✓	–	–	–	44-Pin QFN
CYTT21401-44LQI35(T)	CYTT21000-44LQI	35	✓	✓	✓	–	44-Pin QFN
CYTT21403-44LQI35(T)	CYTT21000-44LQI	35	✓	✓	✓	✓	44-Pin QFN
CYTT21100-48LQI36(T)	CYTT21000-48LQI	36	✓	–	–	–	48-Pin QFN
CYTT21401-48LQI36(T)	CYTT21000-48LQI	36	✓	✓	✓	–	48-Pin QFN
CYTT21403-48LQI36(T)	CYTT21000-48LQI	36	✓	✓	✓	✓	48-Pin QFN

Ordering Code Definitions

CY TT Y Y XXX – ## LQI ZZ (T)



Note

2. All devices have the following base features: Water Rejection, ChargerArmor, CapSense buttons, Large Object Detection and Rejection, and Grip Suppression.



Document History Page

Table with 5 columns: Revision, ECN, Orig. of Change, Submission Date, Description of Change. Contains two rows of revision history.

Sales, Solutions, and Legal Information

Worldwide Sales and Design Support

Cypress maintains a worldwide network of offices, solution centers, manufacturer's representatives, and distributors. To find the office closest to you, visit us at Cypress Locations.

Products

- Automotive cypress.com/go/automotive
Clocks & Buffers..... cypress.com/go/clocks
Interfacecypress.com/go/interface
Lighting & Power Control cypress.com/go/powerpsoc
Memory cypress.com/go/memory
PSoC.....cypress.com/go/psoc
Touch Sensing..... cypress.com/go/touch
USB Controllers cypress.com/go/USB
Wireless/RF..... cypress.com/go/wireless

PSoC® Solutions

psoc.cypress.com/solutions
PSoC 1 | PSoC 3 | PSoC 4 | PSoC 5LP

Cypress Developer Community

Community | Forums | Blogs | Video | Training

Technical Support

cypress.com/go/support

© Cypress Semiconductor Corporation 2015-2016. This document is the property of Cypress Semiconductor Corporation and its subsidiaries, including Spansion LLC ("Cypress"). This document, including any software or firmware included or referenced in this document ("Software"), is owned by Cypress under the intellectual property laws and treaties of the United States and other countries worldwide.

CYPRESS MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARD TO THIS DOCUMENT OR ANY SOFTWARE, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE. Cypress reserves the right to make changes to this document without further notice.

Cypress, the Cypress logo, Spansion, the Spansion logo, and combinations thereof, PSoC, CapSense, EZ-USB, F-RAM, and Traveo are trademarks or registered trademarks of Cypress in the United States and other countries.