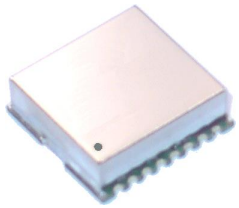
plerow™ APLS0681.5-R

PLL Synthesizer Module

Description

The plerow™ PLL synthesizer module was designed for use in wireless and wire-line systems in a wide range of frequency from 50 MHz to 6 GHz. ASB's PLL provides exceptionally low spurious and phase noise performance with fast locking time and low current consumption. All products are available in a surface-mount type package.



Specifications

Parameter	Unit	Min.	Typical	Max.
Frequency Range	MHz		681.5	
Output Power	dBm		3	
Supply Voltage	V		5	
Current Consumption	mA		30	
2nd Harmonics	dBc		-25	
Spurious Level	dBc		-65	
Reference Frequency	MHz		10	
Reference Input Level	dBm		0	
Phase Noise (C / N)				
@ 1 kHz	dBc/Hz		-78	
@ 10 kHz			-103	
@ 100 kHz			-120	
Output Impedance	Ω		50	
Operating Temp. Range	°C	-40	25	85
Package Type & Size	mm	SMT, 12.7W×12.7L×3H		

1) Measurement conditions are as follows: T = 25°C, V_{CC} = 5 V, Freq. = 681.5 MHz, 50 ohm system.

2) The phase noise tolerance could be within +/- 2 dBc/Hz depending upon the noise applied to PLL from the dc power supplier.

More Information

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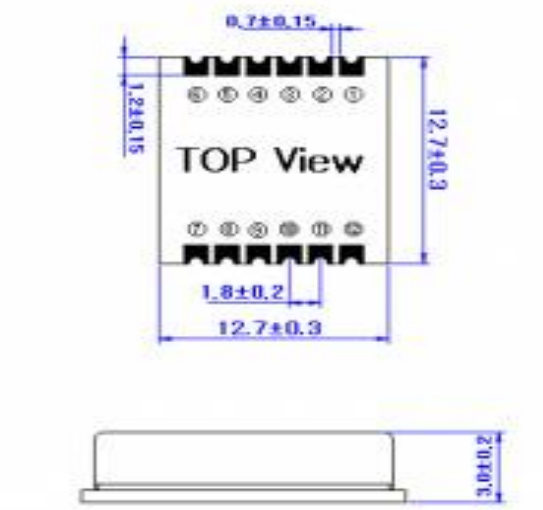
Outline Drawing

Type	Dimension
PLL IC + VCO + ROM	12.7 x 12.7 x 3



Pin Out for PLL			
Pin No.	**Application**	**Pin No.**	**Application**
1	GROUND	7	VCC (VCO)
2	GROUND	9	RF OUT
3	GROUND	11	VCC (PLL)
4	OSC IN	12	LOCK DETECT
All other Pins are Grounded / Internal ROM