

# DAC4101

## 0.01 TO 4.0 GHz ANALOG DETECTOR

Typical Values @ +25 °C	<b>DAC4101</b>
<b>Wide Frequency Range</b> .....	<b>0.01 to 4.5 GHz</b>
<b>Wide Power Range</b> .....	<b>-30.0 to +5.0 dBm</b>
<b>Temperature Stability</b> .....	<b>± 0.25 dB</b>
<b>Power Flatness</b> .....	<b>± 0.6 dB</b>
<b>Low VSWR</b> .....	<b>1.5:1</b>
<b>Single Power Supply</b>	
<b>Standard Size TO-8 Package</b>	

### SPECIFICATIONS<sup>\*,†</sup>

Parameter	Typical	Guaranteed	
		0 to 50 °C	-55 to +85 °C
Frequency (Min.)	<b>0.01-4.5 GHz</b>	<b>0.01-4.0 GHz</b>	<b>0.01-4.0 GHz</b>
Input Power Range (Min.)	-30 to +5 dBm	-25 to 0 dBm	-25 to 0 dBm
VSWR (Max.)	1.5:1†	1.8:1†	1.8:1†
Sensitivity, V <sub>out</sub> (Min.)	120 mV†	90 mV†	90 mV†
Power Flatness (Max.)	±0.6 dB <sup>†</sup>	±0.75 dB <sup>†</sup>	±0.75 dB <sup>†</sup>
Temperature Stability (Max.)	±0.25 dB <sup>†</sup>	±0.5 dB <sup>†</sup>	±0.5 dB <sup>†</sup>
Output Offset Voltage, no RF (Max.)	+0.5 mV	+2.0 mV	+2.0 mV
1 dB Square Law Departure	-10 dBm	—	—
Tangential Sensitivity	-45 dBm <sup>††</sup>	—	—
Pulse Response, Pin = -15 dBm	1.5 μsec‡	—	—
Pulse Response, Pin = 5 dBm	4.0 μsec‡	—	—
Max Output Voltage	V <sub>s</sub> -1 Volts	—	—
Supply Current, no RF	2 +mA, 2 -mA	—	—
Supply Current, Pin = 5 dBm	9 +mA, 2 -mA	—	—

\* 50-Ohm source impedance, 2 KΩ|50 pF output load, +5 Vdc. † Pin = -15 dBm.  
 † V<sub>out</sub> = 100 mV. †† 3 dB NF, 1 MHz Bandwidth. ‡ 50% RF to 10 or 90% Video.

### MAXIMUM RATINGS

DC Voltage .....	+18 V
Continuous RF Input Power .....	+14.0 dBm
Operating Case Temperature .....	-55 °C to +100 °C
Storage Temperature .....	-65 °C to +125 °C
Burn-In Temperature .....	+100 °C
Detector Thermal Resistance <sup>1</sup> (θ <sub>jc</sub> ) .....	+3500 °C/Watt
Temperature Rise @ 0 dBm (T <sub>jc</sub> ) .....	+3.5 °C
Temperature Rise @ 5 dBm (T <sub>jc</sub> ) .....	+35 °C

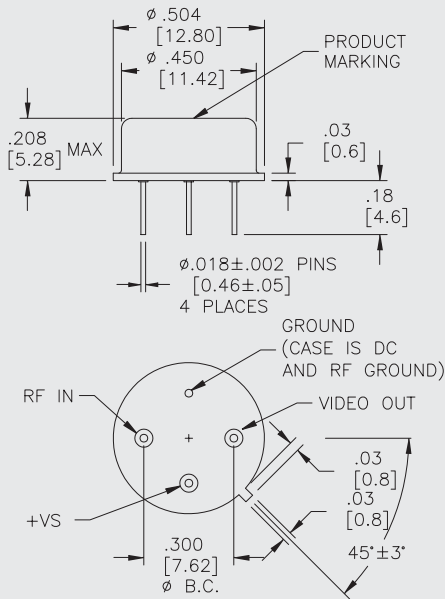
<sup>1</sup> Thermal resistance is based on RF input power. Ratings based on +25 °C.

### APPLICATION NOTES

- ✦ This unit is DC coupled and employs a RF choke at the input (DC short). If the application calls for the input to sink current there will be approximately an additional 1 mV of output offset voltage for each 3 mA of current. Sink current should be limited to 100 mA max to avoid choke burnout.
- ✦ For higher supply voltages, up to ±15 volts, the positive supply pin must include a series current limiting resistor, R<sub>s</sub> = (V<sub>s</sub> - 5) / 0.01 (e.g., V<sub>s</sub> = +15v, R<sub>s</sub> = 1.0KΩ).
- ✦ Average power detection is obtained at power levels below approximately -13 dBm.
- ✦ For best pulse response the supply pins should be bypassed with an additional 1.0 μF capacitor. The unit contains 0.01 μF internal capacitors.

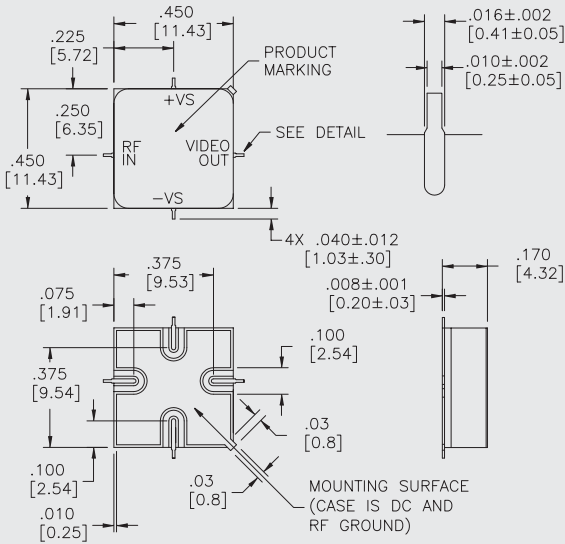
### DAC4101

**TO-8 Package for Detectors**



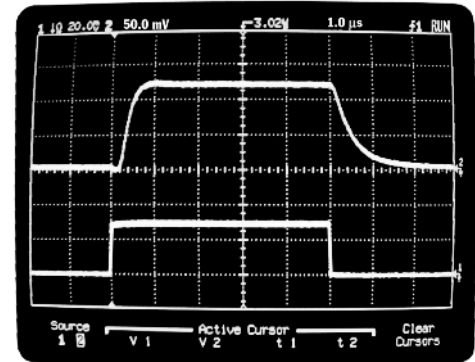
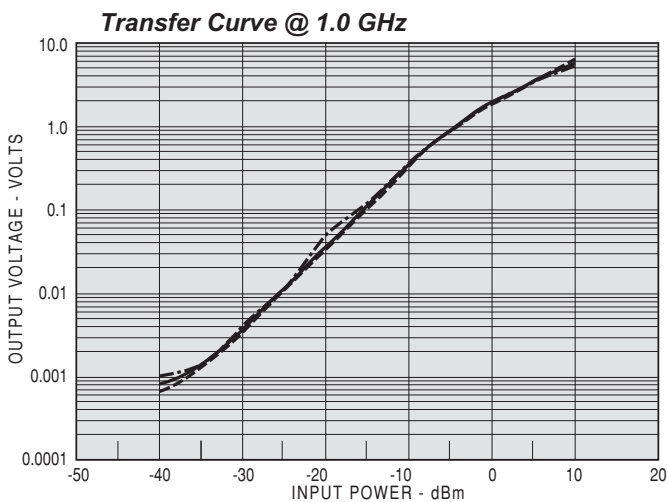
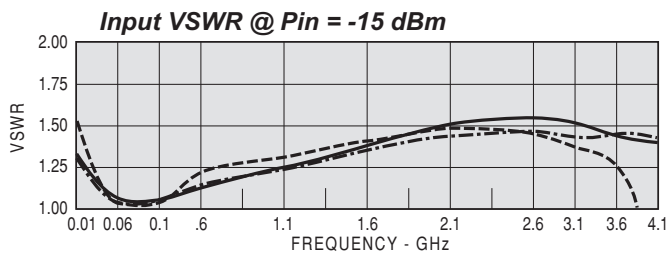
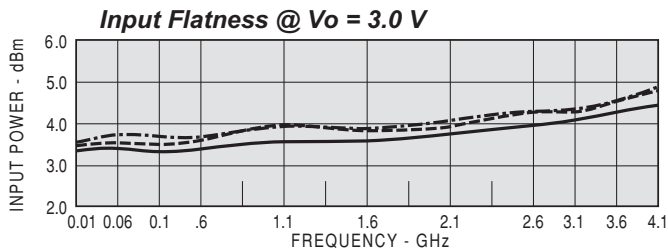
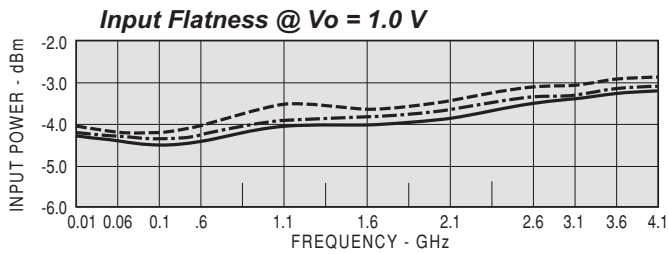
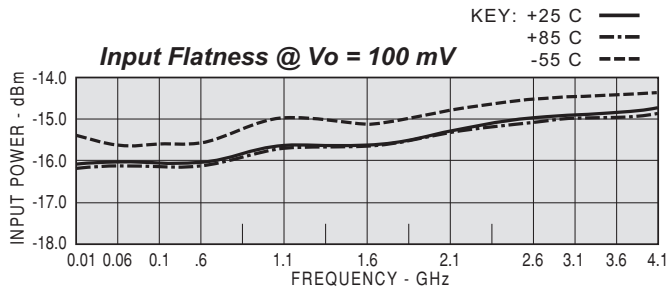
### DAS4101

**SMT0-8 Package for Detectors**

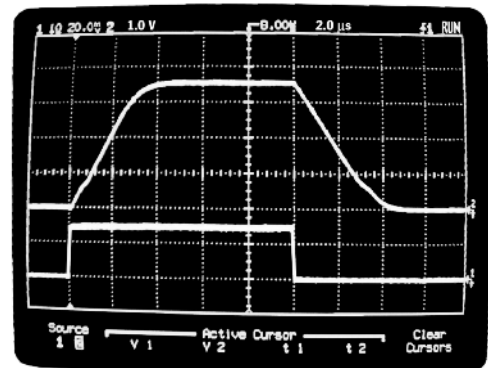


DIMENSIONS ARE IN INCHES [MILLIMETERS]

**TYPICAL PERFORMANCE**



**Pulse Response @  $P_{in} = -15\text{ dBm}$**



**Pulse Response @  $P_{in} = 5\text{ dBm}$**