SPS8040N SERIES

1. PART NO. EXPRESSION:

 $\frac{S P S}{(a)} \frac{8040}{(b)} \frac{N4R7MF}{(c)}$

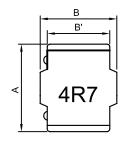
(a) Series code(b) Dimension code

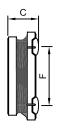
(d) Inductance code : 4R7 = 4.7 μH(e) Tolerance code : M=±20%,Y=±30%

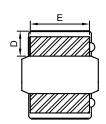
(c) Material code

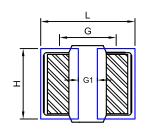
(f) F: RoHS Compliant

2. CONFIGURATION & DIMENSIONS:









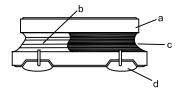
Recommendend Land pattern

Unit:m/m

| А | В | B' | С | D | E | F | L | G | G1 | Н |
|---------|---------|---------|---------|---------|---------|---------|---------|---------|---------|---------|
| 8.0±0.3 | 8.0±0.3 | 6.3±0.2 | 3.7±0.3 | 2.0±0.3 | 6.0±0.3 | 5.5±0.3 | 8.5 Ref | 5.5 Ref | 2.5 Min | 6.3 Ref |

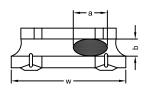
3. MATERIALS:

- (a) Core
- (b) Wire
- (c) Coating
- (d) Solder



Void appearance tolerance Limit

Size of voids occurring to coating resin is specified below.



Exposed wire tolerance limit of coating resin part on product side.

Size of exposed wire occurring to coating resin is specified below.

1. Width direction (dimension a) : Acceptable when a \leq w/2

Nonconforming when a>w/2

- 2. Length direction (dimension b): Dimension b is not specified.
- The total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, and is acceptable.



NOTE: Specifications subject to change without notice. Please check our website for latest information.

30.06.2015



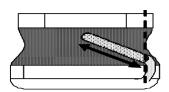
SUPERWORLD ELECTRONICS (S) PTE LTD

PG. 1

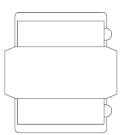
SPS8040N SERIES

External appearance criterion for exposed wire

Exposed end of the winding wire at the secondary side should be 3mm and below.



Electrode appearance criterion for exposed wire



Only top side of wire is exposed.

 \sim

(regardless of whole tope side of wire exposed)

Less than 1/2 of joint side length. (More than 1/2 is selected as defect)

Wire is soldered insufficiently and less than half of outer diameter is covered with solder.

4. GENERAL SPECIFICATION:

- (a) All test data referenced to 25°C ambient , Ls:1MHz/1V.
- (b) Heat Rated Current (Irms) will cause the coil temperature rise approximately ∆t of 40°C (keep 1min.).
- (c) Saturation Current (Isat) will cause L0 to drop 30% typical. (keep quickly).
- (d) The part temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions.
- (e) Operating temperature: -40~+125°C
- f) Storage Condition (Component in its packaging)
 - i) Temperature: -10°C to 40°C
 - ii) Humidity: 60%



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5. ELECTRICAL CHARACTERISTICS:

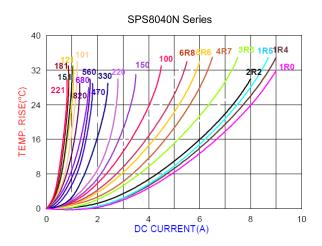
| | | | Rated c | | | | |
|---------------|--------------------|------------------------|---------|-----------------------|-------|--------------------------|--|
| Part Number | Inductance (µH) | Tempe curi I rm: | ent | Satur curi I sa | ent | DCR (mΩ) @25℃ ±20% | |
| | | Тур | Max | Тур | Max | | |
| SPS8040N1R0YF | 1.0±30% | 8.50 | 8.00 | 13.80 | 13.00 | 8.2 | |
| SPS8040N1R4YF | 1.4±30% | 8.20 | 7.80 | 11.80 | 11.20 | 10.0 | |
| SPS8040N1R5YF | 1.5±30% | 8.00 | 7.70 | 11.50 | 11.00 | 10.0 | |
| SPS8040N2R2YF | 2.2±30% | 7.40 | 6.90 | 9.80 | 9.20 | 11.5 | |
| SPS8040N3R3YF | 3.3±30% | 6.60 | 6.20 | 8.00 | 7.50 | 15.0 | |
| SPS8040N4R7MF | 4.7±20% | 5.80 | 5.30 | 6.70 | 6.00 | 19.5 | |
| SPS8040N5R6MF | 5.6±20% | 5.40 | 5.20 | 6.20 | 5.80 | 22.0 | |
| SPS8040N6R8MF | 6.8±20% | 5.10 | 5.00 | 5.60 | 5.10 | 25.0 | |
| SPS8040N100MF | 10±20% | 4.60 | 4.20 | 5.00 | 4.30 | 33.0 | |
| SPS8040N150MF | 15±20% | 3.60 | 3.20 | 4.00 | 3.60 | 50.0 | |
| SPS8040N220MF | 22±20% | 2.90 | 2.45 | 3.10 | 2.80 | 73.0 | |
| SPS8040N330MF | 33±20% | 2.30 | 2.10 | 2.60 | 2.10 | 100 | |
| SPS8040N470MF | 47±20% | 2.00 | 1.70 | 2.20 | 1.90 | 135 | |
| SPS8040N560MF | 56±20% | 1.75 | 1.60 | 1.90 | 1.60 | 160 | |
| SPS8040N680MF | 68±20% | 1.65 | 1.50 | 1.75 | 1.50 | 205 | |
| SPS8040N820MF | 82±20% | 1.40 | 1.30 | 1.60 | 1.40 | 230 | |
| SPS8040N101MF | 100±20% | 1.20 | 1.10 | 1.45 | 1.20 | 300 | |
| SPS8040N121MF | 120±20% | 1.10 | 1.00 | 1.30 | 1.10 | 350 | |
| SPS8040N151MF | 150±20% | 0.98 | 0.90 | 1.20 | 1.03 | 410 | |
| SPS8040N181MF | 180±20% | 0.91 | 0.83 | 1.04 | 0.94 | 490 | |
| SPS8040N221MF | 220±20% | 0.85 | 0.76 | 0.99 | 0.90 | 610 | |

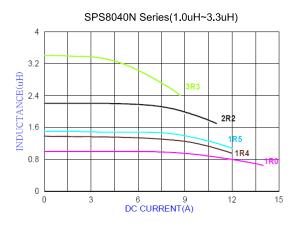


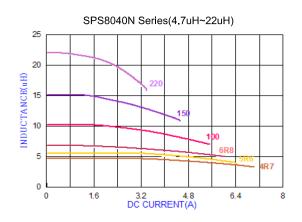
NOTE: Specifications subject to change without notice. Please check our website for latest information.

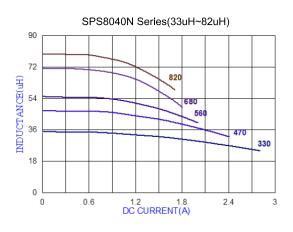
SPS8040N SERIES

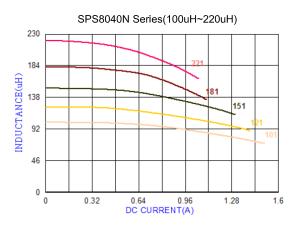
6. CHARACTERISTIC CURVES:













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SPS8040N SERIES

7. SOLDERING AND MOUNTING:

7-1. Soldering

Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. Our terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

7-1.1 Solder Re-flow:

Recommended temperature profiles for re-flow soldering in Figure 1.

7-1.2 Soldering Iron:

Products attachment with soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

Note:

- a) Preheat circuit and products to 150°C.
- b) 355°C tip temperature (max)
- c) Never contact the ceramic with the iron tip
- d) 1.0mm tip diameter (max)
- e) Use a 20 watt soldering iron with tip diameter of 1.0mm
- f) Limit soldering time to 4-5 secs.

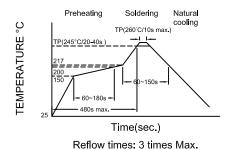
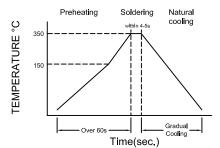


Fig.1



Iron Soldering times: 1 times Max.

Fig.2



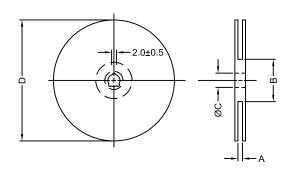
NOTE: Specifications subject to change without notice. Please check our website for latest information.

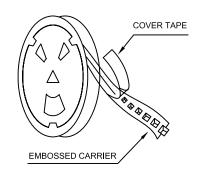


SPS8040N SERIES

8. PACKAGING INFORMATION:

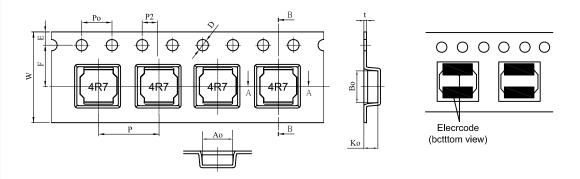
8-1. Reel Dimension





| Туре | A(mm) | B(mm) | C(mm) | D(mm) |
|------------|----------|--------|----------|---------|
| 13" x 16mm | 16.5±0.5 | 80±2.0 | 13.5±0.5 | 330±3.0 |

8-2 Tape Dimension



| Series | Ao(mm) | Bo(mm) | Ko(mm) | P(mm) | W(mm) | t(mm) | E(mm) | F(mm) | D(mm) | P0(mm) | P2(mm) |
|----------|----------|----------|----------|-----------|-----------|----------|-----------|----------|----------|----------|----------|
| SPS8040T | 8.4± 0.1 | 8.4± 0.1 | 4.3± 0.1 | 12.0± 0.1 | 16.0± 0.3 | 0.4± 0.1 | 1.75± 0.1 | 7.5± 0.1 | 1.5± 0.1 | 4.0± 0.1 | 2.0± 0.1 |

8-3 Packaging Quantity

| Size | SPS8040T | | |
|-----------|----------|--|--|
| Reel | 1000 | | |
| Inner Box | 2000 | | |
| Carton | 8000 | | |

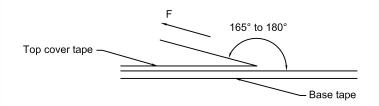
Pb RoHS Compliant

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SPS8040N SERIES

8-4. Tearing Off Force



The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions. (referenced ANSI/EIA-481-C-2003 of 4.11 standard)

| Room Temp. | Room Humidity | Room atm | Tearing Speed |
|------------|---------------|----------|---------------|
| (°C) | (%) | (hPa) | (mm/min) |
| 5~35 | 45~85 | 860~1060 | 300 |

Application Notice

1. Storage Conditions:

To maintain the solderability of terminal electrodes:

- a) Temperature and humidity conditions: Less than 40°C and 60% RH.
- b) Recommended products should be used within 12 months from the time of delivery.
- c) The packaging material should be kept where no chlorine or sulfur exists in the air.

2. Transportation:

- a) Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- b) The use of tweezers or vacuum pick up is strongly recommended for individual components.
- c) Bulk handling should ensure that abrasion and mechanical shock are minimized.



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