MIL-PRF-38534 AND 38535 CERTIFIED FACILITY



HIGH CURRENT, LOW DROPOUT SURFACE MOUNT VOLTAGE REGULATORS

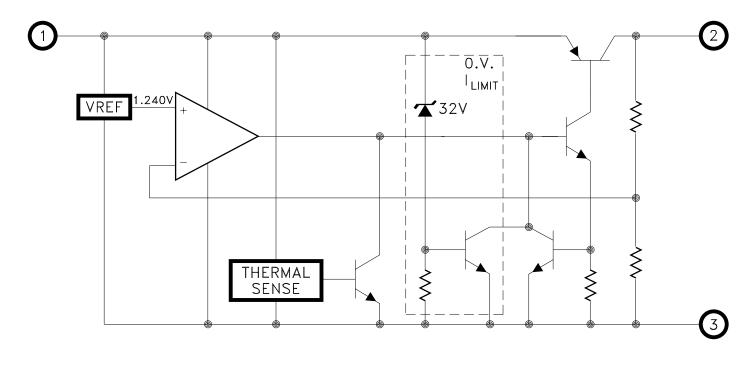


- Hermetic Surface Mount Package
- Extremely Low Dropout Voltage: 350mV @ 3 Amps
- Available in 1.5V, 1.7V, 1.8V, 1.9V, 2.5V, 3.3V, 5.0V and 12.0V
- On Board Thermal Shut Down
- Reverse Battery and Load Dump Protection
- Low Ground Current: 42mA Typical at Full Load
- 1% Maximum Guaranteed Accuracy
- Output Current to 3 Amps
- Alternate Output Voltages Available

DESCRIPTION:

The MSK5230 series voltage regulators are available in +1.5V, +1.7V, +1.8V, +1.9V, +2.5V, +3.3V, +5.0V, and +12.0V output configurations. All boast ultra low dropout specifications due to the utilization of a super PNP output pass transistor with monolithic technology. Dropout voltages of 350mV at 3 amps are typical in this configuration, which drives efficiency up and power dissipation down. Accuracy is guaranteed with a 1% maximum output voltage tolerance. The MSK5230 series is packaged in a space efficient 3 pin power surface mount ceramic package.

EQUIVALENT SCHEMATIC

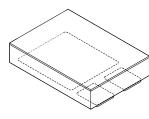


TYPICAL APPLICATIONS

- High Efficiency, High Current Linear Regulators
- Constant Voltage/Current Regulators
- System Power Supplies
- Switching Power Supply Post Regulators
- Battery Powered Equipment

PIN-OUT INFORMATION

1 VIN 2 VOUT 3 Ground LID=ISOLATED





ABSOLUTE MAXIMUM RATINGS

V_{INP}	Input Voltage (100mS 1%D.C.)	-20V to +60V
Vin	Input Voltage	
V_{EN}	Enable Voltage	0.3V to 26V
lout	Output Current	3.0A

(10)

	Ts⊤	Storage	Temperature	Range	65°C to	+150°C
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TLD	Lead Temperature	-
	(10 Seconds Soldering).	
ΤJ	Operating Temperature	
	MSK5230 Series	40°C to +85°C
	MSK5230H Series	55°C to +125°C
	ESD Rating	Class 2

ELECTRICAL SPECIFICATIONS

Devementer	Test Conditions①③	Group A	MSK5230H SERIES			MSK5230 SERIES			Units
Parameter		Subgroup	Min.	Тур.	Max.	Min.	Тур.	Max.	Units
Output Voltage Telerance	IOUT = 10mA; VIN = VOUT +1V	1	-	±0.5	±1.0	-	±0.5	±1.0	%
Output Voltage Tolerance		2, 3	-	±1.0	±2.0	-	-	-	%
Dropout Voltage (2)	∆VOUT = -1%; IOUT = 100 mA	1	-	80	200	-	80	225	mV
	∆VOUT = -1%; IOUT = 3A	1	-	350	600	-	350	625	mV
Load Regulation ⑧	10 mA ≤ IOUT ≤ 2.5A	1	-	±0.2	±1.0	-	±0.2	±1.2	%
		2, 3	-	±0.3	±2.0	-	±0.3	-	%
Line Regulation	$(\text{VOUT +1V}) \leq \text{VIN} \leq 26\text{V}$	1	-	±0.05	±0.5	-	±0.05	±0.6	%
	IOUT = 10 mA	2, 3	-	±0.5	±1.0	-	±0.5	-	%
Output Current Limit ② ⑨	VOUT = 0V; VIN = VOUT +1V	-	-	4.5	5.0	-	4.5	5.0	A
Ground Current ② ⑧	VIN = VOUT +1V; IOUT = 1.5A	-	-	20	45	-	20	45	mA
Ground Current (2) (3)	VIN = VOUT +1V; IOUT = 3A	-	-	42	-	-	42	-	mA
Output Noise (2)	CL = 20µF; 10 HZ ≤ f ≤ 100 KHZ	-	-	400	-	-	400	-	μV
Thermal Resistance 2	Junction to Case @ 125°C	-	-	3.3	3.7	-	3.3	3.9	°C/W
Thermal Shutdown ② TJ		-	-	130	-	-	130	-	°C

NUMBER	AGE ⑦
MSK5230-1.5	+1.5V
MSK5230-1.7	+1.7V
MSK5230-1.8	+1.8V
MSK5230-1.9	+1.9V
MSK5230-2.5	+2.5V
MSK5230-3.3	+3.3V
MSK5230-5.0	+5.0V
MSK5230-12	+12.0V

NOTES:

- ① Output decoupled to ground using 47µF minimum capacitor unless otherwise specified.
- (2) This parameter is guaranteed by design but need not be tested.
- Typical parameters are representative of actual device performance but are for reference only.
- ③ All output parameters are tested using a low duty cycle pulse to maintain TJ = TC.
- (4) Industrial grade devices shall be tested to subgroup 1 unless otherwise specified.
- 5 Military grade devices ('H' suffix) shall be 100% tested to subgroups 1,2,3.
- (6) Subgroup 1 $TC = +25^{\circ}C$

Subgroup 2 TJ = +125°C

- Subgroup 3 TA = -55°C
- ⑦ Please consult the factory if alternate output voltages are required.
- (8) Due to current limit, maximum output current may not be available at all values of VIN-VOUT and temperatures. See typical performance curves for clarification.
- (9) The output current limit function provides protection from transient overloads but it may exceed the maximum continuous rating. Continuous operation in current limit may damage the device.
- ① Continuous operation at or above absolute maximum ratings may adversely effect the device performance and/or life cycle.

REGULATOR PROTECTION:

The MSK5230 series is fully protected against reversed input polarity, overcurrent faults, overtemperature conditions (Pd) and transient voltage spikes of up to 60V. If the regulator is used in dual supply systems where the load is returned to a negative supply, the output voltage must be diode clamped to ground.

OUTPUT CAPACITOR:

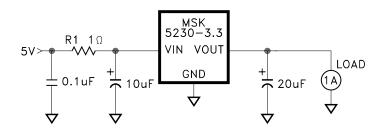
The output voltage ripple of the MSK5230 series voltage regulators can be minimized by placing a filter capacitor from the output to ground. The optimum value for this capacitor may vary from one application to the next, but a minimum of 20μ F is recommended for optimum performance. Transient load response can also be improved by placing a capacitor directly across the load. The capacitor should not be an ultra-low ESR type. Tantalum capacitors are best for fast load transients but aluminum electrolytics will work fine in most applications.

LOAD CONNECTIONS:

In voltage regulator applications where very large load currents are present, the load connection is very important. The path connecting the output of the regulator to the load must be extremely low impedance to avoid affecting the load regulation specifications. Any impedance in this path will form a voltage divider with the load.

MINIMIZING POWER DISSIPATION:

Many applications can not take full advantage of the extremely low dropout specifications of the regulator due to large input to output voltage differences. The simple circuit below illustrates a method to reduce the input voltage at the regulator to just over the dropout specification to keep the internal power dissipation minimized:



For a given continuous maximum load of 1 amp, R1 can be selected to drop the voltage seen at the regulator to 4V. This allows for the output tolerance and dropout specifications. Input voltage variations (5V) also should be included in the calculations. The resistor should be sized according to the power levels required for the application.

PACKAGE CONNECTIONS:

The MSK5230 series are highly thermally conductive devices and the thermal path from the package heat sink to the internal junctions is very short. Standard surface mount soldering techniques should be used when mounting the device. Some applications may require additional heat sinking of the device.

HEAT SINK SELECTION:

To select a heat sink for the MSK5230, the following formula for convective heat flow may be used.

WHERE:

Tj = Junction Temperature Pd = Total Power Dissipation Rθjc = Junction to Case Thermal Resistance Rθcs = Case to Heat Sink Thermal Resistance Rθsa = Heat Sink to Ambient Thermal Resistance Ta = Ambient Temperature

First, the power dissipation must be calculated as follows:

Next, the user must select a maximum junction temperature. The absolute maximum allowable junction temperature is 125° C. The equation may now be rearranged to solve for the required heat sink to ambient thermal resistance (R θ sa).

EXAMPLE:

An MSK5230-3.3 is configured for Vin=+5V and Vout=+3.3V. lout is a continuous 1A DC level. The ambient temperature is +25°C. The maximum desired junction temperature is 125°C.

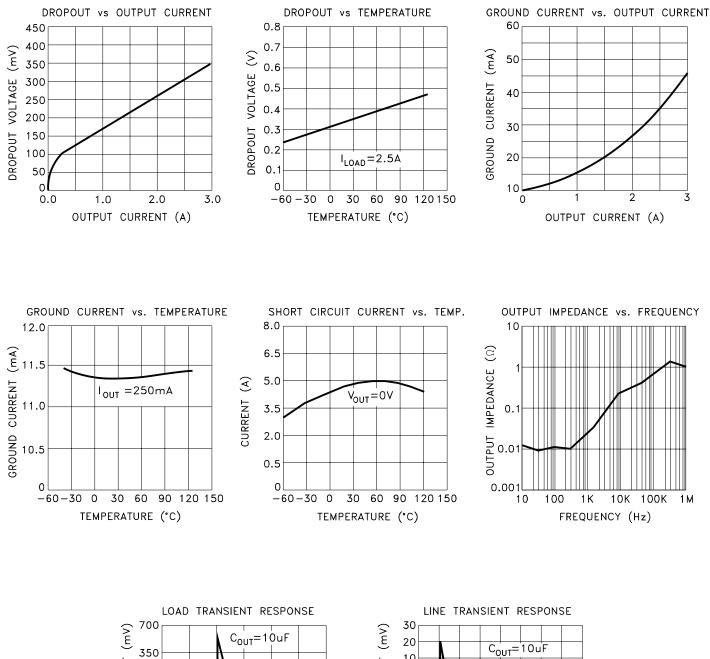
$$R\theta jc = 3.3$$
°C/W and $R\theta cs = 0.5$ °C/W typically.
Power Dissipation = (5V - 3.3V) x (1A)
= 1.7 Watts

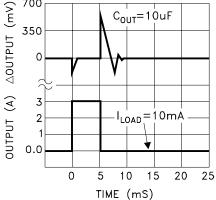
Solve for R0sa:

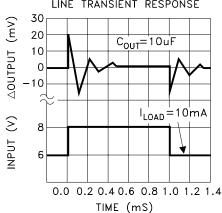
$$R\theta sa = \left[\frac{125^{\circ}C - 25^{\circ}C}{1.7W}\right] - 3.3^{\circ}C/W - 0.5^{\circ}C/W$$

In this example, a heat sink with a thermal resistance of no more than 55° C/W must be used to maintain a junction temperature of no more than 125° C.

TYPICAL PERFORMANCE CURVES

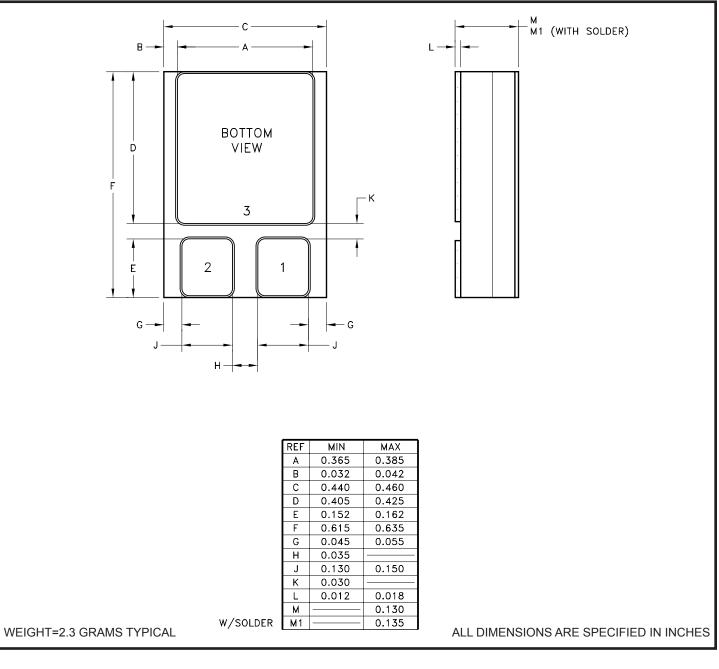




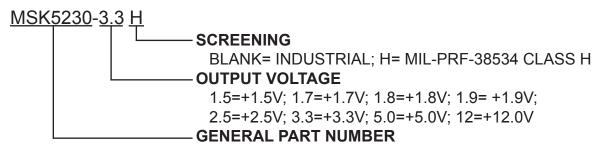


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MECHANICAL SPECIFICATIONS



ORDERING INFORMATION



The above example is a +3.3V, Military regulator.

REVISION HISTORY

REV	STATUS	DATE	DESCRIPTION
J	Released	04/15	Update format and add ESD rating.

MSK www.anaren.com/msk

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