

Multilayer Ceramic Chip Capacitors

CGA7K1X7R3D102K130KE



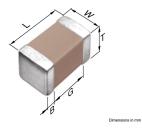






TDK item description CGA7K1X7R3D102KT***S

Applications	Automotive Grade	
	High High Voltage (1000V and over)	
Feature	Soft Termination	
	AEC-Q200 AEC-Q200	
Series	CGA7(4520) [EIA 1808]	
Status	Production	



	Size	
Length(L)	4.50mm +0.50,-0.40mm	
Width(W)	2.00mm +0.30,-0.20mm	
Thickness(T)	1.30mm ±0.20mm	
Terminal Width(B)	0.20mm Min.	
Terminal Spacing(G)		
Recommended Land Pattern (PA)	3.10mm to 3.70mm	
Recommended Land Pattern (PB)	1.20mm to 1.40mm	
Recommended Land Pattern (PC)	1.50mm to 2.00mm	
Recommended Slit Pattern (SD)	1.00mm to 1.30mm	

Electrical Characteristics		
Capacitance	1nF ±10%	
Rated Voltage	2kVDC	
Temperature Characteristic	X7R(±15%)	
Dissipation Factor (Max.)	3%	
Insulation Resistance (Min.)	10000ΜΩ	

Other		
Soldering Method	Reflow	
AEC-Q200	Yes	
Packing	Blister (Plastic)Taping [180mm Reel]	
Package Quantity	1000pcs	

[!] Images are for reference only and show exemplary products. ! This PDF document was created based on the data listed on the TDK Corporation website.

[!] All specifications are subject to change without notice.

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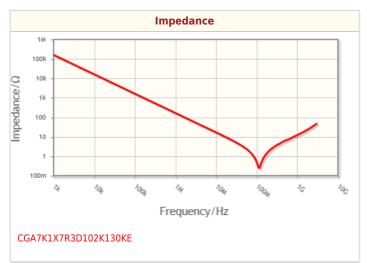


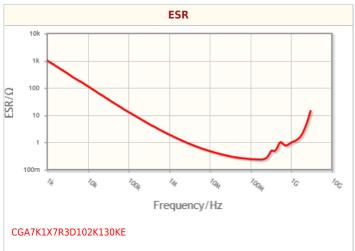


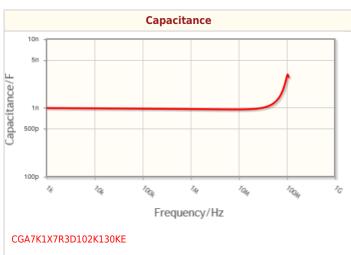


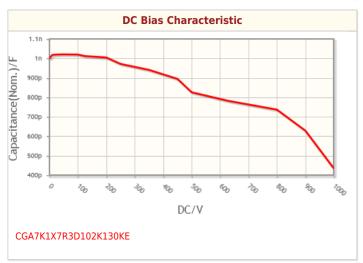


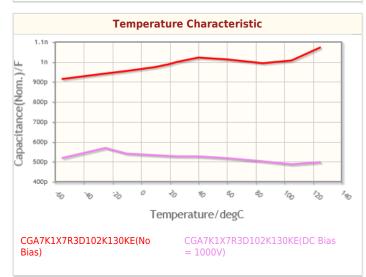
Characteristic Graphs(This is reference data, and does not guarantee the products characteristics.)

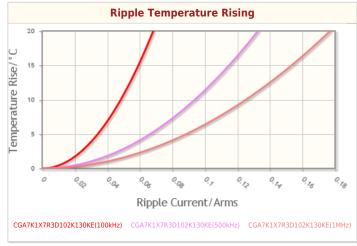












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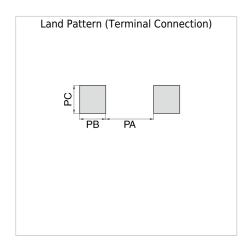








Associated Images



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