### NCE N-Channel Enhancement Mode Power MOSFET

### **Description**

The NCE30D0808J uses advanced trench technology to provide excellent  $R_{DS(ON)}$  and low gate charge. This device is suitable for use as a load switch and PWM applications.

### **Genera Features**

•  $V_{DS} = 30V, I_D = 7.7A$ 

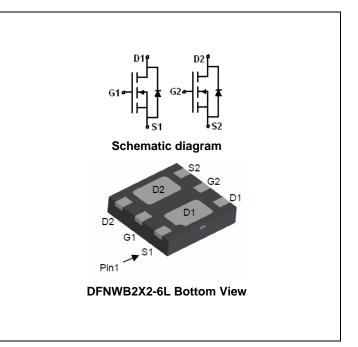
 $R_{DS(ON)}$  <25m $\Omega$  @  $V_{GS}$ =10V

 $R_{DS(ON)}$  <35m $\Omega$  @  $V_{GS}$ =4.5V

- High Power and current handing capability
- Lead free product is acquired
- Surface mount package

### **Application**

- General Purpose Interfacing Switch
- Power Management Functions



### **Package Marking and Ordering Information**

<b>Device Marking</b>	Device	Device Package	Reel Size	Tape width	Quantity
0808	NCE30D0808J	DFNWB2X2-6L	-	-	-

### Absolute Maximum Ratings (T<sub>C</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	30	V
Gate-Source Voltage	V <sub>G</sub> s	±20	V
Drain Current-Continuous	I <sub>D</sub>	7.7	Α
Drain Current-Pulsed (Note 1)	I <sub>DM</sub>	31	Α
Maximum Power Dissipation	P <sub>D</sub>	2	W
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 150	$^{\circ}\!\mathbb{C}$

### **Thermal Characteristic**

Thermal Resistance.Junction-to-Ambient <sup>Note 2)</sup>	Rou	62.5	°C/W
memai Resistance, Junction-to-Ambient	RθJA	02.5	C/VV

### Electrical Characteristics (T<sub>C</sub>=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	30	33	-	٧



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I <sub>DSS</sub> I <sub>GSS</sub> V <sub>GS(th)</sub>	$V_{DS}$ =30V, $V_{GS}$ =0V $V_{GS}$ =±20V, $V_{DS}$ =0V $V_{DS}$ = $V_{GS}$ , $I_{D}$ =250 $\mu$ A $V_{GS}$ =10V, $I_{D}$ =7A	1	- - 1.5	1 ±100	μA nA
V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250μA			±100	nA
, ,		1	15		
, ,		1	1.5		
R <sub>DS(ON)</sub>	V <sub>00</sub> =10V I <sub>0</sub> =7Δ		1.5	3	V
RDS(ON)	VGS-10V, 1D-17A	-	22	25	mΩ
, ,	V <sub>GS</sub> =4.5V, I <sub>D</sub> =5A	-	28	34	mΩ
•		•			
C <sub>lss</sub>	\\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	-	558	-	PF
Coss		-	72.7	-	PF
C <sub>rss</sub>	F=1.UMHZ	-	62.6	-	PF
<u>'</u>			<u>l</u>		
$t_{d(on)}$		-	2.4	-	nS
tr	$V_{DD}$ =15V, $R_L$ =3 $\Omega$	-	2.5	-	nS
$t_{d(off)}$	$V_{GS}$ =10 $V$ , $R_{GEN}$ =3 $\Omega$	-	9	-	nS
t <sub>f</sub>		-	2.5	-	nS
$Q_g$	\/ 45\/  5A	-	12	-	nC
Q <sub>gs</sub>		-	1.7	-	nC
$Q_{gd}$	V <sub>GS</sub> =10V	-	3.2	-	nC
<b>'</b>			L L		
$V_{SD}$	V <sub>GS</sub> =0V,I <sub>S</sub> =7.7A	-	-	1.2	V
Is		-	-	7.7	Α
	$C_{lss}$ $C_{oss}$ $C_{rss}$ $t_{d(on)}$ $t_{r}$ $t_{d(off)}$ $t_{f}$ $Q_{g}$ $Q_{gs}$ $Q_{gd}$ $V_{SD}$	$\begin{array}{c c} & V_{GS}\!=\!4.5\text{V},  I_{D}\!=\!5\text{A} \\ \hline \\ C_{ISS} & V_{DS}\!=\!15\text{V}, V_{GS}\!=\!0\text{V}, \\ \hline C_{rSS} & F\!=\!1.0\text{MHz} \\ \hline \\ t_{d(on)} & \\ t_{r} & V_{DD}\!=\!15\text{V},  R_{L}\!=\!3\Omega \\ \hline \\ t_{d(off)} & V_{GS}\!=\!10\text{V}, R_{GEN}\!=\!3\Omega \\ \hline \\ t_{f} & \\ \hline Q_{g} & \\ \hline Q_{gs} & \\ \hline Q_{gd} & \\ \hline \\ V_{SD} & V_{GS}\!=\!0\text{V}, I_{S}\!=\!7.7\text{A} \\ \hline \end{array}$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

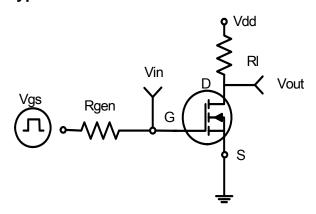
### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- **3.** Pulse Test: Pulse Width ≤  $300\mu$ s, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production

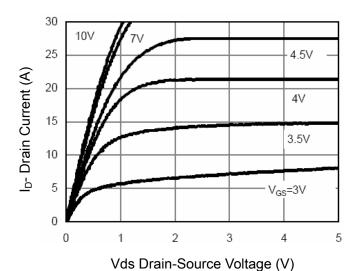
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### **Typical Electrical and Thermal Characteristics**



**Figure 1:Switching Test Circuit** 



**Figure 3 Output Characteristics** 

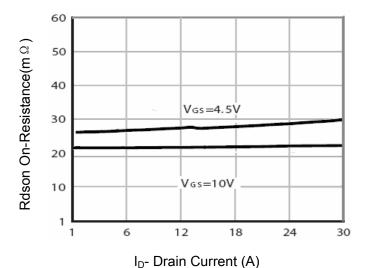


Figure 5 Drain-Source On-Resistance

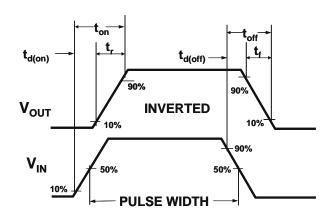
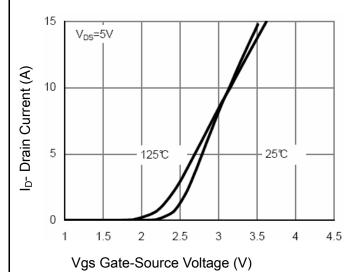


Figure 2:Switching Waveforms



**Figure 4 Transfer Characteristics** 

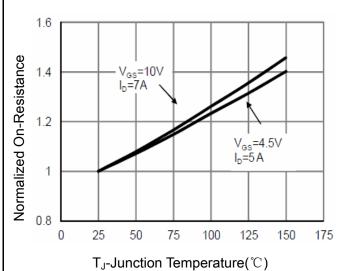
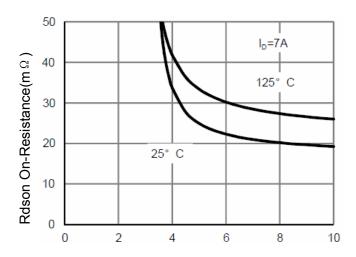


Figure 6 Drain-Source On-Resistance





Vgs Gate-Source Voltage (V)
Figure7 Rdson vs Vgs

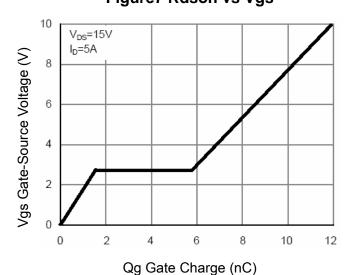


Figure 9 Gate Charge

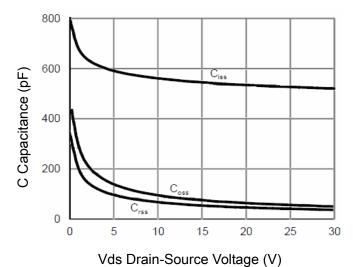


Figure 11 Capacitance vs Vds

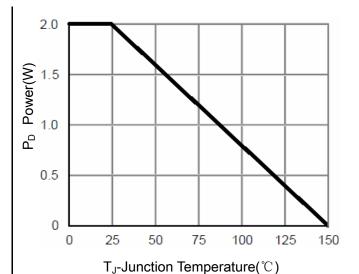


Figure 8 Power Dissipation

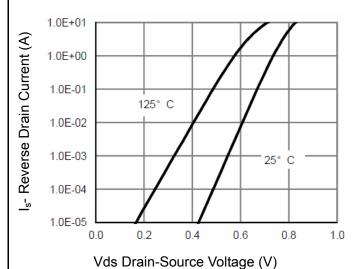
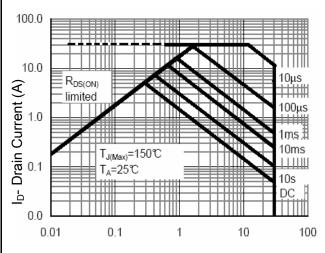
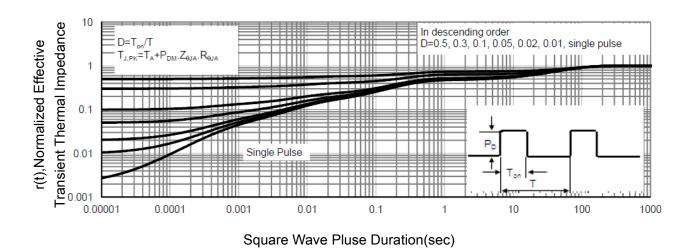


Figure 10 Source- Drain Diode Forward



Vds Drain-Source Voltage (V)
Figure 12 Safe Operation Area



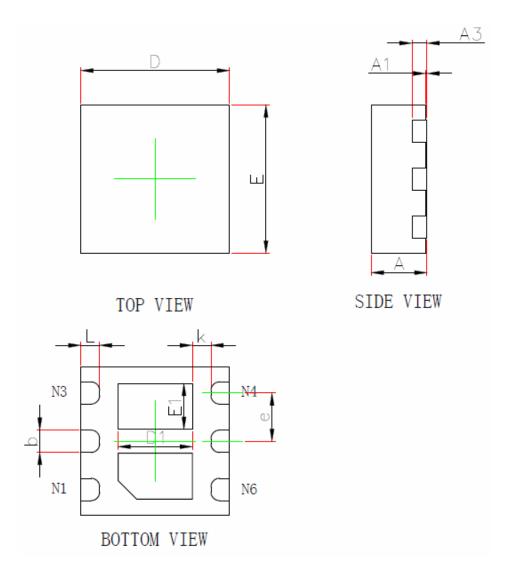


**Figure 13 Normalized Maximum Transient Thermal Impedance** 

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## **DFNWB2X2-6L Package Information**



Cumbal	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	0.700	0.800	0.028	0.031	
A1	0.000	0.050	0.000	0.002	
A3	0.20	3REF.	0.008	REF.	
D	1.900	2.100	0.075	0.083	
Е	1.900	2.100	0.075	0.083	
D1	0.900	1.100	0.035	0.043	
E1	0.520	0.720	0.020	0.028	
b	0.250	0.350	0.010	0.014	
е	0.650TYP. 0.200MIN.		0.026TYP. 0.008MIN.		
k					
L	0.200	0.300	0.008	0.012	



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