

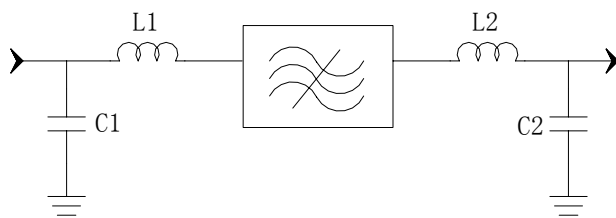
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	139.8	140	140.2
Insertion Loss	dB	-	24.4	25
1 dB Bandwidth	MHz	-	7.86	-
3 dB Bandwidth	MHz	8.25	8.43	-
40 dB Bandwidth	MHz	-	10.6	10.9
50 dB Bandwidth	MHz	-	10.8	11.25
Passband Variation	dB	-	0.6	0.9
Absolute Delay	usec	-	2.1	4
Group Delay Variation($f_0 \pm 4\text{MHz}$)	nsec	-	50	150
Ultimate Rejection	dB	50	62	-
Material Temperature coefficient	KHz/°C	-2.52		
Ambient Temperature	°C	25		
Package Size	DIP2212 (22.2x12.8x4.7mm3)			

Notes:


1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration

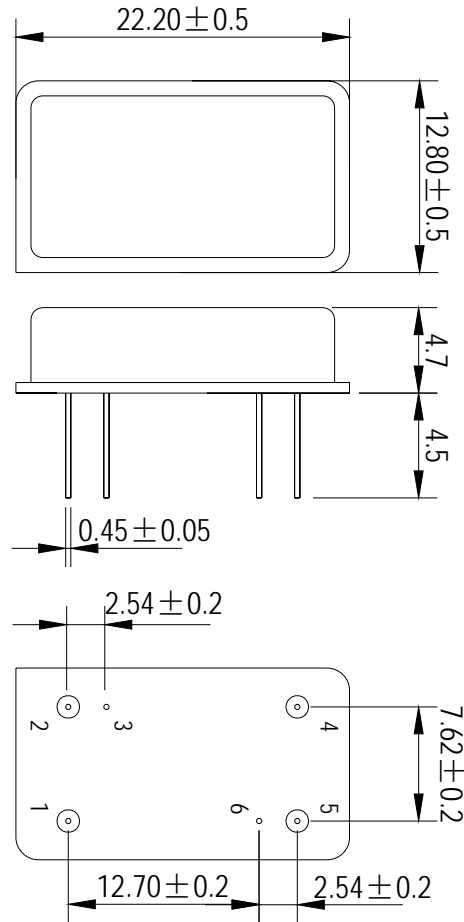


L1=L2=47nH
C1=39pF C2=33pF
Source/Load Impedance=50 ohm

Notes - Component values may change depending on board layout.

	SIPAT Co., Ltd. (CETC No. 26 Research Institute) Nanping Huayuan Road No. 14 Chongqing, China, 400060	Part Number	LBT14080	
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Package Dimension



Input:1
Output:5

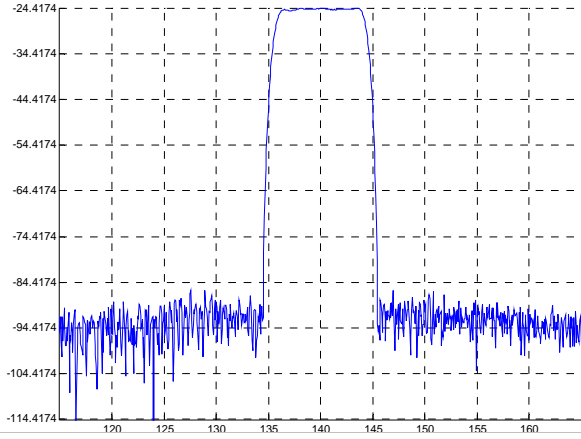


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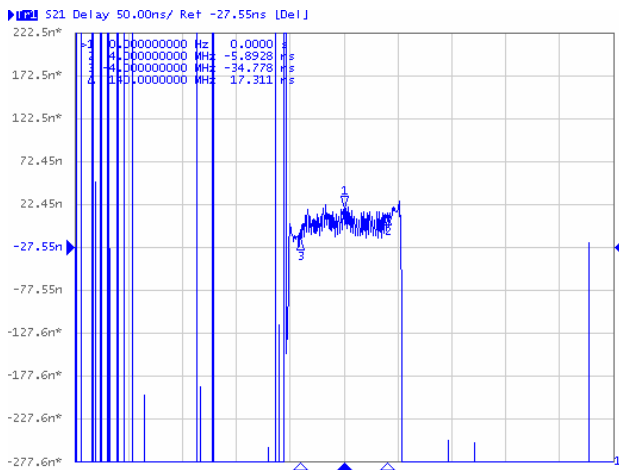
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Typical Performance

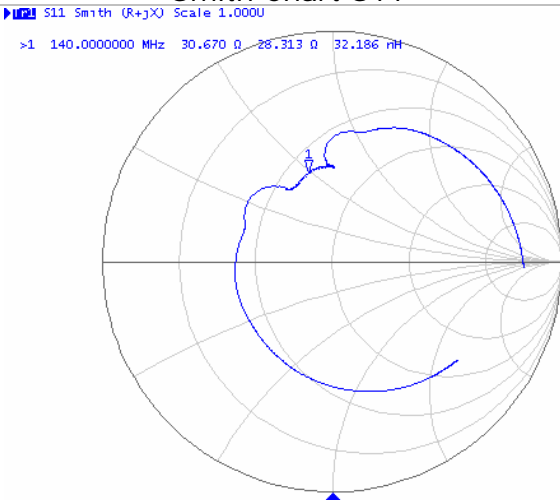
Frequency Respod



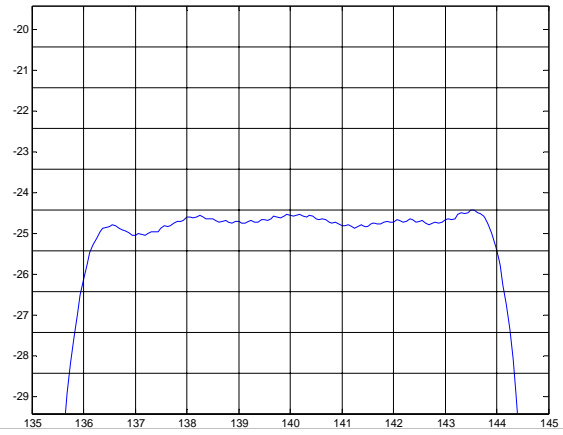
Group Delay Variation($f_0 \pm 4\text{MHz}$)



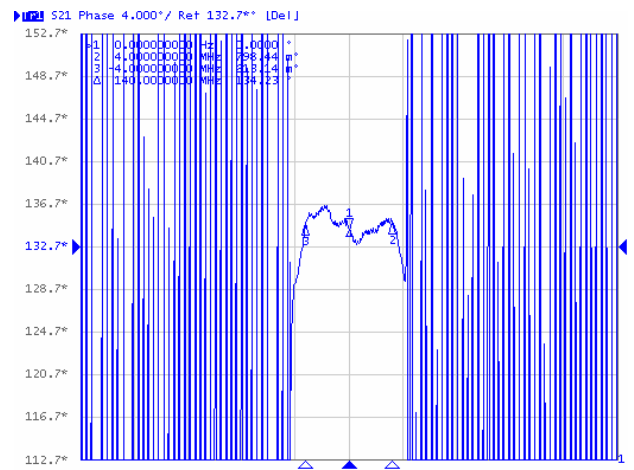
Smith Chart S11



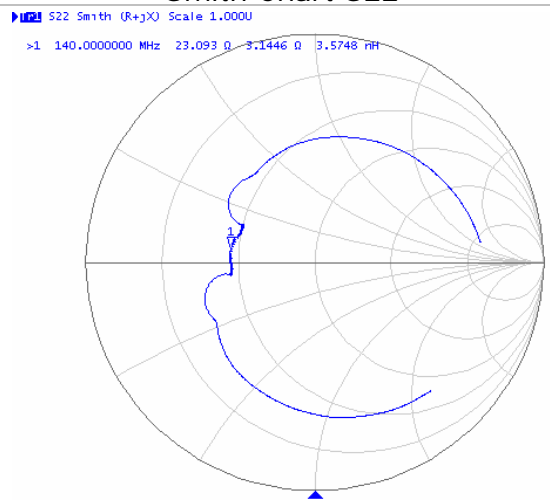
Passband Respod



Phase Linearity($f_0 \pm 4\text{MHz}$)



Smith Chart S22



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