

**AEC-Q101 Qualified** 

# **4V Drive Pch MOSFET**

## RSD080P05FRA

### Structure

Silicon P-channel MOSFET

### Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Drive circuits can be simple.
- 4) Parallel use is easy.

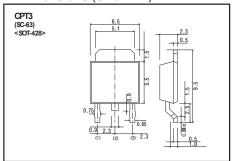
## Application

Switching

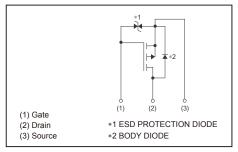
## Packaging specifications

	Package	Taping
Type	Code	TL
	Basic ordering unit (pieces)	2500
RSD080P05	0	

## Dimensions (Unit : mm)



### • Inner circuit



## ● Absolute maximum ratings (Ta = 25°C)

Parame	Symbol	Limits	Unit	
Drain-source voltage	$V_{DSS}$	-45	V	
Gate-source voltage		$V_{GSS}$	±20	V
Drain current	Continuous	I <sub>D</sub>	±8.0	Α
Diam current	Pulsed	I <sub>DP</sub> *1	±16	Α
Source current	Continuous	I <sub>S</sub>	-8.0	Α
(Body Diode)	Pulsed	I <sub>SP</sub> *1	-16	Α
Power dissipation	P <sub>D</sub> *2	15	W	
Channel temperature	Tch	150	°C	
Range of storage temp	Tstg	-55 to +150	°C	

<sup>\*1</sup> Pw≦10μs, Duty cycle≦1%

## Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to Case	R <sub>th (ch-c)</sub> *	8.33	°C / W

<sup>\*</sup> T<sub>c</sub>=25°C

<sup>\*2</sup> T<sub>c</sub>=25°C

## ● Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	$I_{GSS}$	•	-	±10	μA	$V_{GS}=\pm20V$ , $V_{DS}=0V$
Drain-source breakdown voltage	$V_{(BR)DSS}$	<b>-45</b>	-	-	V	$I_D=-1$ mA, $V_{GS}=0$ V
Zero gate voltage drain current	I <sub>DSS</sub>	1	-	-1	μA	$V_{DS}$ =-45V, $V_{GS}$ =0V
Gate threshold voltage	V <sub>GS (th)</sub>	-1.0	-	-3.0	V	$V_{DS}$ =-10V, $I_{D}$ =-1mA
Static ducin course on state	*	1	65	91		$I_D = -8.0A, V_{GS} = -10V$
Static drain-source on-state resistance	R <sub>DS (on)</sub>	1	95	133	mΩ	$I_D = -8.0A, V_{GS} = -4.5V$
		1	105	147		$I_D = -8.0A, V_{GS} = -4.0V$
Forward transfer admittance	IY <sub>fs</sub> I*	6.0	-	-	S	$I_D = -8.0A, V_{DS} = -10V$
Input capacitance	C <sub>iss</sub>	1	1000	-	pF	V <sub>DS</sub> =-10V
Output capacitance	C <sub>oss</sub>	1	160	-	pF	V <sub>GS</sub> =0V
Reverse transfer capacitance	C <sub>rss</sub>	1	80	-	pF	f=1MHz
Turn-on delay time	t <sub>d(on)</sub> *	•	12	-	ns	I <sub>D</sub> =-4.0A, V <sub>DD</sub> ≒-25V
Rise time	t <sub>r</sub> *	1	15	-	ns	V <sub>GS</sub> =-10V
Turn-off delay time	t <sub>d(off)</sub> *	•	50	-	ns	$R_L=6.25\Omega$
Fall time	t <sub>f</sub> *	•	20	-	ns	$R_G=10\Omega$
Total gate charge	Q <sub>g</sub> *	-	9.0	-	nC	V <sub>DD</sub> ≒ –25V
Gate-source charge	Q <sub>gs</sub> *	-	4.0	-	nC	I <sub>D</sub> =-8.0A,
Gate-drain charge	Q <sub>gd</sub> *	-	3.0	-	nC	V <sub>GS</sub> =-5V

<sup>\*</sup>Pulsed

## ●Body diode characteristics (Source-Drain) (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward Voltage	V <sub>SD</sub> *	-	-	-1.2	V	$I_s = -8.0A, V_{GS} = 0V$

<sup>\*</sup>Pulsed

### ●Electrical characteristic curves (Ta=25°C)

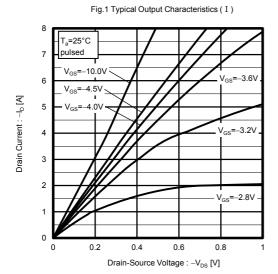


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current

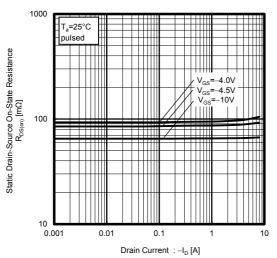


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current

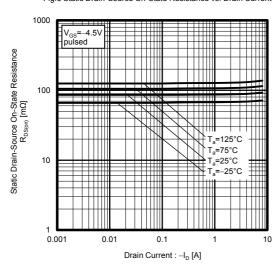


Fig.2 Typical Output Characteristics ( II )

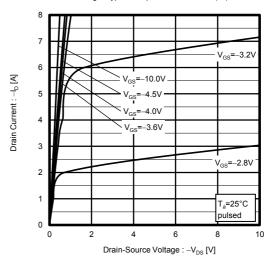


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current

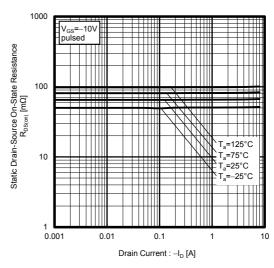


Fig.6 Static Drain-Source On-State Resistance vs. Drain Current

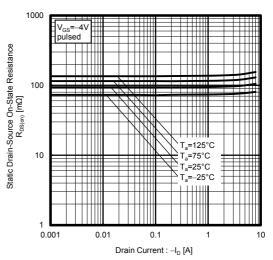


Fig.7 Forward Transfer Admittance vs. Drain Current

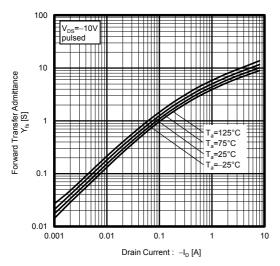


Fig.9 Source Current vs. Source-Drain Voltage

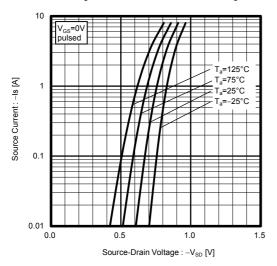


Fig.11 Switching Characteristics

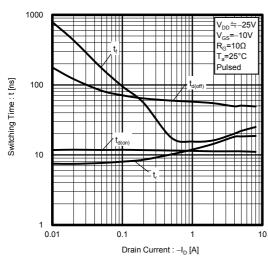


Fig.8 Typical Transfer Characteristics

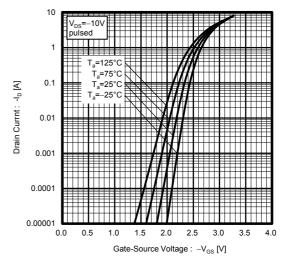


Fig.10 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

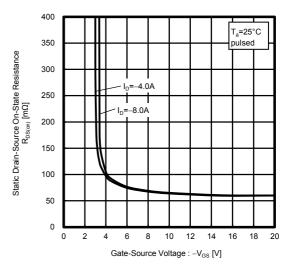


Fig.12 Dynamic Input Characteristics

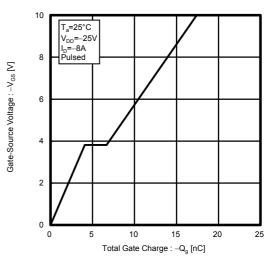


Fig.13 Typical Capacitance vs. Drain-Source Voltage

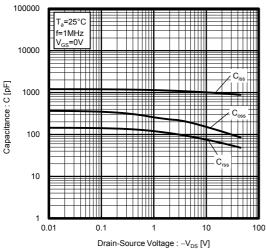
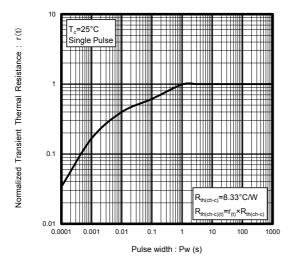


Fig.15 Normalized Transient Thermal Resistance v.s. Pulse Width



Operation in this area is limited by  $R_{DS(on)}$  ( $V_{GS} = -10V$ )  $V_{GS} = -10V$   $V_{$ 

Fig.14 Maximum Safe Operating Area

## Measurement circuits

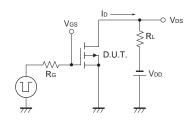


Fig.1-1 Switching Time Measurement Circuit

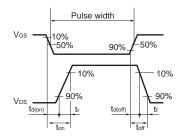


Fig.1-2 Switching Waveforms

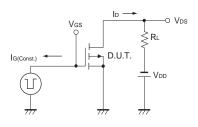


Fig.2-1 Gate Charge Measurement Circuit

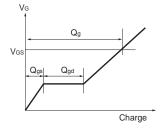


Fig.2-2 Gate Charge Waveform

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Ì	JÁPAN	USA	EU	CHINA
Γ	CLASSⅢ	CLACCIII	CLASS II b	CI VCCIII
Γ	CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
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- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
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# RSD080P05FRA - Web Page

Part Number	RSD080P05FRA
Package	CPT3
Unit Quantity	2500
Minimum Package Quantity	2500
Packing Type	Taping
Constitution Materials List	inquiry
RoHS	Yes