# N-Channel 70-V (D-S) MOSFET

### **Key Features:**

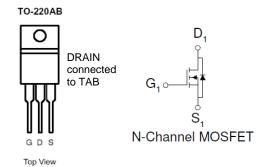
- Low r<sub>DS(on)</sub> trench technology
- · Low thermal impedance
- · Fast switching speed

## **Typical Applications:**

- · White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	$r_{DS(on)}(m\Omega)$	I <sub>D</sub> (A)	
75	$6.3 @ V_{GS} = 10V$	130 <sup>a</sup>	
75	$7.5 @ V_{GS} = 4.5V$	130	





ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^{\circ}$ C UNLESS OTHERWISE NOTED)						
Parameter		Symbol	Limit	Units		
Drain-Source Voltage			75	V		
Gate-Source Voltage		$V_{GS}$	±20	V		
Continuous Drain Current a	T <sub>C</sub> =25°C	I <sub>D</sub>	130	Α		
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	520			
Continuous Source Current (Diode Conduction) <sup>a</sup> T <sub>C</sub> =25°C		I <sub>S</sub>	130	Α		
Power Dissipation <sup>a</sup>	T <sub>C</sub> =25°C	$P_{D}$	300	W		
Operating Junction and Storage Temperature Range	· ·	$T_J, T_{stg}$	-55 to 175	°C		

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Maximum	Units
Maximum Junction-to-Ambient °	$R_{\theta JA}$	62.5	°C/W
Maximum Junction-to-Case	$R_{\theta JC}$	0.5	C/VV

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#### Notes

- a. Package Limited
- b. Pulse width limited by maximum junction temperature
- c. Surface Mounted on 1" x 1" FR4 Board.

### **Electrical Characteristics**

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit		
Static								
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 250 \text{ uA}$	1			V		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA		
Zero Gate Voltage Drain Current	1	$V_{DS} = 56 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA		
	I <sub>DSS</sub>	$V_{DS} = 56 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			10			
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	50			Α		
Drain-Source On-Resistance <sup>a</sup>	r	$V_{GS} = 10 \text{ V}, I_D = 45 \text{ A}$			6.3	mΩ		
	r <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 40 \text{ A}$			7.5			
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_{D} = 20 \text{ A}$		17		S		
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_{S} = 65 \text{ A}, V_{GS} = 0 \text{ V}$		0.93		V		
		Dynamic <sup>b</sup>						
Total Gate Charge	$Q_g$	$V_{DS} = 37.5 \text{ V}, V_{GS} = 4.5 \text{ V},$ $I_{D} = 20 \text{ A}$		61		nC		
Gate-Source Charge	$Q_{gs}$			17				
Gate-Drain Charge	$Q_gd$			22				
Turn-On Delay Time	t <sub>d(on)</sub>	V - 275 V B = 100		19		ns		
Rise Time	t <sub>r</sub>	$V_{DS} = 37.5 \text{ V}, R_{L} = 1.9 \Omega,$ $I_{D} = 20 \text{ A},$ $V_{GEN} = 10 \text{ V}, R_{GEN} = 6 \Omega$		16				
Turn-Off Delay Time	$t_{d(off)}$			162				
Fall Time	t <sub>f</sub>			47				
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V, f = 1 Mhz		7001		pF		
Output Capacitance	C <sub>oss</sub>			398				
Reverse Transfer Capacitance	$C_{rss}$		_	339				

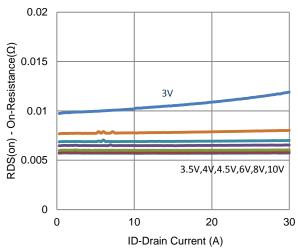
#### **Notes**

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

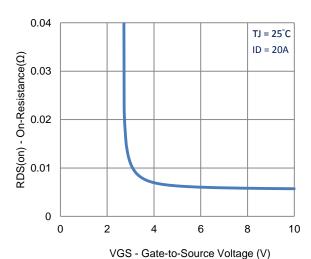
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## **Typical Electrical Characteristics**

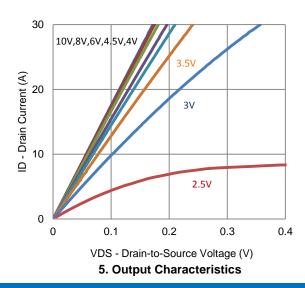
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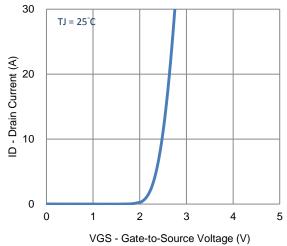


#### 1. On-Resistance vs. Drain Current

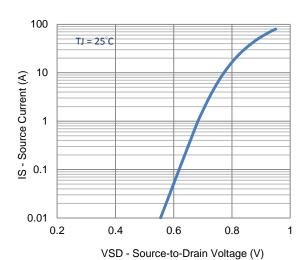


3. On-Resistance vs. Gate-to-Source Voltage

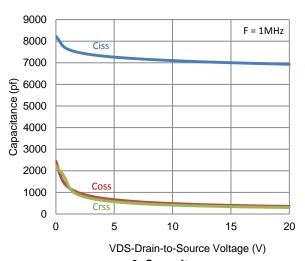




2. Transfer Characteristics

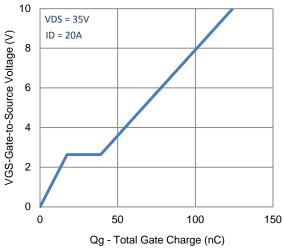


4. Drain-to-Source Forward Voltage



6. Capacitance

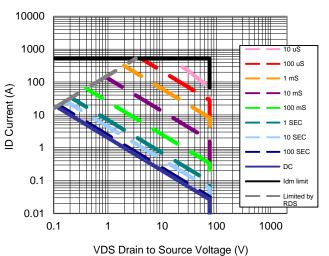
## **Typical Electrical Characteristics**



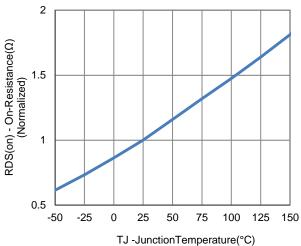
Total Gate Charge (nC) TJ -JunctionTemperature(°C)

7. Gate Charge

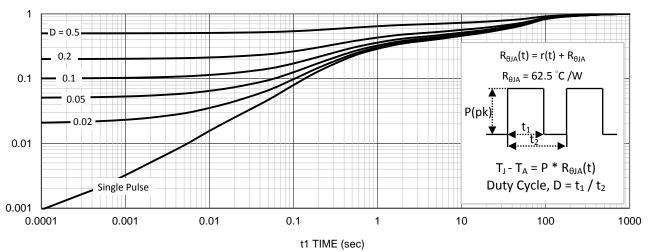
8. Normalized On-Resistance Vs



9. Safe Operating Area 10. Single Pulse Maximum Power Dissipation

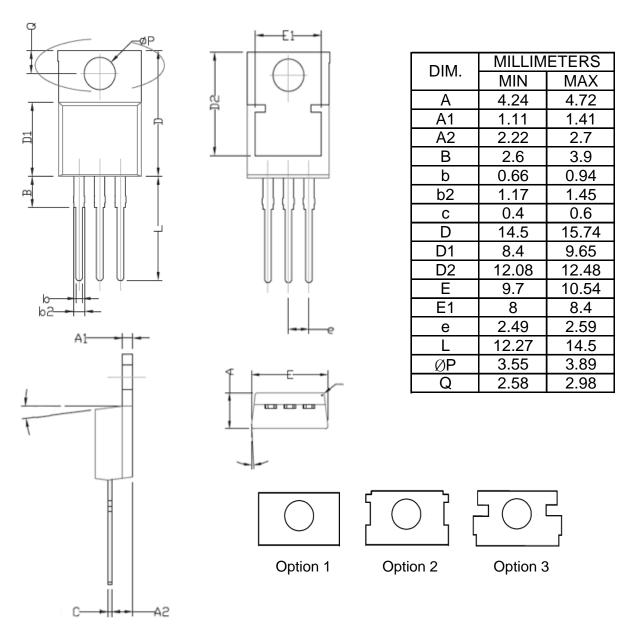


Too (%) 600 (%) 500 (%) 400 (%) 300 (%) 300 (%) 400 (%



11. Normalized Thermal Transient Junction to Ambient

### **Package Information**



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