

### 650V 25A Field Stop Trench IGBT

V <sub>CES</sub>	650V
I <sub>C(100°C)</sub>	16A
V <sub>CE(sat) (Typ.)</sub>	1.6V@I <sub>C</sub> =25A
$P_D$	59W

#### Features

- 1) Low Collector Emitter Saturation Voltage
- 2) High Speed Switching
- 3) Low Switching Loss & Soft Switching
- 4) Built in Very Fast & Soft Recovery FRD (RFN - Series)
- 5) Pb free Lead Plating; RoHS Compliant

#### Applications

**PFC** 

**UPS** 

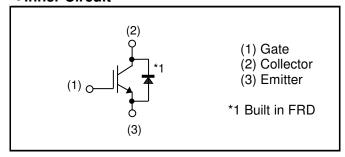
**Power Conditioner** 

ΙH

#### Outline



#### ●Inner Circuit



Packaging Specifications

	Packaging	Tube
	Reel Size (mm)	-
Tuno	Tape Width (mm)	-
Type	Basic Ordering Unit (pcs)	450
	Packing Code	C11
	Marking	RGTH50TK65D

## ● Absolute Maximum Ratings (at T<sub>C</sub> = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V <sub>CES</sub>	650	V
Gate - Emitter Voltage		V <sub>GES</sub>	±30	V
	T <sub>C</sub> = 25°C	I <sub>C</sub>	26	А
Collector Current	T <sub>C</sub> = 100°C	I <sub>C</sub>	16	А
Pulsed Collector Current		I <sub>CP</sub> *1	100	А
Diode Forward Current	T <sub>C</sub> = 25°C	l <sub>F</sub>	26	А
	T <sub>C</sub> = 100°C	l <sub>F</sub>	15	А
Diode Pulsed Forward Current		I <sub>FP</sub> *1	100	А
Power Dissipation	$T_C = 25^{\circ}C$	P <sub>D</sub>	59	W
	T <sub>C</sub> = 100°C	P <sub>D</sub>	29	W
Operating Junction Temperature		T <sub>j</sub>	-40 to +175	°C
Storage Temperature		T <sub>stg</sub>	-55 to +175	°C
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<sup>\*1</sup> Pulse width limited by T<sub>jmax</sub>.

#### ●Thermal Resistance

Darameter	Symbol	Values			Unit
Parameter		Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	2.51	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j-c)}$	-	-	3.93	°C/W

# ullet IGBT Electrical Characteristics (at $T_j = 25$ °C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
i arameter	Syllibol	Conditions	Min.	Тур.	Max.	Offic
Collector - Emitter Breakdown Voltage	BV <sub>CES</sub>	$I_{C} = 10 \mu A, V_{GE} = 0 V$	650	-	1	V
Collector Cut - off Current	I <sub>CES</sub>	$V_{CE} = 650V, V_{GE} = 0V$	ı	ı	10	μΑ
Gate - Emitter Leakage Current	I <sub>GES</sub>	$V_{GE} = \pm 30V, V_{CE} = 0V$	1	-	±200	nA
Gate - Emitter Threshold Voltage	$V_{\text{GE(th)}}$	$V_{CE} = 5V, I_{C} = 17.5 \text{mA}$	4.5	5.5	6.5	V
Collector - Emitter Saturation Voltage	V <sub>CE(sat)</sub>	$I_C = 25A, V_{GE} = 15V$ $T_j = 25^{\circ}C$ $T_j = 175^{\circ}C$	-	1.6 2.1	2.1 -	V

# ●IGBT Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Conditions		Unit		
Parameter			Min.	Тур.	Max.	Offic
Input Capacitance	C <sub>ies</sub>	V <sub>CE</sub> = 30V	-	1410	-	
Output Capacitance	C <sub>oes</sub>	$V_{GE} = 0V$	-	57	-	рF
Reverse Transfer Capacitance	C <sub>res</sub>	f = 1MHz	-	22	-	
Total Gate Charge	$Q_g$	V <sub>CE</sub> = 300V	-	49	-	
Gate - Emitter Charge	$Q_{ge}$	I <sub>C</sub> = 25A	-	15	-	nC
Gate - Collector Charge	$Q_{gc}$	V <sub>GE</sub> = 15V	-	19	-	
Turn - on Delay Time	t <sub>d(on)</sub>	$I_C = 25A, V_{CC} = 400V$	-	27	-	
Rise Time	t <sub>r</sub>	$V_{GE} = 15V, R_G = 10\Omega$	-	38	-	
Turn - off Delay Time	t <sub>d(off)</sub>	T <sub>j</sub> = 25°C	-	94	-	ns
Fall Time	t <sub>f</sub>	Inductive Load	-	50	-	
Turn - on Delay Time	t <sub>d(on)</sub>	$I_C = 25A, V_{CC} = 400V$	-	27	-	
Rise Time	t <sub>r</sub>	$V_{GE} = 15V, R_{G} = 10\Omega$	-	38	-	20
Turn - off Delay Time	t <sub>d(off)</sub>	T <sub>j</sub> = 175°C	-	107	-	ns
Fall Time	t <sub>f</sub>	Inductive Load	-	65	-	
		$I_C = 100A, V_{CC} = 520V$				
Reverse Bias Safe Operating Area	RBSOA	$V_P = 650 V, V_{GE} = 15 V$	FU	LL SQUA	RE	-
		$R_G = 60\Omega, T_j = 175^{\circ}C$				

# **•FRD Electrical Characteristics** (at $T_j = 25$ °C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Lloit
			Min.	Тур.	Max.	Unit
Diode Forward Voltage	$V_{F}$	$I_F = 20A$ $T_j = 25$ °C	-	1.45	1.9	V
		T <sub>j</sub> = 175°C	-	1.25	-	
Diode Reverse Recovery Time	t <sub>rr</sub>	$I_F = 20A$ $V_{CC} = 400V$ $di_F/dt = 200A/\mu s$ $T_j = 25^{\circ}C$	-	58	-	ns
Diode Peak Reverse Recovery Current	l <sub>rr</sub>		-	6.3	-	А
Diode Reverse Recovery Charge	$Q_{rr}$		-	0.20	-	μC
Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 20A	-	256	-	ns
Diode Peak Reverse Recovery Current	l <sub>rr</sub>	$V_{CC} = 400V$ $di_F/dt = 200A/\mu s$ $T_j = 175^{\circ}C$	-	10.4	-	Α
Diode Reverse Recovery Charge	$Q_{rr}$		-	1.35	-	μC

Fig.1 Power Dissipation vs. Case Temperature

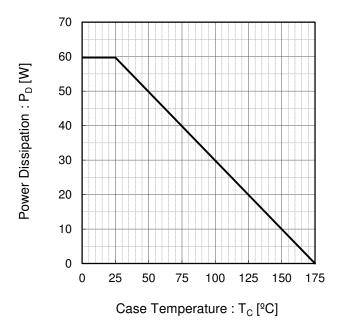
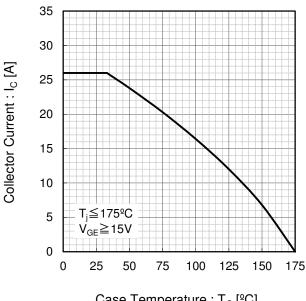
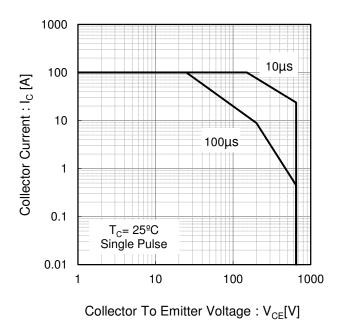


Fig.2 Collector Current vs. Case Temperature



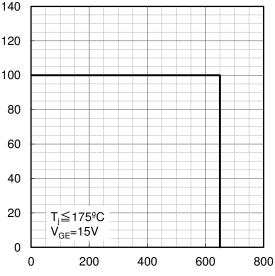
Case Temperature :  $T_C$  [ ${}^{\circ}C$ ]

Fig.3 Forward Bias Safe Operating Area



Collector Current : I<sub>C</sub> [A]

Fig.4 Reverse Bias Safe Operating Area



Collector To Emitter Voltage :  $V_{CE}[V]$ 

Fig.5 Typical Output Characteristics

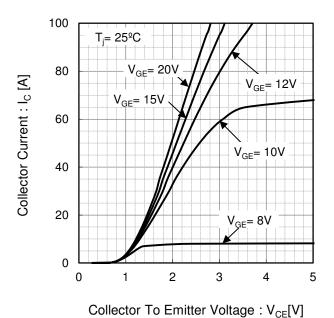
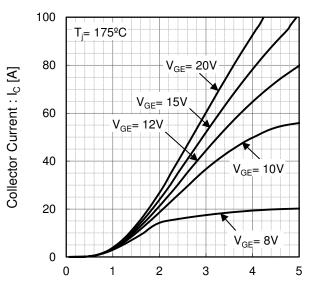


Fig.6 Typical Output Characteristics



Collector To Emitter Voltage: V<sub>CE</sub>[V]

Fig.7 Typical Transfer Characteristics

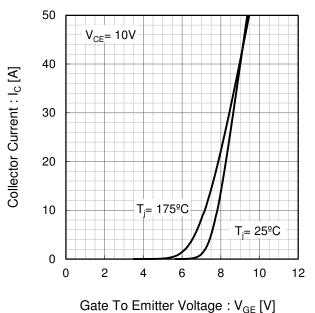


Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature

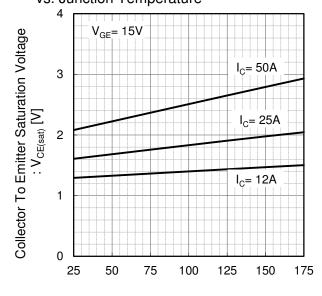
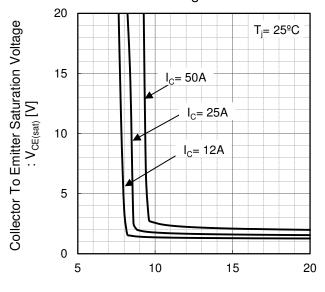
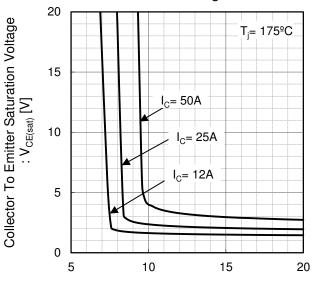


Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage



Gate To Emitter Voltage: V<sub>GE</sub> [V]

Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage



Gate To Emitter Voltage: V<sub>GE</sub> [V]

Fig.11 Typical Switching Time vs. Collector Current

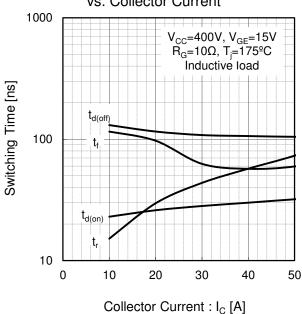
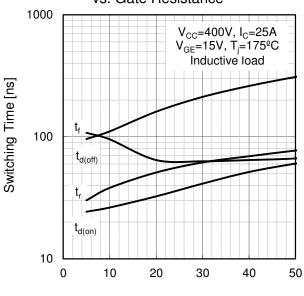


Fig.12 Typical Switching Time vs. Gate Resistance



Gate Resistance :  $R_G[\Omega]$ 

Fig.13 Typical Switching Energy Losses vs. Collector Current 10 Switching Energy Losses [mJ] 1  $E_{off}$ 0.1  $V_{CC}$ =400V,  $V_{GE}$ =15V  $R_{G}$ =10 $\Omega$ ,  $T_{j}$ =175 $^{\circ}$ C Inductive load 0.01 0 10 30 40 20 50 Collector Current : I<sub>C</sub> [A]

vs. Gate Resistance 10 Switching Energy Losses [mJ] E<sub>off</sub> 1 0.1  $V_{CC}$ =400V,  $I_{C}$ =25A  $V_{GE}$ =15V,  $T_{j}$ =175°C Inductive load 0.01 0 10 20 30 40 50 Gate Resistance :  $R_G[\Omega]$ 

Fig.14 Typical Switching Energy Losses

Fig.15 Typical Capacitance vs. Collector To Emitter Voltage 10000 Cies 1000 Capacitance [pF] Coes 100 Cres 10 f=1MHz $V_{GE}=0V$   $T_i=25$  C0.01 0.1 10 100

Collector To Emitter Voltage: V<sub>CE</sub>[V]

Fig.16 Typical Gate Charge

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Fig.17 Typical Diode Forward Current vs. Forward Voltage 100 80 Forward Current : I<sub>F</sub> [A] 60 40 T<sub>i</sub>= 175ºC 20 T<sub>i</sub>= 25ºC 0 0 0.5 1.5 2 2.5 3

Fig.18 Typical Diode Reverse Recovery Time vs. Forward Current 400 V<sub>CC</sub>=400V di<sub>F</sub>/dt=200A/μs Reverse Recovery Time: t<sub>rr</sub> [ns] Inductive load 300 T<sub>i</sub>= 175ºC 200 100 T<sub>i</sub>= 25ºC 0 20 30 0 10 40 50 Forward Current : I<sub>F</sub> [A]

Fig.19 Typical Diode Reverse Recovery Current vs. Forward Current

Forward Voltage: V<sub>F</sub>[V]

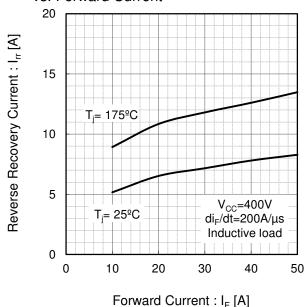


Fig.20 Typical Diode Reverse Recovery Charge vs. Forward Current

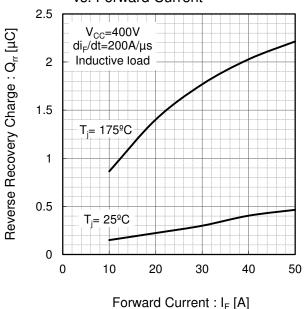


Fig.21 IGBT Transient Thermal Impedance

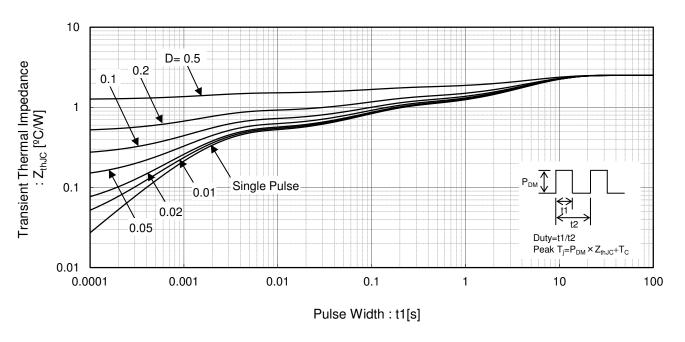
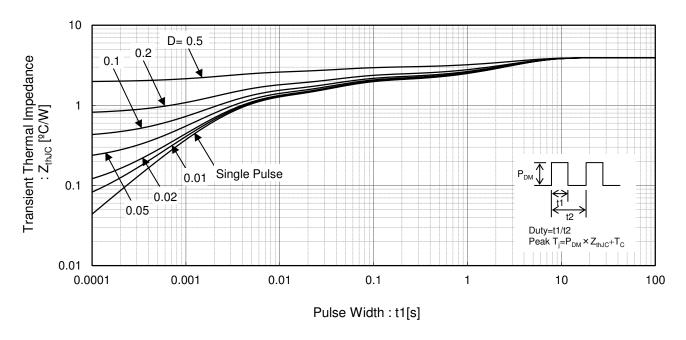


Fig.22 Diode Transient Thermal Impedance



## ●Inductive Load Switching Circuit and Waveform

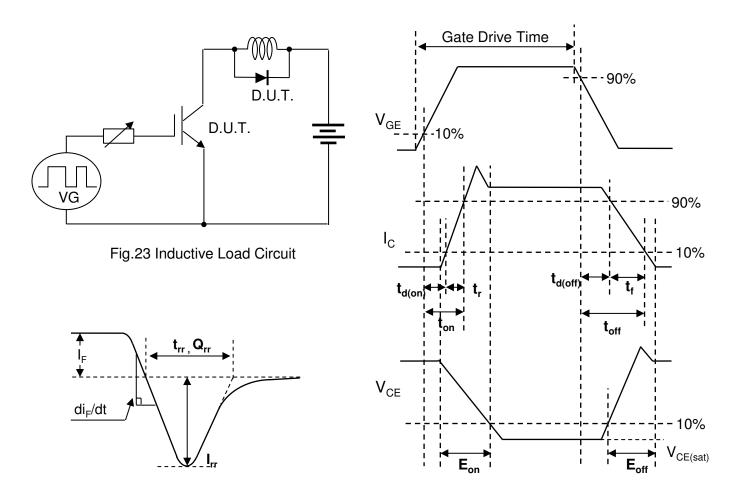


Fig.25 Diode Reverce Recovery Waveform

Fig.24 Inductive Load Waveform

#### Notes

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