# **High Performance Schottky Rectifier, 175 A**

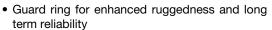


Po	wer	Tak	®

PRODUCT SUMMARY				
Package	PowerTab <sup>®</sup>			
I <sub>F(AV)</sub>	175 A			
$V_{R}$	30 V			
V <sub>F</sub> at I <sub>F</sub>	0.52 V			
I <sub>RM</sub>	650 mA at 125 °C			
T <sub>J</sub> max.	125 °C			
Diode variation	Single die			
E <sub>AS</sub>	80 mJ			

### **FEATURES**

- 150 °C max. operating junction temperature
- High frequency operation
- Ultralow forward voltage drop
- Continuous high current operation





COMPLIANT

- · Screw mounting only
- Designed and qualified according to JEDEC®-JESD 47
- PowerTab<sup>®</sup> package
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

### **DESCRIPTION**

The VS-175BGQ030 Schottky rectifier has been optimized for ultralow forward voltage drop specifically for low voltage output in high current AC/DC power supplies.

The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
	Rectangular waveform	175	A	
I <sub>F(AV)</sub>	T <sub>C</sub>	112	°C	
V <sub>RRM</sub>		30	V	
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	7400	Α	
V <sub>F</sub>	175 A <sub>pk</sub> (typical)	0.47	V	
	$T_J$	150	°C	
T <sub>J</sub>	Range	-55 to +150	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-175BGQ030	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	30	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>			

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 112 °C, rectangular waveform		175	А
Maximum peak one cycle non-repetitive surge current		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V <sub>RRM</sub> applied	7400	А
	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse		1400	
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 12 A, L = 1.12 mH		80	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by $T_J$ maximum $V_A = 1.5 \times V_R$ typical		12	А



ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS		MAX.	UNITS
		100 A	- T <sub>J</sub> = 25 °C -	0.47	0.49	- V
Forward voltage drap	V <sub>FM</sub> <sup>(1)</sup>	175 A		0.55	0.59	
Forward voltage drop	VFM (1)	100 A	T 150 °C	0.36	0.39	
		175 A	- T <sub>J</sub> = 150 °C	0.47	0.52	
Reverse leakage current		T <sub>J</sub> = 125 °C, V <sub>R</sub> = 15 V		160	220	
	I <sub>RM</sub> <sup>(1)</sup>	$T_J = 150 ^{\circ}\text{C},  V_R = 30 ^{\circ}\text{V}$		1400	2000	] <sub>m^</sub>
	IRM (''	T <sub>J</sub> = 25 °C	V Dated V	1.3	4.5	mA
	•	T <sub>J</sub> = 125 °C	V <sub>R</sub> = Rated V <sub>R</sub>	450	650	
Maximum junction capacitance	C <sub>T</sub>	$V_R = 5 V_{DC}$ , (test signal range 100 kHz to 1 MHz), 25 °C		85	00	pF
Typical series inductance	L <sub>S</sub>	Measured from tab to mounting plane 3.5 nh		nH		
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000 V/µs		V/µs		

### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and temperature range	storage	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +150	°C	
Maximum thermal resis	tance,	$R_{thJC}$	DC operation	0.25	°C/W	
Typical thermal resistar case to heatsink	nce,	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.20	C/VV	
Approximate weight				5	g	
Approximate weight				0.18	oz.	
Mounting torque —	minimum			1.2 (10)	N·m	
	maximum			2.4 (20)	(lbf $\cdot$ in)	
Marking device Case style PowerTab® 175BG		Q045				

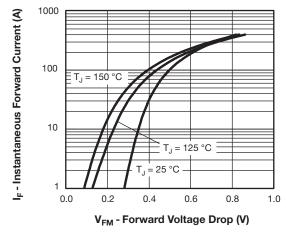


Fig. 1 - Maximum Forward Voltage Drop Characteristics

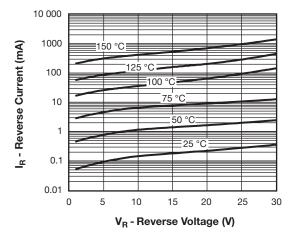


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



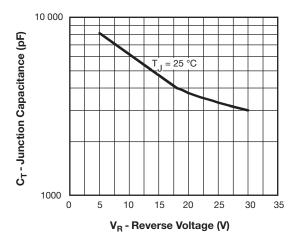


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

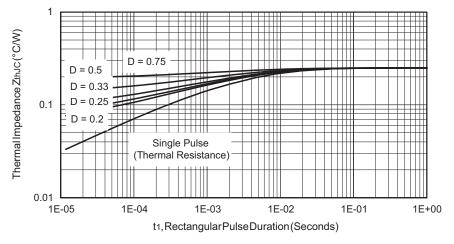


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

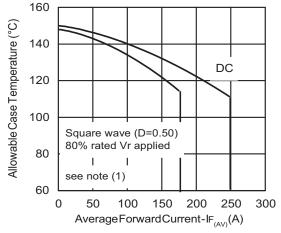


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

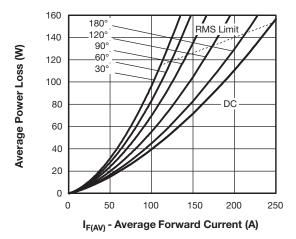
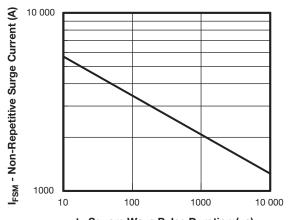


Fig. 6 - Forward Power Loss Characteristics



t - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current

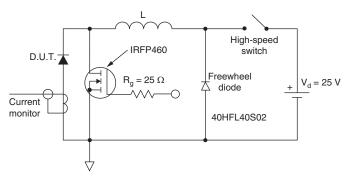


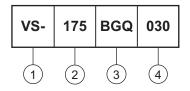
Fig. 8 - Unclamped Inductive Test Circuit

#### Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 80 \text{ \% rated } V_R \\ \end{array}$ 

### **ORDERING INFORMATION TABLE**

Device code



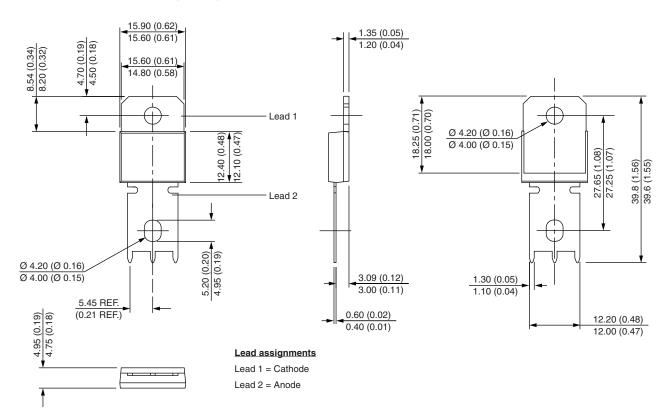
- 1 Vishay Semiconductors product
- 2 Current rating
- 3 Essential part number
- 4 Voltage code = V<sub>RRM</sub>

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95240</u>				
Part marking information	www.vishay.com/doc?95370			
SPICE model	www.vishay.com/doc?95427			
Application note	www.vishay.com/doc?95179			



## PowerTab<sup>®</sup>

### **DIMENSIONS** in millimeters (inches)





## **Legal Disclaimer Notice**

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