UNISONIC TECHNOLOGIES CO., LTD

DTC123J

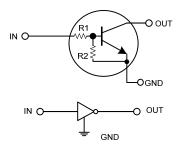
NPN SILICON TRANSISTOR

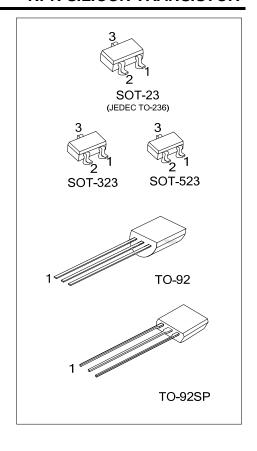
NPN DIGITAL TRANSISTOR (BUILT-IN RESISTORS)

FEATURES

- * Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- * The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- * Only the on/off conditions need to be set for operation, making device design easy.

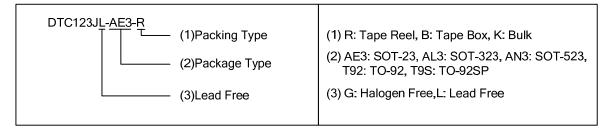
EQUIVALENT CIRCUIT



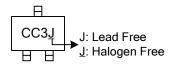


ORDERING INFORMATION

Ordering Number		Dookaga	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
DTC123JL-AE3-R	DTC123JG-AE3-R	SOT-23	G	ı	0	Tape Reel	
DTC123JL-AL3-R	DTC123JG-AL3-R	SOT-323	G	- 1	0	Tape Reel	
DTC123JL-AN3-R	DTC123JG-AN3-R	SOT-523	G	- 1	0	Tape Reel	
DTC123JL-T92-B	DTC123JG-T92-B	TO-92	G	0		Tape Box	
DTC123JL-T92-K	DTC123JG-T92-K	TO-92	G	0	I	Bulk	
DTC123JL-T92-R	DTC123JG-T92-R	TO-92	G	0	Ī	Tape Reel	
DTC123JL-T9S-K	DTC123JG-T9S-K	TO-92SP	G	0	Ī	Bulk	



MARKING (FOR SOT-23/SOT-323/SOR-523 PACKAGE)



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■ **ABSOLUTE MAXIMUM RATINGS** (T_A=25°C, unless otherwise specified)

PARAMETER		SYMBOL RATINGS		UNIT	
Supply Voltage		V _{CC}	50	V	
Input Voltage		V _{IN}	-5 ~ +1 2	V	
Output Current		Io	100		
		I _{C(MAX.)}	100 mA		
Power Dissipation	SOT-23/ SOT-323		200		
	SOT-523		150		
	TO-92	P _D	625	mW	
	TO-92SP		550	1	
Junction Temperature		TJ	150	°C	
Storage Temperature		T _{STG}	-55 ~ + 150	°C	

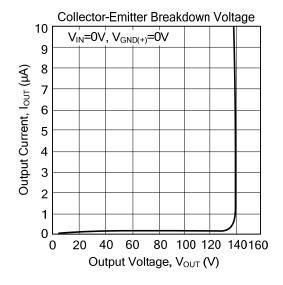
Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

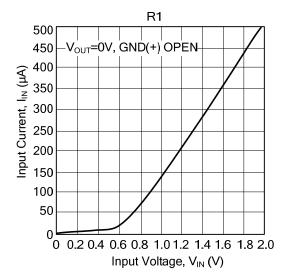
■ ELECTRICAL CHARACTERISTICS (T_A =25°C)

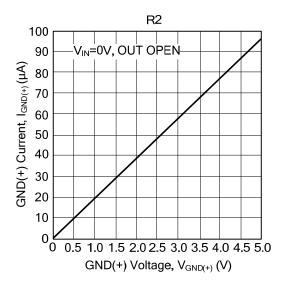
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Innut Voltage	$V_{I (OFF)}$	V _{CC} =5V, I _O =100μA			0.5	V
Input Voltage	$V_{I(ON)}$	$V_O=0.3V$, $I_O=5mA$	1.1			V
Output Voltage	$V_{O(ON)}$	I _O /I _I =5mA/0.25mA		0.1	0.3	V
Input Current	lı	V ₁ =5V			3.6	mA
Output Current	I _{O(OFF)}	V_{CC} =50V, V_{I} =0V			0.5	μΑ
DC Current Gain	Gı	V _O =5V, I _O =10mA	80			
Input Resistance	R ₁		1.54	2.2	2.86	ΚΩ
Resistance Ratio	R_2/R_1	_	17	21	26	
Transition Frequency	f_T	V_{CE} =10V, I_E =-5mA, f=100MHz (Note)		250		MHz

Note: Transition frequency of the device

■ TYPICAL CHARACTERISTICS







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