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Bulletin Date: 4/29/2016		Bulletin Effective Date: 4/29/2016	
Title: Si5315x, Si5214x Data Sheet Update			
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Bulletin Details			
Description: The test condition for Clock Stabilization from Power-up (Tstable) parameter is added for measurement clarification to the Data Sheet. Condition for Tstable is "Measured from the point when both VDD and clock input are valid". In Si52144 Data Sheet, the OEx internal resistor in pin description is updated from pull-down to pull-up. In Si52142, Si52143, Si52144, Si53154 Data Sheets, theta JC is updated to 25°C/W. In Si52142, Si52143, Si52144, Si52146, Si52147 Data Sheets, package land pattern is added.			
Reason: Silicon Labs is issuing this notification to inform customers that the test condition for Clock Stabilization from Power-up (Tstable) parameter is added to the Data Sheets of affected part numbers in Product Identification section below. Si52144 Data Sheet has an additional update to correct the OEx internal resistor in pin description. In Si52142, Si52143, Si52144, Si53154 Data Sheets, the theta JC is updated to 25°C/W. In Si52142, Si52143, Si52144, Si52146, Si52147 Data Sheets, package land pattern is added.			
Product Identification: Si53152-A01AGM Si53152-A01AGMR Si53154-A01AGM Si53154-A01AGMR Si53156-A01AGM Si53156-A01AGMR Si53159-A01AGM Si53159-A01AGMR Si52142-A01AGM Si52142-A01AGMR Si52143-A01AGM Si52143-A01AGMR Si52144-A01AGM Si52144-A01AGMR Si52146-A01AGM Si52146-A01AGMR Si52147-A01AGM Si52147-A01AGMR			



Bulletin #1604293

This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.

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Customer Actions Needed:

No