

NPN SILICON PLANAR TRANSISTOR

2N6717

TO-237 Plastic Package



Designed for General Purpose Medium Power Amplifier and Switching Circuits

ABSOLUTE MAXIMUM RATINGS(Ta=25°C unless specified otherwise)

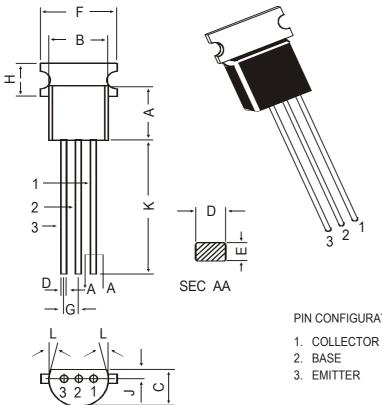
DESCRIPTION	SYMBOL	VALUE	UNIT	
Collector -Base Voltage	V _{CBO}	80	V	
Collector -Emitter Voltage	V _{CEO}	80	V	
Emitter Base Voltage	V _{EBO}	5.0	V	
Collector Current Continuous	I _C	1.5	А	
Power Dissipation @ Ta=25°C	PD	850	mW	
Operating and Storage JunctionTj, TstgTemperature RangeTj, Tstg		-55 to +150	٥C	

ELECTRICAL CHARACTERISTICS (Ta=25°C unless specified otherwise)

SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
I _{CBO}	V _{CB} =60V, I _E =0			100	nA
I _{EBO}	V _{EB} =4V, I _C =0			100	nA
h _{FE}	I _C =50mA,V _{CE} =1V	80			
	I _C =250mA,V _{CE} =1V	50		250	
	I _C =500mA,V _{CE} =1V	20			
V _{CE(sat)}	I _C =250mA,I _B =25mA			0.35	V
f _T	V _{CE} =1V, I _C =200mA,	50		500	MHz
	I _{CBO} I _{EBO} h _{FE} V _{CE(sat)}	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c c c c c c c c c c c c $

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	DIM	MIN.	MAX.	
All diminsions in mm.	А	4.32	5.33	
	В	4.45	5.20	
	С	3.18	4.19	
	D	0.41	0.55	
	Е	0.35	0.50	
	F		5.40	
	G	1.14	1.40	
	Н		2.54	
insic	Κ	12.70	_	
dimi	L	5 DEG		
AI (J	1.14	1.53	

PIN CONFIGURATION

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-237 Bulk	1K/polybag	240 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	26.2 kgs
TO-237 T&A	2K/ammo box	725 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	13.8 kgs

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Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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