

General Description

This planar stripe MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for active power factor correction and switching mode power supplies.

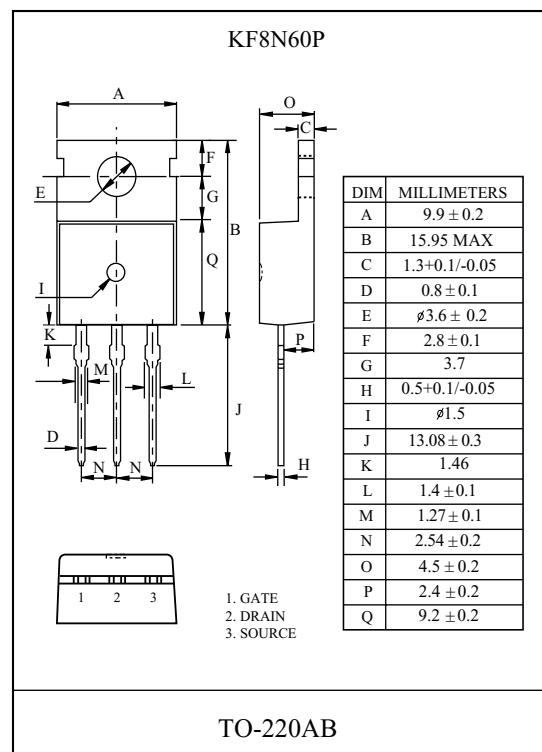
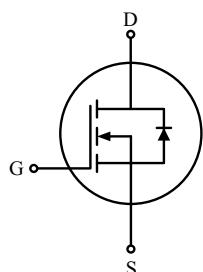
FEATURES

- $V_{DSS}=600V$, $I_D=8A$
- Drain-Source ON Resistance :
 $R_{DS(ON)}(\text{Max})=1.05$ @ $V_{GS}=10V$
- $Q_g(\text{typ.})=22\text{nC}$

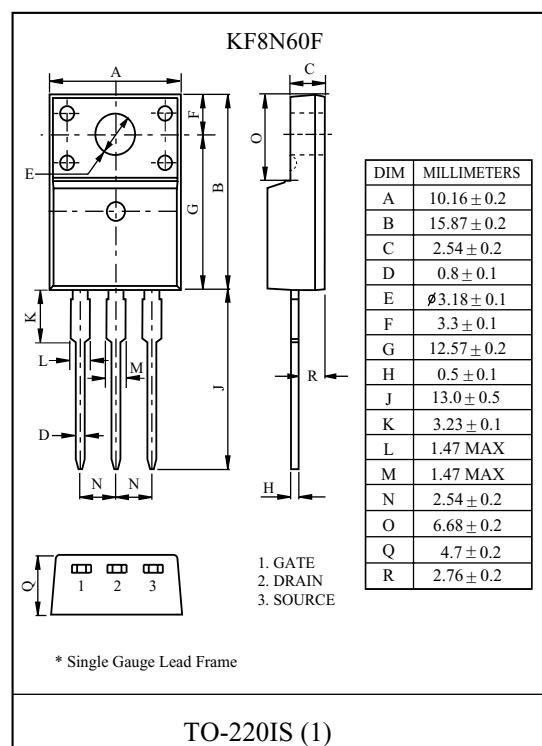
MAXIMUM RATING (T_c=25 °C)

CHARACTERISTIC	SYMBOL	RATING		UNIT
		KF8N60P	KF8N60F	
Drain-Source Voltage	V_{DSS}	600		V
Gate-Source Voltage	V_{GSS}	± 30		V
Drain Current	I_D @ $T_c=25$	8	8*	A
	I_D @ $T_c=100$	5	5*	
	I_{DP} Pulsed (Note 1)	18	18*	
Single Pulsed Avalanche Energy (Note 2)	E_{AS}	230		mJ
Repetitive Avalanche Energy (Note 1)	E_{AR}	14.7		mJ
Peak Diode Recovery dv/dt (Note 3)	dv/dt	4.5		V/ns
Drain Power Dissipation	P_D T _c =25	160	44.6	W
	P_D Derate above 25	1.28	0.36	W/°C
Maximum Junction Temperature	T_j	150		
Storage Temperature Range	T_{stg}	-55 150		
Thermal Characteristics				
Thermal Resistance, Junction-to-Case	R_{thJC}	0.78	2.8	/W
Thermal Resistance, Junction-to-Ambient	R_{thJA}	62.5	62.5	/W

* : Drain current limited by maximum junction temperature.

PIN CONNECTION

TO-220AB



TO-220IS (1)

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ELECTRICAL CHARACTERISTICS (Tc=25 °C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =250 μA, V _{GS} =0V	600	-	-	V
Breakdown Voltage Temperature Coefficient	BV _{DSS} / T _j	I _D =250 μA, Referenced to 25	-	0.65	-	V/°C
Drain Cut-off Current	I _{DSS}	V _{DS} =600V, V _{GS} =0V	-	-	10	μA
Gate Threshold Voltage	V _{th}	V _{DS} =V _{GS} , I _D =250 μA	2.5	-	4.5	V
Gate Leakage Current	I _{GSS}	V _{GS} =±30V, V _{DS} =0V	-	-	± 100	nA
Drain-Source ON Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =4A	-	0.85	1.05	
Dynamic						
Total Gate Charge	Q _g	V _{DS} =480V, I _D =8A V _{GS} =10V (Note 4,5)	-	22	-	nC
Gate-Source Charge	Q _{gs}		-	5	-	
Gate-Drain Charge	Q _{gd}		-	8	-	
Turn-on Delay time	t _{d(on)}	V _{DD} =300V I _D =8A R _G =25 (Note 4,5)	-	45	-	ns
Turn-on Rise time	t _r		-	35	-	
Turn-off Delay time	t _{d(off)}		-	75	-	
Turn-off Fall time	t _f		-	30	-	
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f=1.0MHz	-	1000	-	pF
Output Capacitance	C _{oss}		-	115	-	
Reverse Transfer Capacitance	C _{rss}		-	8	-	
Source-Drain Diode Ratings						
Continuous Source Current	I _S	V _{GS} <V _{th}	-	-	8	A
Pulsed Source Current	I _{SP}		-	-	32	
Diode Forward Voltage	V _{SD}	I _S =8.0A, V _{GS} =0V	-	-	1.4	V
Reverse Recovery Time	t _{rr}	I _S =8.0A, V _{GS} =0V, dI _S /dt=100A/μs	-	400	-	ns
Reverse Recovery Charge	Q _{rr}		-	4.1	-	μC

Note 1) Repetitvity rating : Pulse width limited by junction temperature.

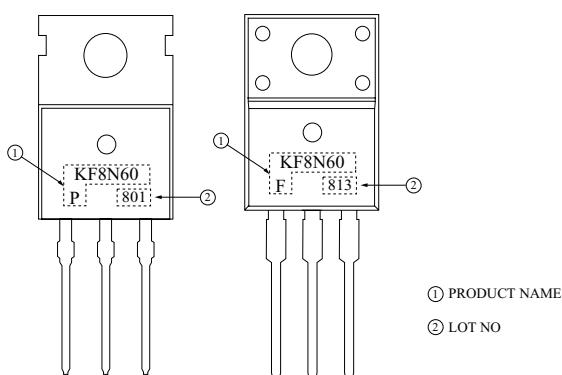
Note 2) L =6.7mH, I_S=8A, V_{DD}=50V, R_G=25 Ω, Starting T_j=25 °C.

Note 3) I_S = 8.0A, dI/dt = 200A/μs, V_{DD} = BV_{DSS}, Starting T_j=25 °C.

Note 4) Pulse Test : Pulse width = 300μs, Duty Cycle = 2%.

Note 5) Essentially independent of operating temperature.

Marking



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Fig1. I_D - V_{DS}

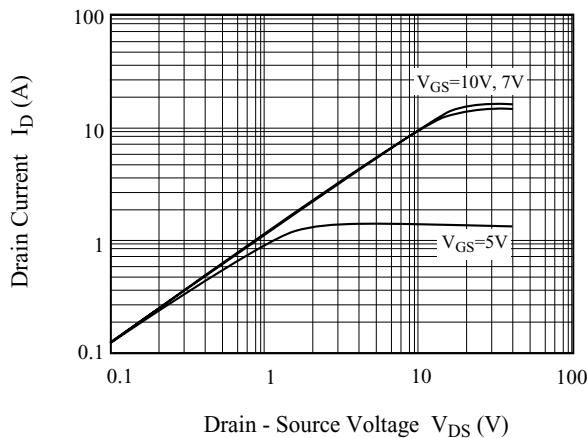


Fig2. I_D - V_{GS}

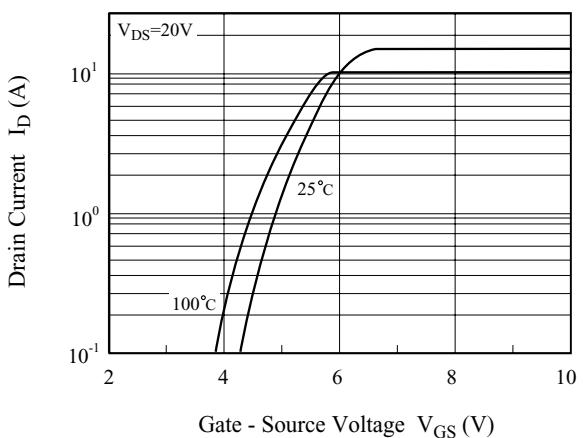


Fig3. BV_{DSS} - T_j

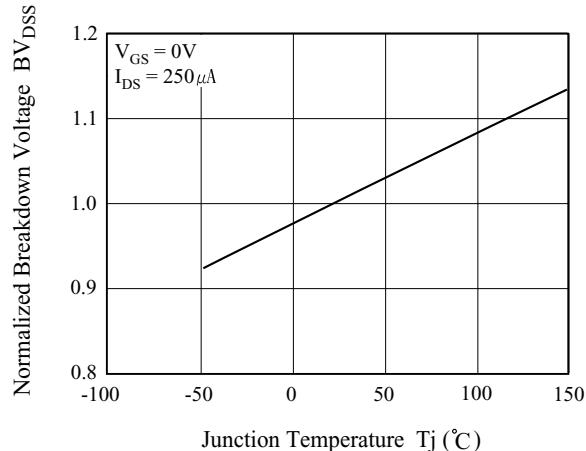


Fig4. $R_{DS(\text{ON})}$ - I_D

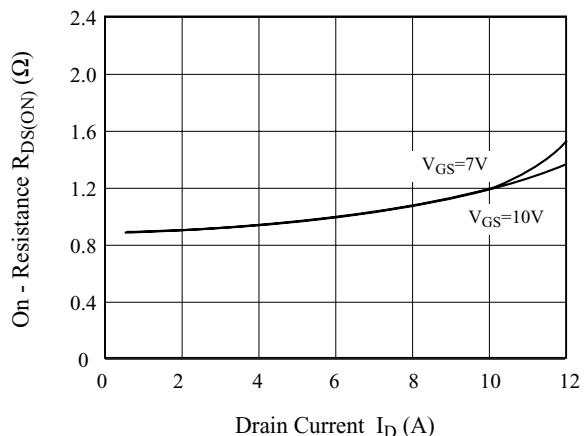


Fig5. I_S - V_{SD}

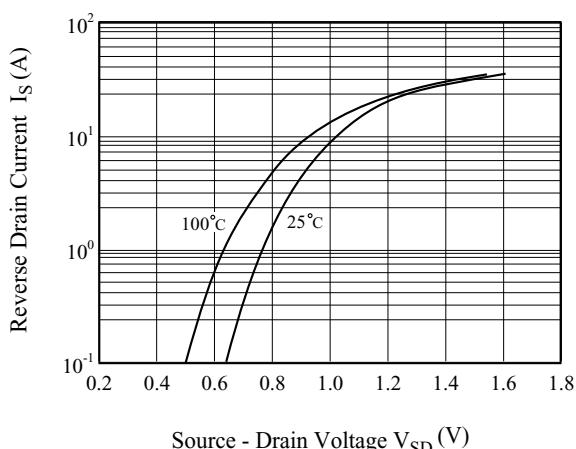
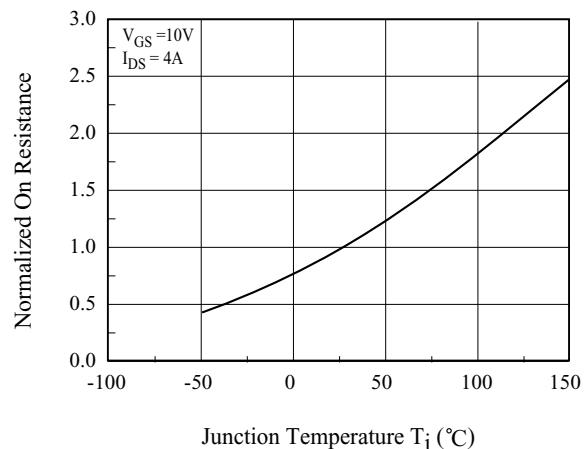


Fig6. $R_{DS(\text{ON})}$ - T_j



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Fig 7. C - V_{DS}

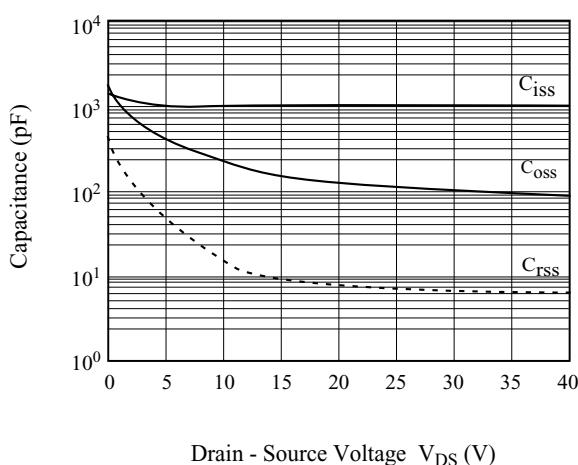


Fig8. Q_g- V_{GS}

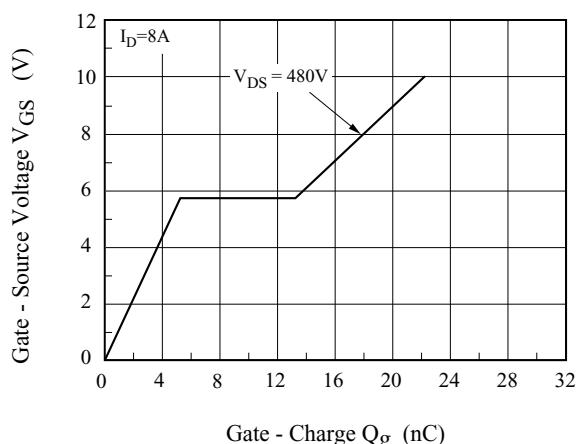


Fig9. Safe Operation Area

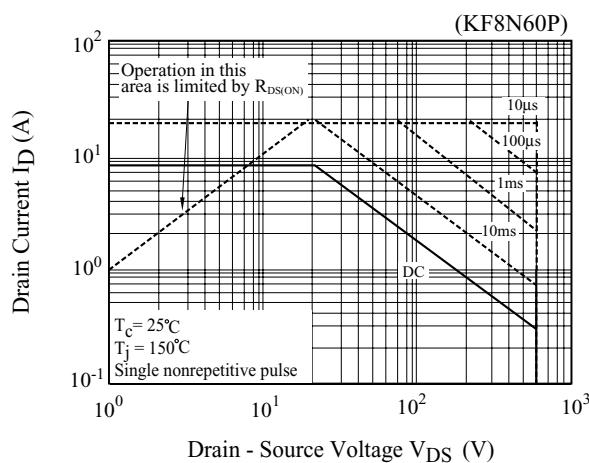


Fig10. Safe Operation Area

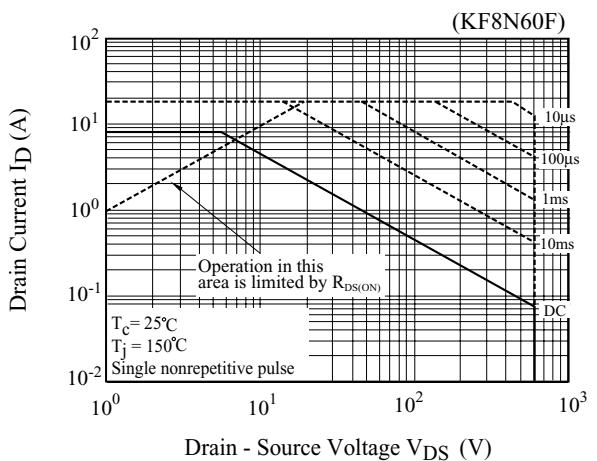
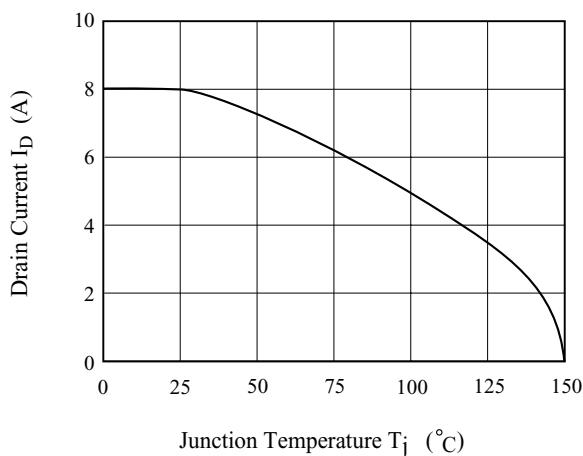


Fig11. I_D - T_j



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Fig12. Transient Thermal Response Curve

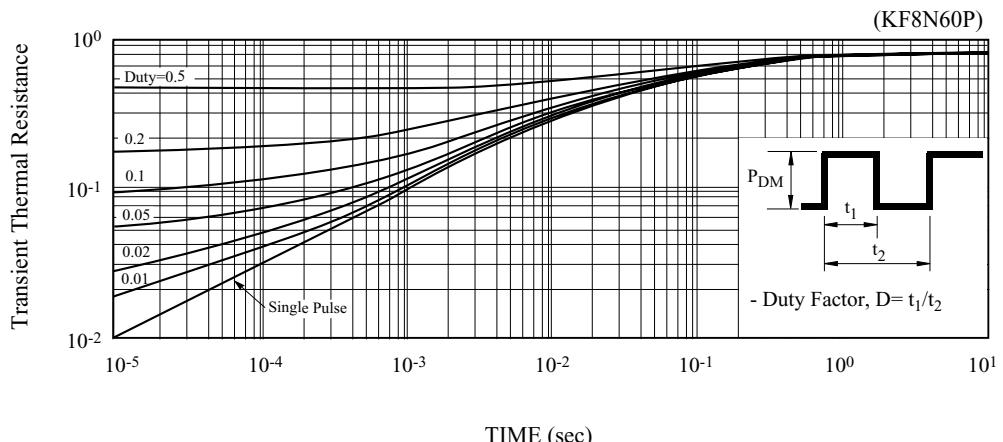
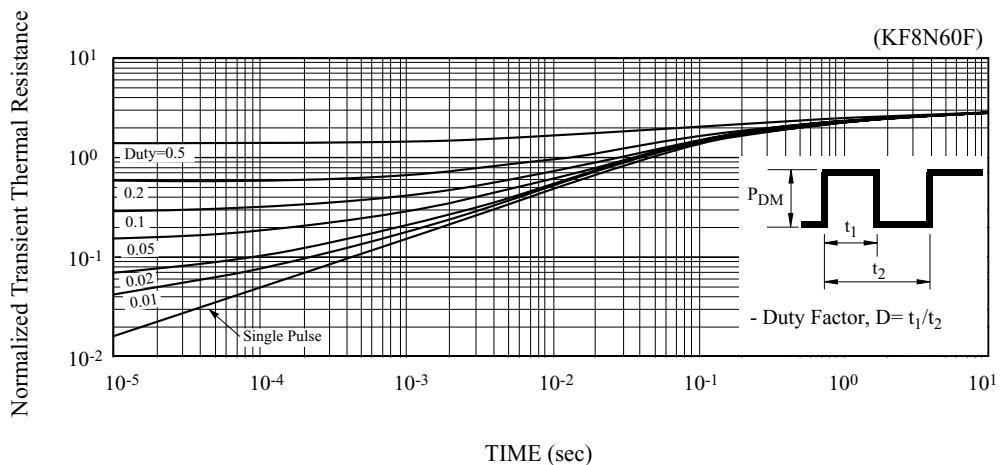


Fig13. Transient Thermal Response Curve



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Fig14. Gate Charge

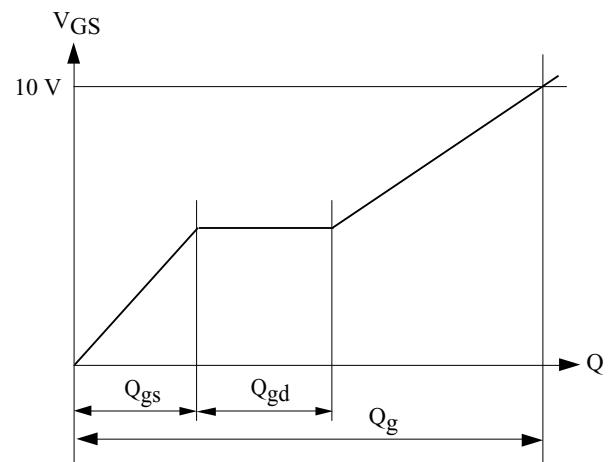
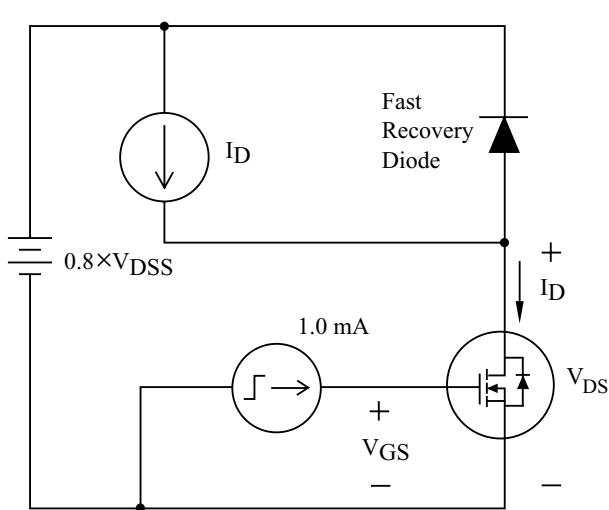


Fig15. Single Pulsed Avalanche Energy

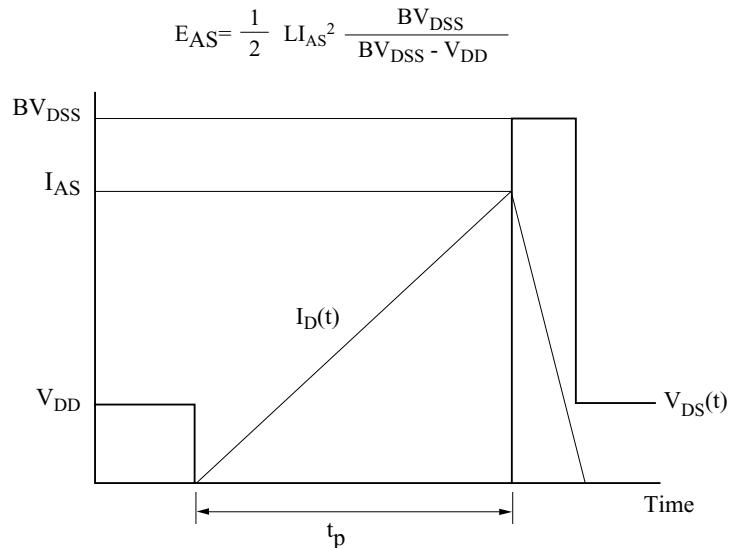
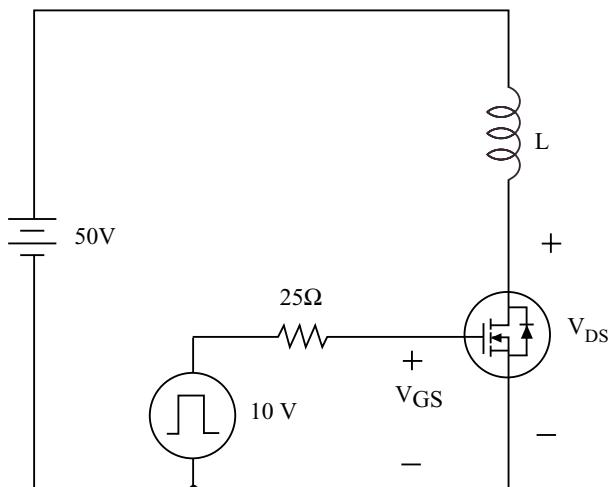
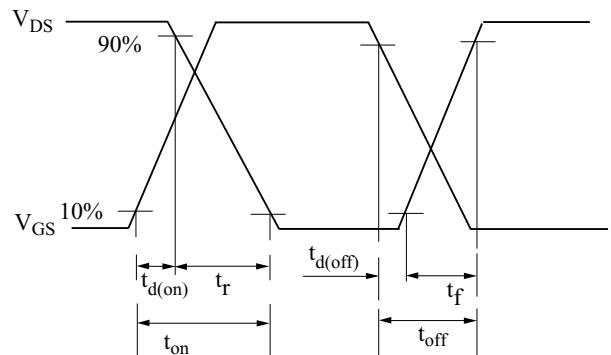
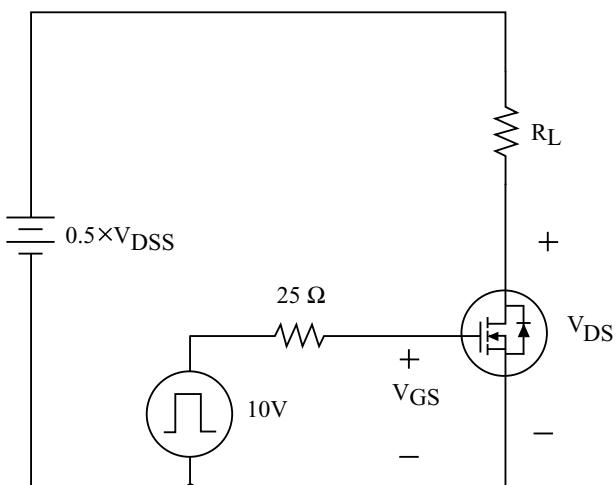


Fig16. Resistive Load Switching



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Fig17. Source - Drain Diode Reverse Recovery and dv /dt

