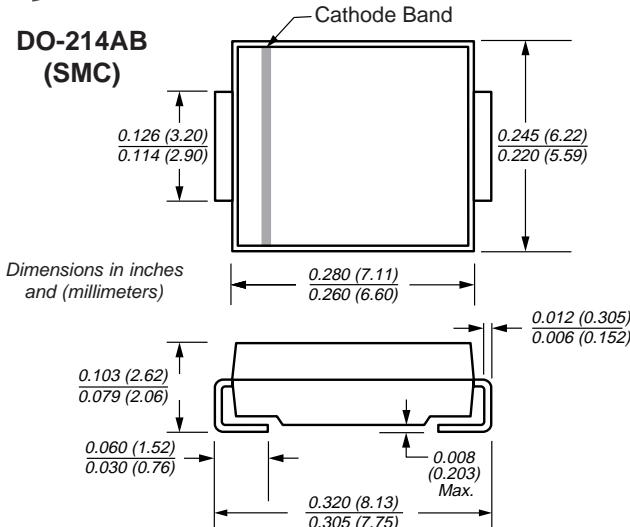
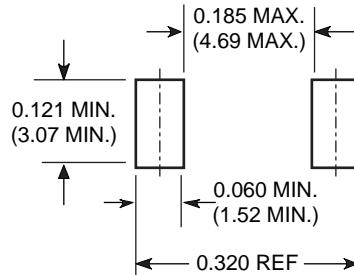




## High Voltage Surface Mount Schottky Barrier Rectifiers

 Reverse Voltage 90 to 100V  
 Forward Current 3.0A


### Mounting Pad Layout



### Features

- Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- Low profile surface mount package
- Built-in strain relief
- Low power loss, high efficiency
- For use in low voltage high frequency inverters, free wheeling, and polarity protection applications
- Guardring for overvoltage protection
- High temperature soldering guaranteed: 250°C/10 seconds at terminals

### Mechanical Data

**Case:** JEDEC DO-214AB molded plastic body

**Terminals:** Solder plated, solderable per MIL-STD750, Method 2026

**Polarity:** Color band denotes cathode end

**Weight:** 0.007 oz., 0.25 g

### Maximum Ratings and Thermal Characteristics (TA = 25°C unless otherwise noted)

Parameter	Symbol	SS3H9	SS3H10	Unit
Device marking code		MS9	MS10	
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	90	100	V
Working peak reverse voltage	V <sub>RWM</sub>	90	100	V
Maximum DC blocking voltage	V <sub>DC</sub>	90	100	V
Maximum average forward rectified current at: T <sub>L</sub> = 115°C	I <sub>F(AV)</sub>	3.0		A
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I <sub>FSM</sub>	100		A
Peak repetitive reverse surge current at t <sub>p</sub> = 2.0μs, 1KHz	I <sub>RRM</sub>	1.0		A
Critical rate of rise of reverse voltage	dv/dt	10,000		V/μs
Typical thermal resistance – junction to lead T <sub>L</sub> = 25°C – junction to ambient <sup>(2)</sup>	R <sub>θJL</sub> R <sub>θJA</sub>	9.7 32		°C/W
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>TSG</sub>	-65 to +175		°C

### Electrical Characteristics (TA = 25°C unless otherwise noted)

Maximum instantaneous forward voltage at: <sup>(1)</sup>	I <sub>F</sub> = 3.0A, T <sub>J</sub> = 25°C I <sub>F</sub> = 3.0A, T <sub>J</sub> = 125°C	V <sub>F</sub>	0.8 0.65	V
Maximum DC reverse current at rated DC blocking voltage	T <sub>J</sub> = 25°C T <sub>J</sub> = 125°C	I <sub>R</sub>	20 4	μA mA

**Notes:** (1) Pulse test: 300μs pulse width, 1% duty cycle

(2) PCB mounted

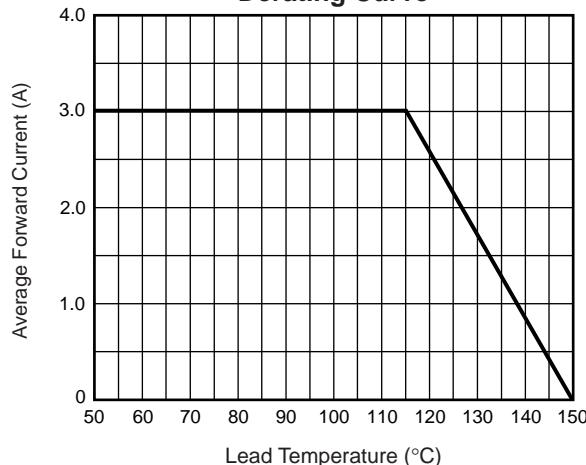
# SS3H9 and SS3H10

Vishay Semiconductors  
formerly General Semiconductor

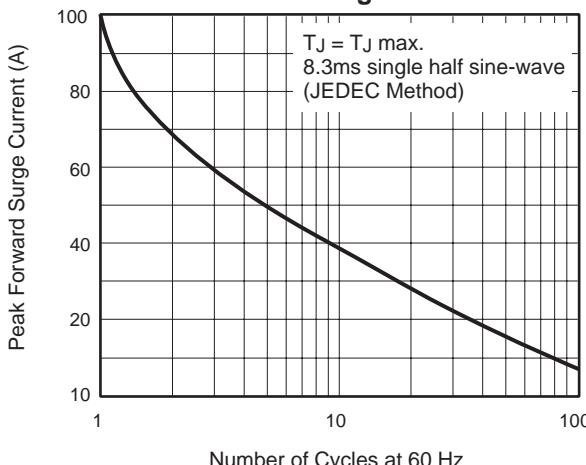


## Ratings and Characteristic Curves ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

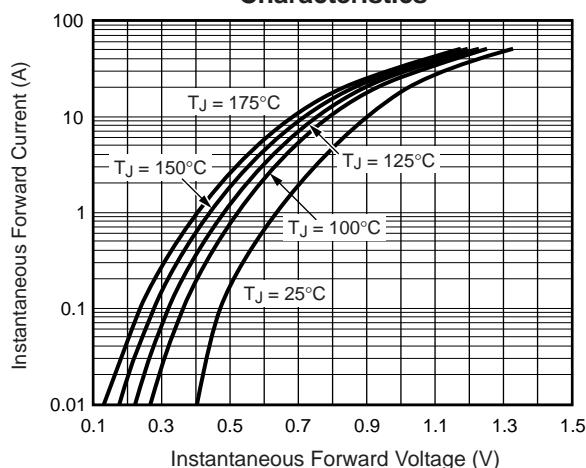
**Fig. 1 – Maximum Forward Current Derating Curve**



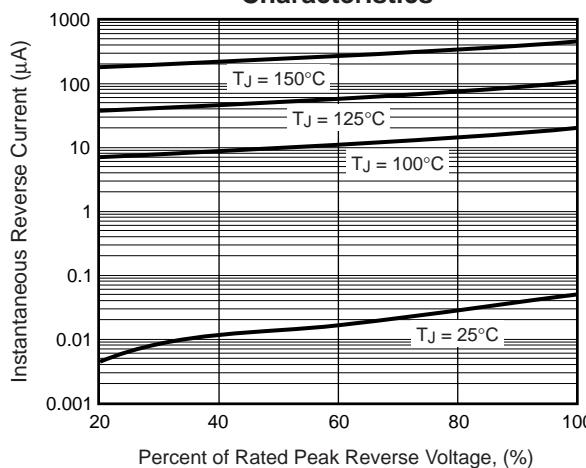
**Fig. 2 – Maximum Non-repetitive Peak Forward Surge Current**



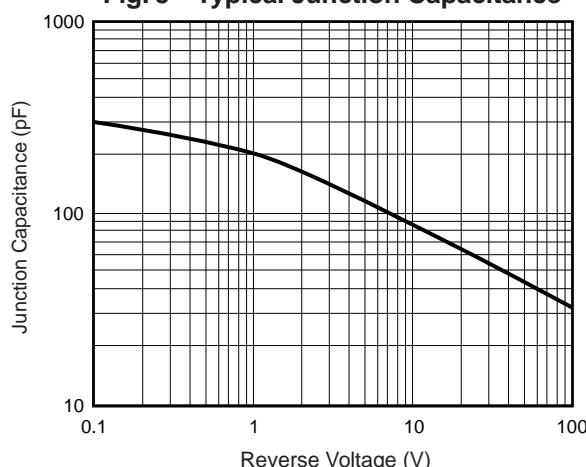
**Fig. 3 – Typical Instantaneous Forward Characteristics**



**Fig. 4 – Typical Reverse Characteristics**



**Fig. 5 – Typical Junction Capacitance**



**Fig. 6 – Typical Transient Thermal Impedance**

