

## **September 16, 2015**

## Datasheet Errata for the S6E2DF Series 32-bit ARM® Cortex®-M4F based Microcontroller

This document describes the errata for the S6E2DF Series 32-bit ARM® Cortex®-M4F based Microcontroller Data Sheet. Compare this document to the device's data sheet for a complete functional description.

Contact your local Cypress Sales Representative, if you have questions.

## **Part Numbers Affected**

Part Number
S6E2DF Series

Page	Item	Description							
Original c	locument code	e: DS709-00031-1v0-E							
Rev. 1.0	June 25, 201	15							
64	9. Handling	"Sub Crystal Oscillator" should be added as indicated by the shading below.							
	Devices								
		■Surface mount type							
		Size: More than 3.2 mm x 1.5 mm							
		Load capacitance: Approximately 6 pF to 7 pF							
		When the Standard setting (CCS/CCB=11001110)							
		Load capacitance: Approximately 4 pF to 7 pF							
		When the low power setting (CCS/CCB=00000100)							
		■Lead type							
		Load capacitance: Approximately 6 pF to 7 pF							
		When the Standard setting (CCS/CCB=11001110)							
		Load capacitance: Approximately 4 pF to 7 pF							
		When the low power setting (CCS/CCB=00000100)							

Page	Item				Desc	ription				
92	14.3.1	Table 14-10 should be added as indicated by the shading below.								
	Current Rating	Table 14-10 Typical and Maximum Current Consumption in Deep Standby Stop Mode, Deep Standby RTC  Mode and VBAT								
		Parameter	Symbol	Pin Name	Conditions	Frequency (MHz)	Va Typ	lue Max	Unit	Remarks
							0.009	0.032	μA	*3, *4, *5 T <sub>A</sub> =+25°C
					RTC stop		-	0.994	μA	*3, *4, *5 T <sub>A</sub> =+85°C
							-	1.491	μA	*3, *4, *5 T <sub>A</sub> =+105°C
					RTC *6		1.0	1.636	μA	*3, *4 T <sub>A</sub> =+25°C
		Power supply current	ICCVBAT	VBAT		-	-	2.828	μA	*3, *4 T <sub>A</sub> =+85°C
							-	4.242	μA	*3, *4 T <sub>A</sub> =+105°C
							0.7	1.153	μА	*3, *4 T <sub>A</sub> =+25°C
					RTC *7 operation		-	2.277	μА	*3, *4 T <sub>A</sub> =+85°C
							-	3.416	μΑ	*3, *4 T <sub>A</sub> =+105°C
		*1: V <sub>CC</sub> =	=3.3 V							
		*2: V <sub>CC</sub> =	=3.6 V							
		*3: Whe	n all ports are f	ixed.						
		*4: Whe	n LVD is OFF							
		*5: Whe	n sub oscillatio	n is OFF						
		*6: Whe	n using the crys	stal oscillator	of 32 kHz (includin	g the current consump	tion of the	oscillation	n circuit)	
	When the Standard setting (CCS/CCB=11001110)									
		*7: When using the crystal oscillator of 32 kHz (including the current consumption of the oscillation circuit) When the low power setting (CCS/CCB=00000100)								

Page	Item	Description						
178	15. Ordering Information	Ordering I	nformation should be corrected	as indicated by the shading below.				
		(Error)	_					
			Part Number	Package				
			S6E2DF5G0AGV20000	Plastic • LQFP (0.5 mm pitch), 120 pin				
			S6E2DF5GJAMV20000	(FPT-120P-M21)				
			S6E2DF5J0AGV20000	Plastic • LQFP (0.5 mm pitch), 176 pin (FPT-176P-M07)				
			S6E2DF5G0AGB30000	Plastic • PFBGA (0.5 mm pitch), 161 pin (FDJ161)				
			S6E2DF5G0AGZ20000	Plastic • Ex-LQFP (0.5 mm pitch), 120 pin (LEM120)				
		(Correct)						
			Part Number	Package				
			S6E2DF5G0AGV20000	Plastic • LQFP (0.5 mm pitch), 120 pin				
			S6E2DF5GJAMV20000	(FPT-120P-M21)				
			S6E2DF5J0AGV20000	Plastic • LQFP (0.5 mm pitch), 176 pin (FPT-176P-M07)				
			S6E2DF5G0AGB30000	Plastic • PFBGA (0.5 mm pitch), 161 pin (FDJ161)				
			S6E2DF5G0AGE20000	Plastic • Ex-LQFP (0.5 mm pitch), 120 pin				

Page	Item	Description							
11	2. Features	Note should be added as indicated by the shading below.							
15	4. Packages	### Product Name    Packages	S6E2DF5G0A  CONTRACTOR OF THE PROPERTY OF T	S6E2DF5J0A  S6E2DF5J0A	S6E2DF5GJA  S6E2DF5GJA  O				

Page	Item				Description							
16, 18	5. Pin	Signal name should be corrected as below.										
	Assignment	(Error) GE SPCSY 0 (Correct) GE SPCSY0										
		(Error) GE_SPCSX_0 (Correct) GE_SPCSX0 (Error) GE_HBCSX_0 (Correct) GE_HBCSX0										
		(Error) GE_HBCSX_1 (Correct) GE_HBCSX1										
24 22	6. Pin	Signal name sho	Signal name should be corrected as below.									
21, 23, 48	Descriptions	Signal name sho	uia be corre	ected as	below.							
		(Error) GE_SPC										
		(Error) GE_HBC (Error) GE HBC										
		(EIIOI) GE_NBC	SA_1 (C0	nect) Gi	=_nbc3x							
67	10. Block	Signal name sho	uld be corre	ected as	below.							
	Diagram	(F=====) OF CDC	CV 0 (Ca		E CDCCVA							
		(Error) GE_SPC (Error) GE_HBC				/1						
		, , –	_ ,	,								
93	14.3 DC Characteristi	"VFLASH memor below.	y Standby	current"	should be co	rrecte	d as in	dicated	by th	e shading		
	CS	below.										
		(Error)	T	1	T	1			1			
		Parameter	Symbol	Pin	Conditions		Value		Unit	Remarks		
		VFLASH memory	-	name		Min	Тур	Max				
		Standby current			At Standby	-	15	25	μΑ			
		VFLASH memory Read current	I <sub>CCVFLASH</sub>	VCC	At Read	-	9	14	mA	40MHz		
					Λ.		13	20		80MHz		
		VFLASH memory write/erase current			At Write/Erase	-	20	25	mA			
		(0 t)										
		(Correct)										
		Parameter	Symbol	Pin name	Conditions	Min	Тур	Max	Unit	Remarks		
		VFLASH memory Standby current			At Standby	-	15	35	μΑ			
		VFLASH memory			At Read	-	9	14	mA	40MHz		
		Read current	I <sub>CCVFLASH</sub>	VCC			13	20		80MHz		
		VFLASH memory			At	_	20	25	mA	OGNITIZ		
		write/erase current			Write/Erase		20	23	1112 1			
162,	14.4 AC	Signal name sho	uld be corre	ected as	below.							
,		Signal name should be corrected as below.										
163,	Characteristi				_							
163, 164	Characteristi cs	(Error) GE_SPC	SX_0 (Co	rrect) GE	E_SPCSX0							
			SX_0 (Co SX_0 (Co	rrect) GE rrect) GE	E_SPCSX0 E_HBCSX0							

## **Document History Page**

	Document Title: Datasheet Errata for the S6E2DF Series 32-bit ARM® Cortex®-M4F based Microcontroller Document Number: 002-05043									
Rev.	ECN No.	Orig. of Change Description of Change								
**	_	AKIH	Initial Release							
*A	5037784	AKIH	Converted to Cypress format							

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