



# Introduction

The Keysight Technologies, Inc. U2300A Series USB modular multifunction data-acquisition (DAQ) devices offer a high-performance PC data-acquisition solution. The U2300A Series DAQ devices consist of two families: the basic multifunction DAQ comes in four models while the high density multifunction DAQ is made up of three models. The U2300A Series DAQ devices applications extend across industrial and education environments. Extending across industrial and education environments, the U2300A series DAQ with fast sampling rates is well suited for research and development, manufacturing as well as design validation designers.

#### Features

- Up to 3 MSa/s sampling rate for a single channel
- Functions as a standalone or modular unit
- Easy to use: Plug-and-play and hot swappable with Hi-Speed USB 2.0
- Up to 384 channels when incorporated into U2781A Keysight modular product chassis
- Easy-to-use bundled software for quick setup and data logging to PC
- 12-bit or 16-bit analog-to-digital (A/D) resolution
- 24-bit programmable digital input/output
- Self-calibration capability
- Compatible with a wide range of Application Development Environments
- USB 2.0 and USBTMC-USB488 standards

## High sampling rate

The U2300A Series DAQ devices can generate sampling rates of up to 3 MSa/s for a single channel. When multiple channels are configured, the device can sample data up to 1 MSa/s. The fast sampling capability allows users to perform intermittent detection easily. This is ideal when dealing with high density analog input/output signals especially when juggling between different input ranges and sampling requirements.

#### Flexible standalone or modular capability

The U2300A Series DAQ devices are uniquely designed with the flexibility to function as a standalone unit or as a part of a modular unit. When used together with the U2781A modular product chassis, the devices has the capability to support up to 384 channels.

# Flexible system and control options with polling and continuous mode

The U2300A Series DAQ devices have two modes, polling mode and continuous mode. Selecting continuous mode enables you to acquire data continuously once the trigger signal is received.

### Arbitrary waveform

Designed to support arbitrary waveforms, the U2300A Series allows you to generate arbitrary waveform via the Keysight Measurement Manager application software or SCPI commands.

#### Burst mode

Equipped with the burst mode, the enhancement feature enables simultaneous mode for analog input acquisition. Now can you can perform sampling measurement up to the highest possible speed of the DAQ.

#### Trigger sources

U2300A Series offers various trigger options from immediate trigger (none), analog/external digital trigger, System Synchronous Interface (SSI)/ Star trigger and Master/Slave trigger sources. These entire trigger options has the capability to configure trigger sources during A/D and digital-to-analog (D/A) operations. Selecting the slave trigger and SSI/Star trigger are recommended when the USB modules are used together with the Keysight U2781A USB modular product chassis.

#### Predefined function generator

Aside from supplying DC voltage, the two analog output channels are capable of generating common and predefined waveforms such as sinusoidal wave, square wave, triangle wave, sawtooth wave and noise wave.

## Product outlook and dimensions

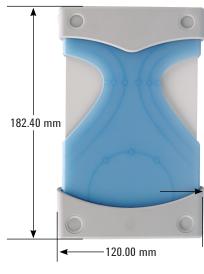
#### Front view



#### **Rear view**



#### Top view



# Standard shipped

## accessories

- AC/DC Power adapter
- Power cord
- USB extension cable
- L-Mount kit (used with modular product chassis)
- Keysight USB Modular Products Quick Start Guide
- Keysight USB Modular Products \_ **Reference CD-ROM**
- Keysight Automation-Ready CD-ROM \_ (contains the Keysight IO Libraries Suite)
- \_ Certificate of Calibration

## Optional accessories

- U2901A Terminal block and SCSI-II 68pin connector with 1-meter cable
- U2902A Terminal block and SCSI-II 68pin connector with 2-meter cable

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Product characteristics and general specifications
Remote interface – Hi-Speed USB 2.0 – USBTMC-USB4881
Power requirement - +12 VDC (TYPICAL) - 2 A (MAX) input rated current
Power consumption +12 VDC, 550 mA maximum
Operating environment – Operating temperature from 0 °C to +55 °C – Relative humidity at 15% to 85% RH (non-condensing) – Altitude up to 2000 meters – Pollution Degree 2 – For indoor use only
Storage compliance -20 °C to 70 °C
Safety compliance
Certified with: – IEC 61010-1:2001/EN 61010-1:2001 (2nd Edition) – USA: UL61010-1: 2004 – Canada: CSA C22.2 No.61010-1:2004
EMC compliance – IEC/EN 61326-1 1998 – CISPR 11: 1990/EN55011:1991, Class A, Group 1 – Canada: ICES-001: 1998 – Australia/New Zealand: AS/NZS 2064.1
Shock and vibration Tested to IEC/EN 60068-2
IO connector 68-pin female VHDCI Type
Dimension (W $\times$ D $\times$ H)
Module dimension: — 120.00 mm × 182.40 mm × 44.00 mm (with plastic casing) — 105.00 mm × 174.54 mm × 25.00 mm (without plastic casing)
Terminal block dimension: — 103.00 mm × 85.20 mm × 42.96 mm
Weight - 565 g (with plastic casing) - 400 g (without plastic casing)
Warranty Three years for U2300A series DAQ devices Three months for standard shipped accessories

<sup>1</sup> Compatible with Microsoft Windows operating systems only. Requires a direct USB connection to the PC so the appropriate driver can be installed in the USB DAQ module.

# Electrical specifications

# Basic multifunction USB DAQ

Model number	U2351A	U2352A	U2353A	U2354A		
Analog input						
Resolution		16 bits, no	missing codes			
Number of channels	16 SE/8 DI (software selectable/channel)					
Maximum sampling rate <sup>1</sup>	250 kSa	250 kSa/s 500 kSa/s				
Scan list memory		Up to 100 selec	table channel entries			
Programmable bipolar input range		±10 V, ±5 V	/, ±2.5 V, ±1.25 V			
Programmable unipolar input range		0 to 10 V, 0 to 5 V	7, 0 to 2.5 V, 0 to 1.25 V			
Input coupling			DC			
Input impedance		1 GC	Ω / 100 pF			
Operational common mode voltage range		±7.	5 V <sub>maximum</sub>			
Overvoltage protection	Power-or		V, Power-off: Continuous ±15	V		
Trigger sources	Exte	ernal analog/digit	al trigger, SSI/Star trigger <sup>2</sup>			
Trigger modes	Pre- trig	ger, delay-trigger,	post-trigger, and middle-trigge	er		
FIFO buffer size		Up	to 8 MSa			
Analog output						
Resolution	16 bits	-	16 bits	-		
Number of channels	2	-	2	-		
Maximum update rate	1 MSa/s	-	1 MSa/s	-		
Output ranges	0 to 10 V, ±10 V, 0 to A0_EXT_REF, ±A0_EXT_REF <sup>3</sup>	-	0 to 10 V, ±10 V, 0 to A0_EXT_REF, ±A0_EXT_REF <sup>3</sup>	-		
Output coupling	DC	-	DC	-		
Output impedance	0.1 Ω typical	-	0.1 Ω typical	-		
Stability	Any passive load up to 1500 pF	-	Any passive load up to 1500 pF	-		
Power-on state	0 V steady state	-	0 V steady state	-		
Trigger sources	External analog/digital trigger, SSI/Star trigger <sup>2</sup>	-	External analog/digital trigger, SSI/Star trigger <sup>2</sup>	-		
Trigger modes	Post-trigger and delay-trigger	-	Post-trigger and delay-trigger	-		
FIFO buffer size	One channel: Maximum 8 MSa Two channels: Maximum 4 MSa/ch	-	One channel: Maximum 8 MSa Two channels: Maximum 4 MSa/ch	-		
Function generation mode	Sine, square, triangle, sawtooth, and noise waveforms	-	Sine, square, triangle, sawtooth, and noise waveforms	-		

## Basic multifunction USB DAQ (continued)

compatibility       TTL         nput voltage       V <sub>ii</sub> = 0.7 V max, I <sub>i</sub> = 10 µA max         nput voltage range       -0.5 V to +5.5 V         Output voltage range       -0.5 V to +5.5 V         Vulput voltage       V <sub>in</sub> = 0.45 V max, I <sub>in</sub> = 8 mA max         V <sub>on</sub> = 0.45 V max, I <sub>in</sub> = 8 mA max       V <sub>on</sub> = 2.4 V min, I <sub>on</sub> = 400 µA max         Beneral purpose digital counter       42 minn. I <sub>on</sub> = 400 µA max         Adximum count       (2 <sup>21</sup> -1) bits         Number of channels       TWo independent up/down counter         Compatibility       TTL         Sae clock available       48 MHz         daximum clock source       12 MHz         Internal or external       3ase clock available         Valse width measurement range       0.167 µs to 78.956 s         Inalog trigger       12 MHz         rigger source       All analog input channels, External analog trigger (EXTA_TRIG)         rigger level       ±Full scale for internal; ±10 V for external         Sandwidth       400 kHz         mput impedance for EXTA_TRIG       20 kG         Soupling       DC         Vervoltage protection       Continuous for ± 35 V <sub>maximm</sub> Digital trigger       20 kG         Soupling       DC         Vervol	Digital I/O	
Description         nput voltage       V <sub>k</sub> = 0.7 V max, l <sub>k</sub> = 10 µA max         nput voltage range       -0.5 V to +5.5 V         Dutput voltage range       -0.6 V to +5.5 V         Dutput voltage of 0.65 V max, l <sub>m</sub> = 8 mA max       V <sub>bl</sub> = 2.4 V min, l <sub>m</sub> = 400 µA max         Semeral purpose digital counter       (2 <sup>31</sup> -1) bits         Maximum count       (2 <sup>31</sup> -1) bits         Umber of channels       Two independent up/down counter         Compatibility       TTL         Sase clock available       48 MHz         Adaximum clock source       12 MHz         Requency       12 MHz         Nput frequency range <sup>4</sup> 0.1 Hz to 6 MHz at 50% duty cycle         Values width measurement range       0.167 µs to 178.956 s         Inalog trigger       Full scale for internal; ±10 V for external         rigger source       All analog input channels, External analog trigger (EXTA_TRIG)         rigger level       ±Full scale for internal; ±10 V for external         rigger conditions       Above high, below low, and window (software selectable)         rigger conditions       Above high, below low, and window (software selectable)         rigger source       Continuous for ± 35 V <sub>maxima</sub> Stardwidth       400 kHz         naput impedance for EXTA_TRIG       20 kΩ<	Number of channels	24-bit programmable input/output
upper Voltage $V_{im} = 2.0 \text{ V min, } I_{im} = 10 \text{ µA max}}$ nput voltage range $-0.5 \text{ V to +5.5 V}$ Dutput voltage $V_{0m} = 0.45 \text{ V max}, I_{0m} = 8 \text{ mA max}} V_{0m} = 2.4 \text{ V min, } I_{0m} = 400 \text{ µA max}}$ seneral purpose digital counter       (2*1-1) bits         daximum count       (2*1-1) bits         Number of channels       Tru         Sompatibility       TrL         Clock source       Internal or external         Base clock available       48 MHz         Aaximum clock source       12 MHz         nput frequency range <sup>4</sup> 0.1 Hz to 6 MHz at 50% duty cycle         values width measurement range       0.167 µs to 178.956 s         nalog trigger       All analog input channels, External analog trigger (EXTA_TRIG)         rigger source       All analog input channels, External analog trigger (EXTA_TRIG)         rigger revel       ±Full scale for internal; ±10 V for external         rigger conditions       Above high, below low, and window (software selectable)         rigger revel       20 kΩ         coupling       DC         coupling       DC         vorvoltage protection       Continuous for ± 35 V <sub>maximum</sub> Nigital trigger       S V         valse width       20 ns minimum	Compatibility	TTL
Dutput voltage       V <sub>us</sub> = 0.45 V max, I <sub>us</sub> = 8 mA max V <sub>get</sub> = 2.4 V min, I <sub>get</sub> = 400 µA max         Seneral purpose digital counter       (2**-1) bits         Maximum count       (2**-1) bits         Wumber of channels       Two independent up/down counter         Compatibility       TTL         Dick source       Internal or external         Base clock available       48 MHz         Aaximum clock source       12 MHz         nput frequency range*       0.1 Hz to 6 MHz at 50% duty cycle         Vulse width measurement range       0.167 µs to 178.956 s         Nnalog trigger       All analog input channels, External analog trigger (EXTA_TRIG)         rigger source       All analog input channels, External analog trigger (EXTA_TRIG)         rigger revel       ±full scale for internal; ±10 V for external         rigger conditions       Above high, below low, and window (software selectable)         rigger level       £full scale for internal; ±10 V for external         rigger level       £full scale for internal; ±10 V for external         rigger level       £full scale for internal; ±10 V for external         rigger level       £full scale for internal; ±10 V for external         rigger protection       Continuous for ± 35 V         coupling       DC         Overvoltage protection	Input voltage	$V_{IL} = 0.7 V max$ , $I_{IL} = 10 \mu A max$ $V_{IH} = 2.0 V min$ , $I_{IH} = 10 \mu A max$
Number of channels     (2 <sup>31</sup> -1) bits       Seneral purpose digital counter     (2 <sup>31</sup> -1) bits       Aaximum count     (2 <sup>31</sup> -1) bits       Wumber of channels     Two independent up/down counter       Compatibility     TTL       Constrained     Internal or external       Base clock available     48 MHz       Aaximum clock source     12 MHz       requency     12 MHz       Pulse width measurement range     0.167 µs to 178.956 s       Inalog trigger     ±1 Nat to 6 MHz at 50% duty cycle       Pulse width measurement range     0.167 µs to 178.956 s       Inalog trigger source     All analog input channels, External analog trigger (EXTA_TRIG)       "rigger source isolution     8 bits       Bandwidth     400 kHz       anput impedance for EXTA_TRIG     20 kΩ       Compatibility     TTL/CMOS       Response     Rising or falling edge       Pulse width     20 ns minimum       Compatibility     TTL/CMOS       Response     Rising or falling edge       Pulse width     20 ns minimum       Calibration <sup>5</sup> 5 V       "emperature drift     ±2 prm.º°C       Stability     ±6 ppm.1000 hrs       Beneral     Hi-Speed USB 2.0       Deven class     USBTMC-USB488	Input voltage range	–0.5 V to +5.5 V
Aximum count       (2 <sup>33</sup> -1) bits         Number of channels       Two independent up/down counter         Compatibility       TTL         Clock source       Internal or external         Base clock available       48 MHz         Aximum clock source       12 MHz         requency       12 MHz         nput frequency range <sup>4</sup> 0.1 Hz to 6 MHz at 50% duty cycle         Values width measurement range       0.167 µs to 178.956 s         Nanalog trigger       All analog input channels. External analog trigger (EXTA_TRIG)         rigger source       All analog input channels. External analog trigger (EXTA_TRIG)         rigger clock       ±full scale for internal; ±10 V for external         Trigger clocking       Above high, below low, and window (software selectable)         rigger level resolution       8 bits         Bandwidth       400 kHz         nput impedance for EXTA_TRIG       20 kΩ         Compatibility       TTL/CMOS         Response       Rising or falling edge         Pulse width       20 ns minimum         2alibration <sup>8</sup> 5 V         remerature drift       ±2 ppm/°C         Stability       ±6 ppm/1000 hrs         Stability       ±6 ppm/1000 hrs         Stability <td< td=""><td>Output voltage</td><td><math>V_{_{OL}} = 0.45</math> V max, <math>I_{_{OL}} = 8</math> mA max <math>V_{_{OH}} = 2.4</math> V min, <math>I_{_{OH}} = 400</math> <math>\mu</math>A max</td></td<>	Output voltage	$V_{_{OL}} = 0.45$ V max, $I_{_{OL}} = 8$ mA max $V_{_{OH}} = 2.4$ V min, $I_{_{OH}} = 400$ $\mu$ A max
Number of channels         Two independent up/down counter           Compatibility         TTL           Construction         Internal or external           Base clock available         48 MHz           Aaximum clock source         12 MHz           requency         12 MHz           Pulse width measurement range         0.1 Hz to 6 MHz at 50% duty cycle           Pulse width measurement range         0.167 µs to 178.956 s           Analog trigger         All analog input channels, External analog trigger (EXTA_TRIG)           Trigger source         All analog input channels, External analog trigger (EXTA_TRIG)           Trigger conditions         Above high, below low, and window (software selectable)           Trigger level         ±Full scale for internal; ±10 V for external           Addith         400 kHz           nuput impedance for EXTA_TRIG         20 kG           Coupling         DC           Divervoltage protection         Continuous for ± 35 V           Compatibility         TTL/CMOS           Response         Rising or falling edge           Vulse width         20 ns minimum           Calibration <sup>5</sup> D           On board reference voltage         5 V           "emperature drift         ±2 ppm/°C           Stability<	General purpose digital counter	
Compatibility       TTL         Clock source       Internal or external         Base clock available       48 MHz         Aaximum clock source       12 MHz         requency       12 MHz         ubles width measurement range       0.1 Hz to 6 MHz at 50% duty cycle         ubles width measurement range       0.167 µs to 178.956 s         Analog trigger       All analog input channels, External analog trigger (EXTA_TRIG)         Trigger source       All analog input channels, External analog trigger (EXTA_TRIG)         Trigger conditions       Above high, below low, and window (software selectable)         Trigger conditions       Above high, below low, and window (software selectable)         Sandwidth       400 kHz         nuput impedance for EXTA_TRIG       20 kQ         Coupling       DC         Overvoltage protection       Continuous for ± 35 V         Compatibility       TTL/CMOS         Response       Rising or falling edge         Ualibration <sup>5</sup> 5 V         Compartaire drift       ±2 ppm/°C         Stability       ±6 ppm/1000 hrs         General       Hi-Speed USB 2.0         Device class       USETMC-USB488	Maximum count	(2 <sup>31</sup> –1) bits
Clock source       Internal or external         Base clock available       48 MHz         Maximum clock source       12 MHz         requency       0.1 Hz to 6 MHz at 50% duty cycle         nput frequency range <sup>4</sup> 0.1 Hz to 6 MHz at 50% duty cycle         Pulse width measurement range       0.167 µs to 178.956 s         valadg trigger       17 mger source         All analog input channels, External analog trigger (EXTA_TRIG)       17 mger source         Trigger level       ±Full scale for internal; ±10 V for external         Trigger level resolution       8 bits         Bandwidth       400 kHz         nput impedance for EXTA_TRIG       20 kΩ         Coupling       DC         Digital trigger       20 kΩ         Coupling       DC         Digital trigger       20 ns minum         Coupling       DC         Digital trigger       20 ns minum         Coupling       DC         Digital trigger       11 L/CMOS         Response       Rising or falling edge         Pulse width       20 ns minum         2alibration <sup>6</sup> 10         Dn board reference voltage       5 V         Temperature drift       ±2 ppm/°C         Stability	Number of channels	Two independent up/down counter
Base clock available         48 MHz           Aximum clock source requency         12 MHz           Apput frequency range <sup>4</sup> 0.1 Hz to 6 MHz at 50% duty cycle           Pulse width measurement range         0.167 µs to 178.956 s           Analog trigger         Image requency           Trigger source         All analog input channels, External analog trigger (EXTA_TRIG)           Trigger source         All analog input channels, External analog trigger (EXTA_TRIG)           Trigger conditions         Above high, below low, and window (software selectable)           Trigger level         ±Full scale for internal; ±10 V for external           Trigger level resolution         8 bits           Bandwidth         400 kHz           nuput impedance for EXTA_TRIG         20 kΩ           Coupling         DC           Diveroltage protection         Continuous for ±35 V_maximum           Digital trigger         Image trigger felling edge           Pulse width         20 ns minimum           Calibration <sup>5</sup> Image trigger ference voltage           Di board reference voltage         5 V           "emperature drift         ±2 ppm/°C           Stability         ±6 ppm/1000 hrs           General         Hi-Speed USB 2.0           Diverc class         USBTMC-USB	Compatibility	TTL
Asximum clock source requency       12 MHz         nput frequency range <sup>4</sup> 0.1 Hz to 6 MHz at 50% duty cycle         vulse width measurement range       0.167 µs to 178.956 s         knalog trigger       12 MHz         Trigger source       All analog input channels. External analog trigger (EXTA_TRIG)         Trigger conditions       Above high, below low, and window (software selectable)         Trigger level       ± Full scale for internal; ±10 V for external         Trigger conditions       A bove high, below low, and window (software selectable)         Trigger level resolution       8 bits         Bandwidth       400 kHz         nput impedance for EXTA_TRIG       20 kΩ         Coupling       DC         Divervoltage protection       Continuous for ± 35 V <sub>maximum</sub> Digital trigger       Coupatibility         Compatibility       TTL/CMOS         Response       Rising or falling edge         Pulse width       20 ns minimum         Calibration <sup>5</sup> 12 MHz         Din board reference voltage       5 V         Temperature drift       ±2 ppm/°C         Stability       ±6 ppm/1000 hrs         General       Mi-Speed USB 2.0         Device class       USBTMC-USB488	Clock source	Internal or external
Trequency       12 MHz         nput frequency range <sup>4</sup> 0.1 Hz to 6 MHz at 50% duty cycle         Pulse width measurement range       0.167 μs to 178.956 s         Analog trigger       12 MHz         Trigger source       All analog input channels, External analog trigger (EXTA_TRIG)         Trigger level       ±Full scale for internal; ±10 V for external         Trigger conditions       Above high, below low, and window (software selectable)         Trigger level resolution       8 bits         Bandwidth       400 kHz         nput impedance for EXTA_TRIG       20 kΩ         Coupling       DC         Divervoltage protection       Continuous for ± 35 V         Stappose       Rising or falling edge         Pulse width       20 ns minimum         Calibration <sup>5</sup> 5 V         Comparative drift       ±2 ppm/°C         Stability       ±6 ppm/1000 hrs         Stability       ±6 ppm/1000 hrs         Stability       ±6 ppm/1000 hrs         Stability       ±10 FMz         Weater and the interface       Hi-Speed USB 2.0         Device class       USBTMC-USB488	Base clock available	48 MHz
All analog trigger         Trigger source       All analog input channels, External analog trigger (EXTA_TRIG)         Trigger source       All analog input channels, External analog trigger (EXTA_TRIG)         Trigger conditions       Above high, below low, and window (software selectable)         Trigger level       ±Full scale for internal; ±10 V for external         Trigger conditions       Above high, below low, and window (software selectable)         Trigger level resolution       8 bits         Bandwidth       400 kHz         nput impedance for EXTA_TRIG       20 kΩ         Soupling       DC         Dervoltage protection       Continuous for ± 35 V         Digital trigger       20 kΩ         Scapponse       Rising or falling edge         Pulse width       20 ns minimum         Calibration <sup>5</sup> V         Cemperature drift       ±2 ppm/°C         Stability       ±6 ppm/1000 hrs         Seneral       Hi-Speed USB 2.0         Device class       USBTMC-USB488	Maximum clock source frequency	12 MHz
Analog trigger         Trigger source       All analog input channels, External analog trigger (EXTA_TRIG)         Trigger level       ±Full scale for internal; ±10 V for external         Trigger conditions       Above high, below low, and window (software selectable)         Trigger level resolution       8 bits         Bandwidth       400 kHz         nput impedance for EXTA_TRIG       20 kΩ         Coupling       DC         Dvervoltage protection       Continuous for ± 35 V <sub>maximum</sub> Digital trigger       20 kΩ         Compatibility       TTL/CMOS         Response       Rising or falling edge         Pulse width       20 ns minimum         Calibration <sup>5</sup> 5 V         Temperature drift       ±2 ppm/°C         Stability       ±6 ppm/1000 hrs         General       Hi-Speed USB 2.0         Device class       USBTMC-USB488	Input frequency range <sup>4</sup>	0.1 Hz to 6 MHz at 50% duty cycle
Trigger sourceAll analog input channels, External analog trigger (EXTA_TRIG)Trigger level±Full scale for internal; ±10 V for externalTrigger conditionsAbove high, below low, and window (software selectable)Trigger level resolution8 bitsBandwidth400 kHznput impedance for EXTA_TRIG20 kΩCouplingDCOvervoltage protectionContinuous for ± 35 VDigital trigger0CompatibilityTTL/CMOSResponseRising or falling edgePulse width20 ns minimumCalibration <sup>5</sup> 5 VOn board reference voltage5 VTemperature drift±2 ppm/°CStability±6 ppm/1000 hrsBeneralHi-Speed USB 2.0Device classUSBTMC-USB488	Pulse width measurement range	0.167 µs to 178.956 s
trigger level       ±Full scale for internal; ±10 V for external         rrigger conditions       Above high, below low, and window (software selectable)         rrigger level resolution       8 bits         Bandwidth       400 kHz         nput impedance for EXTA_TRIG       20 kΩ         Coupling       DC         Overvoltage protection       Continuous for ± 35 V <sub>maximum</sub> Digital trigger       TTL/CMOS         Response       Rising or falling edge         Pulse width       20 ns minimum         Calibration <sup>6</sup> 5 V         On board reference voltage       5 V         Gremperature drift       ±2 ppm/°C         Stability       ±6 ppm/1000 hrs         General       Hi-Speed USB 2.0         Device class       USBTMC-USB488	Analog trigger	
Above high, below low, and window (software selectable)         rigger conditions       8 bits         Bandwidth       400 kHz         nput impedance for EXTA_TRIG       20 kΩ         Coupling       DC         Overvoltage protection       Continuous for ± 35 V         Outgital trigger       Compatibility         Object of EXTA_TRIG       20 kΩ         Devervoltage protection       Continuous for ± 35 V         Compatibility       TTL/CMOS         Response       Rising or falling edge         Pulse width       20 ns minimum         Calibration <sup>6</sup> SV         Cremperature drift       ±2 ppm/°C         Stability       ±6 ppm/1000 hrs         General       Hi-Speed USB 2.0         Device class       USBTMC-USB488	Trigger source	All analog input channels, External analog trigger (EXTA_TRIG)
rigger level resolution 8 bits Bandwidth 400 kHz nput impedance for EXTA_TRIG 20 kΩ Coupling DC Dvervoltage protection Continuous for ± 35 V <sub>maximum</sub> Digital trigger Compatibility TTL/CMOS Response Rising or falling edge Pulse width 20 ns minimum Calibration <sup>5</sup> Dn board reference voltage 5 V remperature drift ±2 ppm/°C Stability ±6 ppm/1000 hrs General Remote interface Hi-Speed USB 2.0 Device class USBTMC-USB488	Trigger level	±Full scale for internal; ±10 V for external
Audwidth       400 kHz         nput impedance for EXTA_TRIG       20 kΩ         Coupling       DC         Overvoltage protection       Continuous for ± 35 V <sub>maximum</sub> Digital trigger       TTL/CMOS         Compatibility       TTL/CMOS         Response       Rising or falling edge         Pulse width       20 ns minimum         Calibration <sup>5</sup> 5 V         Comperature drift       ±2 ppm/°C         Stability       ±6 ppm/1000 hrs         General       Hi-Speed USB 2.0         Device class       USBTMC-USB488	Trigger conditions	Above high, below low, and window (software selectable)
nput impedance for EXTA_TRIG       20 kΩ         Coupling       DC         Overvoltage protection       Continuous for ± 35 V <sub>maximum</sub> Digital trigger       TTL/CMOS         Compatibility       TTL/CMOS         Response       Rising or falling edge         Pulse width       20 ns minimum         Calibration <sup>5</sup> 5 V         Cemperature drift       ±2 ppm/°C         Stability       ±6 ppm/1000 hrs         General       Hi-Speed USB 2.0         Device class       USBTMC-USB488	Trigger level resolution	8 bits
Coupling       DC         Overvoltage protection       Continuous for ± 35 V <sub>maximum</sub> Digital trigger       Compatibility         Compatibility       TTL/CMOS         Response       Rising or falling edge         Pulse width       20 ns minimum         Calibration <sup>5</sup> Comperature drift         ±2 ppm/°C       Etability         Stability       ±6 ppm/1000 hrs         General       Hi-Speed USB 2.0         Device class       USBTMC-USB488	Bandwidth	400 kHz
Divervoltage protection       Continuous for ± 35 V <sub>maximum</sub> Digital trigger       TTL/CMOS         Compatibility       TTL/CMOS         Response       Rising or falling edge         Pulse width       20 ns minimum         Calibration <sup>5</sup> 1         On board reference voltage       5 V         remperature drift       ±2 ppm/°C         Stability       ±6 ppm/1000 hrs         General       Hi-Speed USB 2.0         Device class       USBTMC-USB488	Input impedance for EXTA_TRIG	20 kΩ
Digital triggerCompatibilityTTL/CMOSResponseRising or falling edgePulse width20 ns minimumCalibration <sup>5</sup> 5 VOn board reference voltage5 VTemperature drift±2 ppm/°CStability±6 ppm/1000 hrsGeneral1000 hrsRemote interfaceHi-Speed USB 2.0Device classUSBTMC-USB488	Coupling	DC
Digital triggerCompatibilityTTL/CMOSResponseRising or falling edgePulse width20 ns minimumCalibration <sup>5</sup> 5 VOn board reference voltage5 VTemperature drift±2 ppm/°CStability±6 ppm/1000 hrsGeneral1000 hrsRemote interfaceHi-Speed USB 2.0Device classUSBTMC-USB488	Overvoltage protection	Continuous for $\pm$ 35 V <sub>maximum</sub>
Response       Rising or falling edge         Pulse width       20 ns minimum         Calibration <sup>5</sup> 20 ns minimum         On board reference voltage       5 V         remperature drift       ±2 ppm/°C         Stability       ±6 ppm/1000 hrs         General       Hi-Speed USB 2.0         Device class       USBTMC-USB488	Digital trigger	
Pulse width     20 ns minimum       Calibration <sup>5</sup> On board reference voltage     5 V       Temperature drift     ±2 ppm/°C       Stability     ±6 ppm/1000 hrs       General     Hi-Speed USB 2.0       Device class     USBTMC-USB488	Compatibility	TTL/CMOS
Calibration <sup>5</sup> On board reference voltage       5 V         Femperature drift       ±2 ppm/°C         Stability       ±6 ppm/1000 hrs         General       Hi-Speed USB 2.0         Device class       USBTMC-USB488	Response	Rising or falling edge
On board reference voltage     5 V       remperature drift     ±2 ppm/°C       Stability     ±6 ppm/1000 hrs       General     Hi-Speed USB 2.0       Device class     USBTMC-USB488	Pulse width	20 ns minimum
femperature drift     ±2 ppm/°C       Stability     ±6 ppm/1000 hrs       General     Hi-Speed USB 2.0       Device class     USBTMC-USB488	Calibration <sup>5</sup>	
Stability     ±6 ppm/1000 hrs       General     Hi-Speed USB 2.0       Device class     USBTMC-USB488	On board reference voltage	5 V
General     Hi-Speed USB 2.0       Device class     USBTMC-USB488	Temperature drift	±2 ppm/°C
Remote interface     Hi-Speed USB 2.0       Device class     USBTMC-USB488	Stability	±6 ppm/1000 hrs
Device class USBTMC-USB488	General	
	Remote interface	Hi-Speed USB 2.0
Programmable interface Standard Commands for Programmable Instruments (SCPI) and IVI-COM	Device class	USBTMC-USB488
	Programmable interface	Standard Commands for Programmable Instruments (SCPI) and IVI-COM

1

When multiple channels are used, the sampling rate of each channel is the maximum sampling rate divided by the number of channels used. System Synchronous Interface (SSI) and Star trigger commands are used when modular devices are used in the product chassis. 2

3 Maximum external reference voltage for analog output channels (A0\_EXT\_REF) is  $\pm 10$  V. 4

Measurement frequency's resolution:

= 12 MHz/n, n = 2, 3, 4, 5, ..., 120 M

= 6 MHz, 4 MHz, 3 MHz, 2.4 MHz, 2.0 MHz, ..., 0.1 Hz (up to six decimal points)

<sup>5</sup> 20 minutes warm-up time is recommended.

# High density multifunction USB DAQ

Model number	U2355A	U2356A	U2331A		
Analog input Resolution	16 hits no	missing codes	12 bits, no missing codes		
Number of channels		/channel)			
Maximum sampling rate <sup>1</sup>	250 kSa/s	500 kSa/s	3 MSa/s (single channel) 1 MSa/s (multiple channels)		
Scan list memory	U	p to 100 selectable channel (	entries		
Programmable bipolar input range	±10 V, ±5 V,	±2.5 V, ±1.25 V	±10 V, ±5 V, ±2.5 V, ±1.25 V, ±1 V, ±0.5 V, ±0.25 V, ±0.2 V, ±0.05 V		
Programmable unipolar input range	0 to 10 V, 0 to 5 V,	0 to 2.5 V, 0 to 1.25 V	0 to 10 V, 0 to 5 V, 0 to 4 V, 0 to 2.5 V, 0 to 2 V, 0 to 1 V, 0 to 0.5 V, 0 to 0.4 V, 0 to 0.1V		
Input coupling		DC			
Input impedance		1 GΩ / 100 pF			
Operational common mode voltage range		±7.5 V maximum			
Overvoltage protection	Power-on: Co	ontinuous ±30 V, Power-off:	Continuous ±15 V		
Trigger sources	External analog/digital trigger, SSI/Star trigger <sup>2</sup>				
Trigger modes	Pre- trigger, delay-trigger, post-trigger, and middle-trigger				
FIFO buffer size	Up to 8 MSa				
Analog output					
Resolution	12 bits				
Number of channels	2				
Maximum update rate		1 MSa/s			
Output ranges	0 to 10 V	, ±10 V, 0 to A0_EXT_REF, ±	AO_EXT_REF <sup>3</sup>		
Output coupling		DC			
Output impedance		0.1 Ω Typical			
Stability		Any passive load up to 150	D pF		
Power-on state		0 V steady state			
Trigger sources	Externa	l analog/digital trigger, SSI/	Star trigger <sup>2</sup>		
Trigger modes		Post-trigger and delay-trigg	ger		
FIFO buffer size	Т	One channel: Maximum 8 M wo channels: Maximum 4 M			
Function generation mode	Sine, squa	re, triangle, sawtooth, and no	oise waveforms		
Digital I/O					
Number of bits		24-bit programmable input/o	utput		
Compatibility		TTL			
Input voltage	$V_{\mu} = 0.7 \text{ V max}$ , $I_{\mu} = 10 \mu\text{A max}$ $V_{\mu} = 2.0 \text{ V min}$ , $I_{\mu} = 10 \mu\text{A max}$				
Input voltage range		-0.5 V to +5.5 V			
Output voltage	$V_{0L} = 0.45 \text{ V} \text{ max}, I_{0L} = 8 \text{ mA max}$ $V_{0H} = 2.4 \text{ V} \text{ min}, I_{0H} = 400 \text{ µA max}$				

# High density multifunction USB DAQ (continued))

General purpose digital counter	
Maximum count	(2 <sup>31</sup> – 1) bits
Number of channels	Two independent up/down counter
Compatibility	TTL
Clock source	Internal or external
Base clock available	48 MHz
Maximum clock source frequency	12 MHz
Input frequency range <sup>4</sup>	0.1 Hz to 6 MHz at 50% duty cycle
Pulse width measurement range	0.167 µs to 178.956 s
Analog trigger	
Trigger source	All analog input channels, External analog trigger (EXTA_TRIG)
Trigger level	±Full scale for internal; ±10 V for external
Trigger conditions	Above high, below low, and window (software selectable)
Trigger level resolution	8 bits
Bandwidth	400 kHz
Input impedance for EXTA_TRIG	20 kΩ
Coupling	DC
Overvoltage protection	Continuous for ±35 $V_{maximum}$
Digital trigger	
Compatibility	TTL/CMOS
Response	Rising or falling edge
Pulse width	20 ns minimum
Calibration <sup>₅</sup>	
On board reference	5 V
Temperature drift	±2 ppm/°C
Stability	±6 ppm/1000 hrs
General	
Remote interface	Hi-Speed USB 2.0
Device class	USBTMC-USB488
Programmable interface	Standard Commands for Programmable Instruments (SCPI) and IVI-COM

<sup>1</sup> When multiple channels are used in the U2355A or U2356A, the sampling rate of each channel is the maximum sampling rate divided by the number of channels used. For multiple channels used in the U2331A, the sampling rate of each channel = (1 MSa/s) / number of channels used.

<sup>2</sup> System Synchronous Interface (SSI) and Star trigger commands are used when modular devices are used in the product chassis.

<sup>3</sup> Maximum external reference voltage for analog output channels (AO\_EXT\_REF) is ±10 V.

Measurement frequency's resolution:

= 12 MHz/n, n = 2, 3, 4, 5, ..., 120 M

= 6 MHz, 4 MHz, 3 MHz, 2.4 MHz, 2.0 MHz, ..., 0.1 Hz (up to six decimal points)

<sup>5</sup> 20 minutes warm-up time is recommended.

# Electrical measurement specifications

# Basic multifunction USB DAQ

1% THD Large signal bandwidth <sup>2</sup> 300 kHz       System noise     1 mVrms     2 mVrms     1 mVrms	0 °C to 18 °C 28 °C to 45 °C ±5 mV ±5 mV 1.5 MHz 300 kHz
Function23 °C ± 5 °C28 °C to 45 °C23 °C ± 5 °COffset error±1 mV±5 mV±1 mVGain error±2 mV±5 mV±2 mV-3 dB Small signal bandwidth²760 kHz1% THD Large signal bandwidth²300 kHzSystem noise1 mVrms2 mVrms	28 °C to 45 °C ±5 mV ±5 mV 1.5 MHz 300 kHz
Gain error     ±2 mV     ±5 mV     ±2 mV       -3 dB Small signal bandwidth <sup>2</sup> 760 kHz     100 kHz       1% THD Large signal bandwidth <sup>2</sup> 300 kHz     1 mVrms       System noise     1 mVrms     2 mVrms     1 mVrms	±5 mV 1.5 MHz 300 kHz
-3 dB Small signal bandwidth2760 kHz1% THD Large signal bandwidth2300 kHzSystem noise1 mVrms2 mVrms	1.5 MHz 300 kHz
1% THD Large signal bandwidth <sup>2</sup> 300 kHz       System noise     1 mVrms     2 mVrms     1 mVrms	300 kHz
System noise 1 mVrms 2 mVrms 1 mVrms	
	2.5 mVrms
CMRR 62 dB	62 dB
Spurious-Free Dynamic Range (SFDR) <sup>3</sup> 88 dB	82 dB
Signal-to-Noise and Distortion Ratio 80 dB	78 dB
Total Harmonic Distortion (THD) <sup>3</sup> –90 dB	—82 dB
Signal-to-Noise Ratio (SNR) <sup>3</sup> 80 dB	78 dB
Effective Number of Bits (ENOB) <sup>3</sup> 13	12.6
Model number U2351A, U2353A	
Analog output measurement <sup>1</sup>	
Function         23 °C ± 5 °C         0 °C to 18 °C           28 °C to 45 °C         28 °C to 45 °C	
Offset error ±1 mV	±4 mV
Gain error ±4mV	±5 mV
Slew rate 19 V/µs	
Rise time 0.9 µs	
Fall time 0.9 µs	
Settling time to 1% output error 4 µs	
Driving capability 5 mA	
Glitch energy 5 ns-V (Typical), 80 ns-V (Maximum)	

Specifications are for 20 minutes of warm-up time, calibration temperature at 23 °C and input range of ±10 V.
 Specifications are based on the following test condition:

Bandwidth test	Model number	Test conditions (DUT setting at $\pm 10$ V bipo	lar)	
–3 dB Small signal bandwidth 1% THD large signal bandwidth	U2351A U2352A	Sampling rate: Input voltage:	250 kSa/s	
		<ul> <li>— —3 dB Small signal bandwidth</li> <li>— 1% THD Large signal bandwidth</li> </ul>	– 10% FSR – FSR –1 dB FS	
	U2353A U2354A	Sampling rate: Input voltage:	500 kSa/s	
		<ul> <li>– –3 dB Small signal bandwidth</li> <li>– 1% THD Large signal bandwidth</li> </ul>	<ul><li>– 10% FSR</li><li>– FSR –1 dB FS</li></ul>	
<sup>3</sup> Specifications are based on the follo	wing test conditions			
Dynamic range test	Model num	per Test conditions (DUT setting at $\pm 10$ V	bipolar)	
SFDR, THD, SINAD, SNR, ENOB	U2351A	<ul> <li>Sampling rate:</li> </ul>	250 kSa/s	

, ENUB	U2351A U2352A	<ul> <li>Sampling rate:</li> <li>Fundamental frequency:</li> <li>Number of points:</li> <li>Fundamental input voltage:</li> </ul>	250 KSa/S 2.4109 kHz 8192 FSR –1 dB FS	
	U2353A U2354A	<ul> <li>Sampling rate:</li> <li>Fundamental frequency:</li> <li>Number of points:</li> <li>Fundamental input voltage:</li> </ul>	500 kSa/s 4.974 kHz 16384 FSR –1 dB FS	

# High density multifunction USB DAQ

Model number	U2355A		U2356A		U2331A	
Analog input measurement <sup>1</sup>						
Function	23 °C ± 5 °C	0 °C to 18 °C 28 °C to 45 °C	23 °C ± 5 °C	0 °C to 18 °C 28 °C to 45 °C	23 °C ± 5 °C	0 °C to 18 °C 28 °C to 45 °C
Offset error	±1 mV	±2 mV	±1 mV	±2 mV	±2 mV	±3 mV
Gain error	±2 mV	±3 mV	±2 mV	±6 mV	±6 mV	±7.5 mV
–3 dB small signal bandwidth <sup>2</sup>	760 kHz		1.3 MHz		1.2 MHz	
1% THD large signal bandwidth <sup>2</sup>	400	kHz	400 kHz		N/A	
System noise	1 mVrms	2 mVrms	1 mVrms	4 mVrms	3 mVrms	5 mVrms
CMRR	64 dB		61 dB		62 dB	
Spurious-Free Dynamic Range (SFDR) <sup>3</sup>	88 dB		86 dB		71 dB	
Signal-to-Noise and Distortion Ratio (SINAD) <sup>3</sup>	80 dB		78 dB		72 dB	
Total Harmonic Distortion (THD) <sup>3</sup>	90 dB		84 dB		-76 dB	
Signal-to-Noise Ratio (SNR) <sup>3</sup>	80 dB		78 dB		72 dB	
Effective Number of Bits (ENOB) <sup>3</sup>	1	3	12.6		11.6	

 $^1$  Specifications are for 20 minutes of warm-up time, calibration temperature at 23 °C and input range of  $\pm 10$  V.  $^2$  Specifications are based on the following test conditions.

Bandwidth test	Model number	Test conditions (DUT setting at $\pm 10$ V bipolar)	
–3 dB Small signal bandwidth 1% THD Large signal bandwidth	U2355A	Sampling rate: Input voltage:	250 kSa/s
176 THE Large orginal ballawidth		<ul> <li>– –3 dB Small signal bandwidth</li> </ul>	10% FSR
		<ul> <li>1% THD Large signal bandwidth</li> </ul>	FSR –1 dB FS
	U2356A	Sampling rate: Input voltage:	500 kSa/s
		– –3 dB Small signal bandwidth	10% FSR
		<ul> <li>1% THD Large signal bandwidth</li> </ul>	FSR –1 dB FS
	U2331A	Sampling rate: Input voltage:	3 MSa/s
		– –3 dB Small signal bandwidth	10% FSR
		<ul> <li>1% THD Large signal bandwidth</li> </ul>	FSR –1 dB FS

<sup>3</sup> Specifications are based on the following test conditions.

Dynamic range test	Model number	Test conditions (DUT setting at $\pm 10$ V bipolar)	
–3 dB Small signal bandwidth 1% THD Large signal bandwidth	U2355A	<ul> <li>Sampling rate:</li> <li>Fundamental frequency:</li> <li>Number of points:</li> <li>Fundamental input voltage:</li> </ul>	250 kSa/s 2.4109 kHz 8192 FSR –1 dB FS
	U2356A	<ul> <li>Sampling rate:</li> <li>Fundamental frequency:</li> <li>Number of points:</li> <li>Fundamental input voltage:</li> </ul>	500 kSa/s 4.974 kHz 16384 FSR –1 dB FS
	U2331A	<ul> <li>Sampling rate:</li> <li>Fundamental frequency:</li> <li>Number of points:</li> <li>Fundamental input voltage:</li> </ul>	3 MSa/s 29.892 kHz 65536 FSR –1 dB FS

# High density multifunction USB DAQ (continued)

Model number Analog output measurement <sup>1</sup>	U2355A	U2355A, U2356A		331A	
Function	23 °C ± 5 °C	0 °C to 18 °C 28 °C to 45 °C	23 °C ± 5 °C	0 °C to 18 °C 28 °C to 45 °C	
Offset error	±1 mV	±4 mV	±1.5 mV	±3 mV	
Gain error	±4 mV	±5 mV	±4 mV	±5 mV	
Slew rate	19	19 V/µs		19 V/µs	
Rise time	0.9	0.9 µs		) µs	
Fall time	0.9	) µs	0.9	) µs	
Settling time to 1% output error	4	4 µs		μs	
Driving capability	5 mA		5	mA	
Glitch energy	5 ns-V (Typical), 8	30 ns-V (Maximum)	5 ns-V (Typical), 8	0 ns-V (Maximum)	

<sup>&</sup>lt;sup>1</sup> Specifications are for 20 minutes of warm-up time, calibration temperature at 23 °C and input range of ±10 V.

# DC Characteristics

## Accuracy specifications

Model Number	lumber U2351A, U2352A, U2353A, U2354A				
		Analog Input			
Unipolar Range (V)	Offset Error (mV) <sup>1</sup>	Gain Error (mV)	Accuracy (% of reading + offset error) <sup>2</sup>		
10	1.5	2.0	0.04% + 1.5 mV		
5	1.5	2.0	0.08% + 1.5 mV		
2.5	1.0	1.0	0.08% + 1.0 mV		
1.25	1.0	1.0	0.16% + 1.0 mV		
Bipolar Range (V)	Offset Error (mV) <sup>1</sup>	Gain Error (mV)	Accuracy (% of reading + offset error) <sup>2</sup>		
10	1.0	2.0	0.02% + 1.0 mV		
5	1.0	2.0	0.04% + 1.0 mV		
2.5	1.0	1.5	0.06% + 1.0 mV		
1.25	1.0	1.5	0.12% + 1.0 mV		
Model Number		U2355A, U	2356A		
Unipolar Range (V)	Offset Error (mV) <sup>1</sup>	Gain Error (mV)	Accuracy (% of reading + offset error) <sup>2</sup>		
10	1.0	1.5	0.03% + 1.0 mV		
5	1.0	1.5	0.06% + 1.0 mV		
2.5	1.0	1.0	0.08% + 1.0 mV		
1.25	1.0	1.0	0.16% + 1.0 mV		
Bipolar Range (V)	Offset Error (mV) <sup>1</sup>	Gain Error (mV)	Accuracy (% of reading + offset error) <sup>2</sup>		
10	1.0	2.0	0.02% + 1.0 mV		
5	1.0	2.0	0.04% + 1.0 mV		
2.5	1.0	1.5	0.06% + 1.0 mV		
1.25	1.0	1.5	0.12% + 1.0 mV		
Model		U2331	IA		
Unipolar Range (V)	Offset Error (mV) <sup>1</sup>	Gain Error (mV)	Accuracy (% of reading + offset error) <sup>2</sup>		
10	1.5	4.0	0.08% + 1.5 mV		
5	1.5	2.0	0.08% + 1.5 mV		
4	1.5	2.0	0.10% + 1.5 mV		
2.5	1.0	1.5	0.12% + 1.0 mV		
2	1.0	1.0	0.10% + 1.0 mV		
1	1.0	1.0	0.20% + 1.0 mV		
0.5	1.0	1.0	0.41% + 1.0 mV		
0.4	1.0	1.0	0.51% + 1.0 mV		
0.1	1.0	1.0	2.04% + 1.0 mV		

 $-\,$  The above specifications are typical for 23 °C.

Specifications are for 20 minutes warm-up and self calibration.
 The measurement are calculated with 100 points avearaging of data.

<sup>&</sup>lt;sup>1</sup> Offset error is measured at midscale of full range.

<sup>&</sup>lt;sup>2</sup> Accuracy =  $\pm$  [% of |(Gain error / (Measured value – Midscale of FSR))| + Offset error]

## Accuracy specifications (continued)

Model		U2331	Α
Bipolar Range (V)	Offset Error (mV) <sup>1</sup>	Gain Error (mV)	Accuracy (% of reading + offset error) <sup>2</sup>
10	2.0	6.0	0.06% + 2.0 mV
5	1.5	4.0	0.08% + 1.5 mV
2.5	1.5	2.0	0.08% + 1.5 mV
1.25	1.0	1.5	0.12% + 1.0 mV
1	1.0	1.0	0.10% + 1.0 mV
0.5	1.0	1.0	0.20% + 1.0 mV
0.25	1.0	1.0	0.40% + 1.0 mV
0.2	1.0	1.0	0.50% + 1.0 mV
0.05	1.0	1.0	2.02% + 1.0 mV

 $-\,$  The above specifications are typical for 23 °C.

Specifications are for 20 minutes warm-up and self calibration.
 The measurement are calculated with 100 points averaging of data.

Model		U2351A, U2352A, U	2353A, U2354A
		Analog output	
Unipolar Range (V)	Offset Error (mV) <sup>3</sup>	Gain Error (mV)	Accuracy (% of reading + offset error) <sup>4</sup>
10	1.0	2.0	0.02% + 1.0 mV
Bipolar Range (V)	Offset Error (mV) <sup>3</sup>	Gain Error (mV)	Accuracy (% of reading + offset error) <sup>4</sup>
10	1.0	4.0	0.04% + 1.0 mV
Model		U2355A, U	2356A
Unipolar Range (V)	Offset Error (mV) <sup>3</sup>	Gain Error (mV)	Accuracy (% of reading + offset error) <sup>4</sup>
10	1.0	2.0	0.02% + 1.0 mV
Bipolar Range (V)	Offset Error (mV) <sup>3</sup>	Gain Error (mV)	Accuracy (% of reading + offset error) <sup>4</sup>
10	1.0	4.0	0.04% + 1.0 mV
Model		U233	1A
Unipolar Range (V)	Offset Error (mV) <sup>3</sup>	Gain Error (mV)	Accuracy (% of reading + offset error) <sup>4</sup>
10	2.5	4.0	0.04% + 2.5 mV
Bipolar Range (V)	Offset Error (mV) <sup>3</sup>	Gain Error (mV)	Accuracy (% of reading + offset error) <sup>4</sup>
10	1.5	4.0	0.04% + 1.5 mV

 $-\,$  The above specifications are typical for 23 °C.

- Specifications are for 20 minutes warm-up and self calibration.

<sup>&</sup>lt;sup>1</sup> Offset error is measured at midscale of full range.

<sup>2</sup> Accuracy =  $\pm$  [% of |(Gain error / (Measured value – Midscale of FSR))| + Offset error]

<sup>&</sup>lt;sup>3</sup> Offset error is measured at 0 V.

<sup>&</sup>lt;sup>4</sup> Accuracy =  $\pm$  [% of |Gain error/Output value| + Offset voltage]

# Keysight Measurement Manager

The Keysight Measurement Manager (AMM) is an application data viewer software that comes with the standard purchase of the U2300A Series USB modular data acquisition. This software is designed to help you perform quick device configuration, data logging and data acquisition using the products.

Supported features found in the U2300A Series USB modular multifunction data acquisition devices:

- Averaging
- Command logger
- Self-calibration
- Option to save the current instrument configuration to a file
- Data logging and export feature to CSV, HTML and text only format files that can be printed
- Data viewer to load and review previously logged data
- Trigger settings between modules in the instrument chassis with Star trigger and Master/Slave trigger
- Synchronization display and data logging for modules in the instrument chassis

## Keysight Measurement Manager prerequisites

Prior to installing the Keysight Measurement Manager software, ensure that your PC meets the following minimum system requirements for installation and operations.

Requirement	Windows XP operating systems	Windows Vista operating systems	Windows 7 operating systems
Operating system	Windows XP Service Pack 3 (or later) <sup>1</sup>	Windows Vista (32-bit) Service Pack 1 and 2 <sup>2</sup>	Windows 7 (32-bit and 64-bit) <sup>3.4</sup>
Processor speed	600 MHz or higher required, 800 MHz recommended	1 GHz 32-bit (x86)	3 GHz 32-bit (x86)
Memory	256 MB minimum (1 GB or greater recommended)	1 GB minimum	2 GB minimum
Hard-disk space	1.5 GB minimum	1.5 GB minimum	1.5 GB minimum
Video	Super VGA (800 × 600) 256 colors or more	Support for DirectX 9 graphics with 128 MB graphics memory recommended <sup>5</sup>	Support for DirectX 9 graphics with 128 MB graphics memory recommended <sup>5</sup>
CD-ROM drive or DVD-ROM drive <sup>6</sup>	Required	Required	Required
Browser	Microsoft Internet Ex- plorer 5.01 or greater	Microsoft Internet Explorer 7 or greater	Microsoft Internet Explorer 7 or greater

Supported Windows XP editions — Home or Professional

<sup>2</sup> Supported Windows Vista (32-bit) editions — Home Basic, Home Premium, Business, or Ultimate

- <sup>3</sup> Supported Windows 7 (32-bit and 64-bit) editions Home Basic, Home Premium, Professional, Enterprise, or Ultimate
- <sup>4</sup> Keysight Measurement Manger for Windows 7 64-bit support is a 32-bit application running on a WOW64 (Windows-on-Windows 64-bit) emulator.
- <sup>5</sup> Super VGA graphics is supported for Windows Vista and Windows 7.
- <sup>3</sup> The type of media provided with the product determines whether a CD-ROM drive or DVD-ROM drive is required.

#### Software requirements

Keysight IO Libraries Suite 15.1 and above<sup>1</sup>

Keysight T&M Toolkit Runtime version 2.1<sup>2</sup>

#### Keysight T&M Toolkit Redistributable Package 2.1 patch<sup>2</sup>

#### Microsoft .NET Framework version 2.0<sup>2</sup>

Available on the Keysight Automation-Ready CD-ROM.

Bundled with Keysight Measurement Manager software application installer.

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# Other products in the Keysight USB Modular Data Acquisition (DAQ) Family





Features:

- $-\,$  High analog input sampling rate coverage of up to 2 MSa/s for a each channel
- High speed USB 2.0
- Simultaneous acquisition of multiple data points
- Multifunction capabilities analog input (AI), analog output (AO), digital input output (DIO), and counter

For more information: www.keysight.com/find/U2500A



Features:

- 64 opto-isolated lines that can meet demand up to 24 V
- High speed USB 2.0

Isolation voltage of 1250 Vrms for protection from transient voltage spikes
 For more information: www.keysight.com/find/U2600A

## U2781A USB Modular Product Chassis

#### Features:

- Expansion of channels for each modular product
- Multiple instrument synchronization
- Internal and external 10 MHz reference clock
- High-speed USB 2.0
- SSI/Star trigger bus synchronization between external trigger source and modules

For more information: www.keysight.com/find/U2781A



# **Optional Accessories**

The Keysight U2802A is a 31-channel thermocouple signal conditioning module with a built-in thermistor for cold junction compensation. The U2802A is designed to convert low input voltage signals (less than ±100 mV) from a thermocouple into an output voltage range suitable for data acquisition devices (±10 V). The U2802A device is to be used in conjunction with the Keysight U2355A or U2356A model DAQ device to enable temperature measurements using thermocouples. It works as a standalone device attached to a single DAQ device via two SCSI-II 68 conductor cables. Designed with the compatibility to complement eight standard thermocouple types, it caters to a wide range of applications in various industrial settings.

### Features to meet your demands

- 31 input channels that can be independently configured to either differential thermocouple input mode, single-ended voltage input mode, or differential voltage input mode using two input channels set to voltage input mode
- Supports the standard thermocouple types (J, K, R, S, T, N, E, and B) defined in the NIST ITS-90 Thermocouple Database
- Error detection for open thermocouple channels
- Built-in isothermal construction on terminal block for improved measurement accuracy
- Built-in thermistor for cold junction compensation
- Built-in zeroing function to compensate for overall system offset errors due to temperature drift and long term drift
- Up to ±10 V input voltage range for higher voltage inputs
- Sampling rate of 500 kSa/s for overall module
- Sampling rate of 10 kSa/s total for all channels in thermocouple mode
- Quick and easy USB setup
- Robust, cost-effective, and user friendly

## Applications

The U2802A thermocouple input device is designed for robust and demanding industrial applications. This product is suitable and ideal for thermocouple measurement applications such as;

- Product thermal analysis and characterization
- Environmental chamber profiling
- Process monitoring in consumer electronics markets
- Material properties testing in education environments
- Study of electronic temperature properties
- Appliances testing



### Thermocouple input mode

In thermocouple input mode, the U2802A can acquire up to ±100 mV input signals. Each channel includes an instrumentation amplifier and a 4 Hz low-pass filter. The low-pass filter removes unwanted noise from the thermocouple wires to obtain accurate measurement data.

## Voltage input mode

Alternatively, you can select separate voltage input modes for each channel. The channel will be set to bypass the amplifier and filter, allowing up to  $\pm 10V$  input signals to be directly routed to the DAQ device analog input. The bandwidth in this mode is more than 500 kHz.

## Zero mode

In zero mode, the positive and negative inputs of the instrumentation amplifier are shorted together. The voltage measured in this mode corresponds to the offset voltage of the channel. You can subtract this offset voltage from subsequent thermocouple mode measurements to increase measurement accuracy. This mode is only applicable in thermocouple mode.

## Thermocouple compatibility

The U2802A is compatible with a wide range of standard thermocouple types defined in the NIST ITS-90 Thermocouple Database. This includes types J, K, R, S, T, N, E, and B.

# Open thermocouple detection

The U2802A includes open thermocouple detection circuitry to indicate the presence of an open thermocouple.

## Calibration EEPROM

The U2802A gain and offset calibration factors for each channel are stored in the EEPROM during factory calibration and can be retrieved prior to taking measurements. This on-board EEPROM also stores the module ID, serial number, and date of calibration for your reference. A section of the EEPROM is also allocated for you to save your calibration data.

## Restoring factory calibration

Using the AMM software, you can easily restore the U2802A calibration data from your settings to the original factory settings.



# Product outlook and dimensions

#### Front view



#### **Rear view**



#### Top and side view



# Standard shipped accessories

- Power supply splitter
- Two 68-pin SCSI cables (1 m)
- One J-type thermocouple
- Keysight U2802A 31-Channel Thermocouple Input Device Quick Start Guide
- Keysight USB Modular Products Reference CD-ROM
- Keysight Automation-Ready CD-ROM (contains the Keysight IO Libraries Suite)
- Certificate of Calibration

## Product characteristics and general specifications

#### Power consumption

+12 VDC, 480 mA maximum

#### Operating environment

- Operating temperature from 0 °C to +55 °C
- Relative humidity at 50% to 85% RH (non-condensing)
- Altitude up to 2000 meters

## Storage compliance

–40 °C to 70 °C

#### Safety compliance

Certified with:

IEC 61010-1:2001/EN 61010-1:2001 (2nd Edition)

#### EMC compliance

- IEC 61326-1:2002/EN 61326-1:1997+A2:2001+A3:2003
- CISPR 11: 1990/EN 55011:1990-Group 1 Class A
- Canada: ICES-001:2004
- Australia/New Zealand: AS/NZS CISPR 11:2004

#### Shock and vibration

Tested to IEC/EN 60068-2

#### IO connector

- 2 × 68-pin female SCSI connector
- $-2 \times 34$  pin screw terminal block
- 1 × 24 pin screw terminal block

#### Dimension ( $W \times D \times H$ )

158.70 mm × 254.20 mm × 40.50 mm

#### Weight

1.036 kg

#### Warranty

- Three years for U2802A
- Three months for standard shipped accessories

# Product specifications

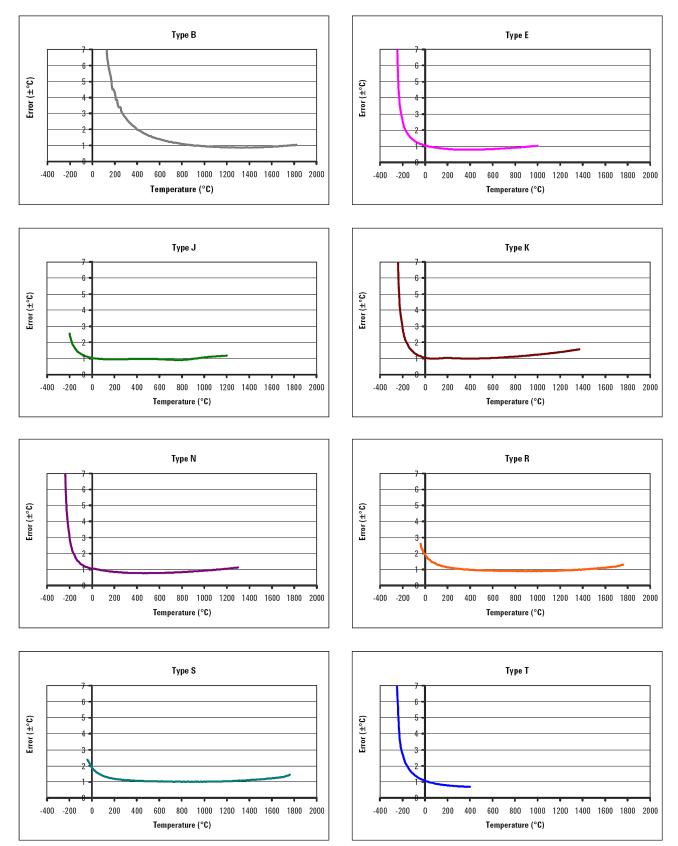
General characteristics	
Number of channels	31 differential and 1 CJC
Input voltage range for voltage mode	±10 V (signal + common mode)
Input voltage (thermocouple mode)	±100 mV
Sampling rate for thermocouple mode	10 kSa/s total for all channels
Sampling rate for overall module	500 kSa/s
Thermocouple types	J, K, R, S, T, N, E, and B
Input specifications	
Accuracy (thermocouple mode) – Overall gain error – Overall offset error – Nonlinearity	0.06% (23 °C ± 5 °C ) 15 μV (without zeroing) (23 °C ± 5 °C ) 6 μV (with zeroing) < 0.005% of full scale range
System noise (rms) – Gain (× 1) – Gain (× 100)	100 μVrms 5 μVrms
Common Mode Rejection Ratio (CMRR) – Voltage mode – Thermocouple mode	> 60 dB > 80 dB
Cold junction accuracy	±1.0 °C typical (23 °C ± 5 °C ) ±1.5 °C typical (0 °C to 18 °C, 28 °C to 55 °C)
Input characteristics	
Bandwidth (voltage mode)	> 500 kHz
Bandwidth (thermocouple mode)	4.0 Hz
Overvoltage protection <sup>1</sup>	<ul> <li>TC Mode<sup>2</sup></li> <li>Common mode: ±17 V (TC+ and TC- with respect to GND)</li> <li>Differential mode: ±7 V (Differential voltage between TC+ and TC-)</li> <li>Bypass mode     ±20 V (TC+ input with respect to GND)</li> <li>Power-off Mode     ±11 V (TC+, TC- input with respect to GND)</li> </ul>
Input impedance	> 1 GΩ
Input bias current	±2.5 nA max
Input offset current	±1.5 nA max
Gain drift	60 ppm/°C max
Offset drift	1 μV/°C max
Offset drift Filter cutoff frequency (–3 dB) (thermocouple mode)	1 μV/°C max 4.0 Hz
Filter cutoff frequency (-3 dB)	
Filter cutoff frequency (–3 dB) (thermocouple mode)	4.0 Hz

<sup>1</sup> The overvoltage protection levels specified above indicate the maximum voltage each input pin can tolerate without resulting in any damages. However, prolonged exposure to these levels may affect device safety and reliability. Hence, it should be avoided where possible.

<sup>&</sup>lt;sup>2</sup> On the channels configured for thermocouple mode, the TC+ and TC- pins can tolerate up to  $\pm 17$  V of differential voltage for a few minutes. However, exceeding a voltage range of  $\pm 100$  mV on these channels can cause additional current to be drawn from the device's power supply regulators, which may damage the device if multiple channels are overdriven for prolonged periods. This is the case when a voltage source is tied across the TC<sub>n</sub> + and TC<sub>n</sub> - pin. Voltage sources greater than  $\pm 100$  mV should be tied to TC<sub>n</sub> + and GND (floating source), or TC<sub>n</sub> + and TC<sub>n+1</sub> + (grounded source), and have the channels set for bypass mode.

# Thermocouples typical measurement accuracy

The U2802A measurement error with U2355A or U2356A at 23 °C  $\pm$  5 °C is shown below.



# System accuracy specifications

The U2802A system accuracy specifications are shown in Table 1, Table 2, and Table 3. These measurements are derived from the U2802A and DAQ input accuracy specifications without including the thermocouple error. Refer to *"Calculating System Accuracy"* section in the Keysight U2802A 31-Channel Thermocouple Input Device User's Guide for calculation methodology.

Table 1. Measurement accuracy of the U2355A and U2356A at 23 °C  $\pm$  5 °C

T/C type	ITS-90 Temper	ature range (°C)	Optimum m rang	easurement e(°C)	Without averaging	50 points averaging	500 points averaging
., • • • • • •	Low	High	Low	High	(± °C)	(± °C)	(± °C)
В	0	1820	1100	1820	1.9	1.2	1.0
			400	1100	4.4	2.5	2.0
Е	-270	1000	-150	1000	1.7	1.6	1.6
			-200	-150	2.4	2.3	2.3
J	-210	1200	-150	1200	1.6	1.5	1.5
			-210	-150	2.7	2.6	2.5
К	-270	1372	-100	1200	1.5	1.4	1.4
			-200	-100	2.7	2.6	2.6
Ν	-270	1300	-100	1300	1.5	1.3	1.3
			-200	-100	3.0	2.7	2.6
R	-50	1768	300	1760	2.0	1.4	1.3
			-50	300	5.0	3.1	2.6
S	-50	1768	400	1760	2.1	1.6	1.4
			-50	400	4.5	2.8	2.4
Т	-270	400	-100	400	1.5	1.4	1.4
			-200	-100	2.7	2.5	2.5

## Thermocouple measurement accuracy (U2355A, U2356A at 23 °C ± 5 °C)

## Table 2. Measurement accuracy of the U2355A at 0 to 18 °C and 28 to 45 °C

#### Thermocouple measurement accuracy (U2355A at 0 to 18 °C and 28 to 45 °C)

T/C type	ITS-90 Temperature ra	inge (°C)	•	surement range C)	Without  averaging	50 points averaging	500 points averaging
	Low H	ligh	Low	High	(± °C)	(± °C)	(± °C)
В	0 1	820	1100	1820	3.4	2.4	2.2
			400	1100	7.5	3.6	2.2
E	-270 1	000	-150	1000	2.7	2.6	2.5
			-200	-150	3.8	3.6	3.6
J	-210 1	200	-150	1200	2.5	2.4	2.4
			-210	-150	4.2	4.0	3.9
К	-270 1	372	-100	1200	2.9	2.8	2.8
			-200	-100	4.3	4.0	3.9
Ν	-270 1	300	-100	1300	2.6	2.5	2.5
			-200	-100	4.9	4.2	4.0
R	50 1	768	300	1760	3.8	3.1	3.0
			-50	300	8.5	4.6	3.3
S	-50 1	768	400	1760	4.2	3.4	3.2
			-50	400	7.7	4.2	3.1
Т	-270	400	-100	400	2.4	2.2	2.2
			-200	-100	4.3	4.3	3.9

	Ther	mocouple measu	rement accuracy	(U2356A @ 0 to 1	8 °C and 28 to 4	5 °C)	
T/C type	ITS-90 Temperature range (°C)		Optimum meas (°	-	Without averaging	50 points averaging	500 points averaging
	Low	High	Low	High	(± °C)	(± °C)	(± °C)
В	0	1820	1100	1820	6.1	3.1	2.4
			400	1100	14.4	6.3	2.7
E	-270	1000	-150	1000	3.0	2.6	2.6
			-200	-150	4.2	3.7	3.6
J	-210	1200	-150	1200	2.9	2.5	2.5
			-210	-150	4.9	4.1	4.0
К	-270	1372	-100	1200	3.3	2.9	2.9
			-200	-100	5.3	4.2	4.0
Ν	-270	1300	-100	1300	3.4	2.7	2.6
			-200	-100	6.8	4.6	4.1
R	-50	1768	300	1760	6.2	3.7	3.2
			-50	300	15.7	7.2	3.8
S	-50	1768	400	1760	6.4	4.0	3.4
			-50	400	14.2	6.6	3.4
Т	-270	400	-100	400	3.0	2.4	2.2
			-200	-100	5.3	4.2	3.9

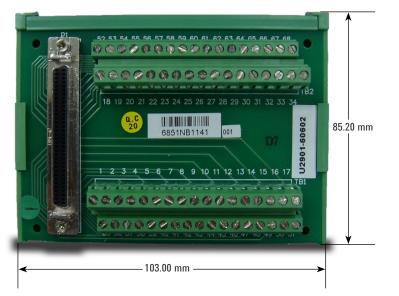
# Table 3. Measurement accuracy of the U2356A at 0 to 18 $^{\circ}\mathrm{C}$ and 28 to 45 $^{\circ}\mathrm{C}$

# U2901/U2902A -Terminal block and SCSI- II 68-pin connector with 1-meter/2-meter cable

The U2901A/U2902A is a terminal block and SCSI-II 68-pin connector with 1 meter cable or 2 meter cable that can be used conjunction with the U2300A Series and U2500A Series.

## Terminal block overview

#### **Front view**





# Ordering information

Model	Description
U2351A	16-Channel 250kSa/s USB modular multifunction DAQ
U2352A	16-Channel 250kSa/s USB modular multifunction DAQ; without analog output
U2353A	16-Channel 500kSa/s USB modular multifunction DAQ
U2354A	16-Channel 500kSa/s USB modular multifunction DAQ; without analog output
U2355A	64-Channel 250kSa/s USB modular multifunction DAQ
U2356A	64-Channel 500kSa/s USB modular multifunction DAQ
U2331A	64-Channel 1MSa/s USB modular multifunction DAQ

# Optional accessories

Model	Description
U2802A	U2802A 31-Channel Thermocouple Input Device
U2901A	Terminal block and SCSI-II 68-pin connector with 1-meter cable
U2902A	Terminal block and SCSI-II 68-pin connector with 2-meter cable

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