

MGBR10S100C

Preliminary

DIODE

DUAL MOS GATED BARRIER RECTIFIER

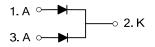
DESCRIPTION

The UTC **MGBR10S100C** is a dual mos gated barrier rectifiers, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

FEATURES

*Super low forward voltage drop * High switching speed

SYMBOL

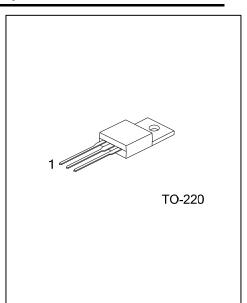


ORDERING INFORMATION

Ordering Number		Deekege	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
MGBR10S100CL-TA3-T MGBR10S100CG-TA3-T		TO-220	А	К	А	Tube	

Note: Pin Assignment: A: Anode, K: Cathode

(1) T: Tube
(2) TA3: TO-220
(3) L: Lead Free, G: Halogen Free



■ ABSOLUTE MAXIMUM RATINGS (PER LEG) (T_A=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%

PARAMETER		SYMBOL	RATINGS	UNIT		
DC Blocking Voltage		V _{RM}	100	V		
Working Peak Reverse Voltage		V _{RWM}	100	V		
Peak Repetitive Reverse Voltage		V _{RRM}	100	V		
Average Rectified Output Current Per Device	Per Leg	lo	5	А		
	Total		10	А		
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load		I _{FSM}	180	А		
Operating Junction Temperature		TJ	-65~+150	°C		
Storage Temperature		T _{STG}	-65~+150	°C		

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

THERMAL CHARACTERISTICS (PER LEG)

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ _{JA}	62.5	°C/W	
Junction to Case	θ」	2	°C/W	

■ ELECTRICAL CHARACTERISTICS (PER LEG) (T_A =25°C unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	V _{(BR)R}	I _R =0.50mA	100			V
Forward Voltage Drop	V _{FM}	I _F =5A, T _J =25°C			0.67	V
		I _F =5A, T _J =125°C			0.62	V
Leakage Current (Note 1)	DM	V _R =100V, T _J =25°C			200	μA
		V _R =100V, T _J =125°C			100	mA

Notes: 1. Short duration pulse test used to minimize self-heating effect.

2. Thermal resistance junction to case mounted on heatsink.



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