

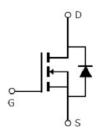
Main Product Characteristics

V _{DSS}	700V
R _{DS} (on)	1.3Ω (typ.)
I _D	5A ①





Assignment



Schematic Diagram

Features and Benefits

- High dv/dt and avalanche capabilities
- 100% avalanche tested
- Low input capacitance and gate charge
- Low gate input resistance



Description:

The SSF5NS70GB series MOSFETs is a new technology, which combines an innovative technology and advance process. This new technology achieves low Rdson, energy saving, high reliability and uniformity, superior power density and space saving.

Absolute Max Rating

Symbol	Parameter	Max.	Units	
I _D @ TC = 25°C	Continuous Drain Current, V _{GS} @ 10V	5 ①		
I _D @ TC = 100°C	Continuous Drain Current, V _{GS} @ 10V	3.1①	Α	
I _{DM}	Pulsed Drain Current ②	15		
D @TC 25°C	Power Dissipation ③	50	W	
P _D @TC = 25°C	Linear Derating Factor	0.4	W/°C	
V _{DS}	Drain-Source Voltage	700	V	
V _{GS}	Gate-to-Source Voltage	± 30	V	
E _{AS}	Single Pulse Avalanche Energy @ L=22.4mH	54	mJ	
I _{AR}	Avalanche Current @ L=22.4mH	2.2	А	
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to +150	°C	



Thermal Resistance

Symbol	Characteristics	Тур.	Max.	Units	
R ₀ JC	Junction-to-case 3	For TO-251S PKG	_	2.5	℃W
R _{θJA}	Junction-to-ambient (t \leq 10s) \oplus	For TO-251S PKG	_	75	°C/W

Electrical Characteristics @TA=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source breakdown voltage	700	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
D	Static Drain-to-Source on-resistance	_	1.3	1.6	Ω	$V_{GS}=10V,I_D=1A$
R _{DS(on)}	Static Dialif-to-Source off-resistance	_	3.1	_		T _J = 125°C
V	Cata threshold voltage	2	_	4	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
$V_{GS(th)}$	Gate threshold voltage	_	2.8	_	V	T _J = 125°C
1	Drain to Source leakage current	_	_	1		$V_{DS} = 700V, V_{GS} = 0V$
I _{DSS}	Drain-to-Source leakage current	_	_	50	μA	T _J = 125°C
1	Cata ta Sauraa farurard laakaga	_	_	100	- Λ	V _{GS} =30V
I _{GSS}	Gate-to-Source forward leakage	_	_	-100	nA	V _{GS} = -30V
Q_g	Total gate charge	_	8.3	_		$I_D = 4A$,
Q _{gs}	Gate-to-Source charge	_	2.3	_	nC	V _{DS} =100V,
Q_{gd}	Gate-to-Drain("Miller") charge	_	2.6	_		V _{GS} = 10V
t _{d(on)}	Turn-on delay time	_	10	_		
t _r	Rise time	_	18	_		V_{GS} =10V, V_{DS} =380V,
t _{d(off)}	Turn-Off delay time	_	17	_	ns	$R_{GEN}=18\Omega, I_D=4.5A$
t _f	Fall time	_	15	_		
C _{iss}	Input capacitance	_	272	_		V _{GS} = 0V
Coss	Output capacitance	_	168	_	pF	$V_{DS} = 25V$
C _{rss}	Reverse transfer capacitance	_	3.14	_		f = 1MHz

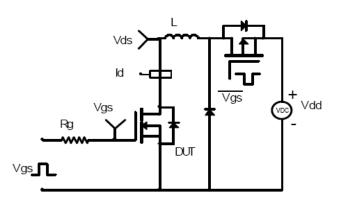
Source-Drain Ratings and Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
	Continuous Source Current			.	۸	MOSFET symbol
Is	(Body Diode)	_	_	5 ①	А	showing the
Ism	Pulsed Source Current			15	А	integral reverse
	(Body Diode)	_	_			p-n junction diode.
V _{SD}	Diode Forward Voltage	_	0.84	1.2	V	I _S =2.8A, V _{GS} =0V
t _{rr}	Reverse Recovery Time	_	284	_	nS	$T_J = 25^{\circ}C, I_F = I_S,$
Q _{rr}	Reverse Recovery Charge	_	1395	_	nC	di/dt = 100A/µs

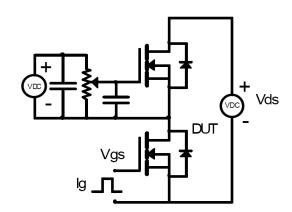


Test circuits and Waveforms

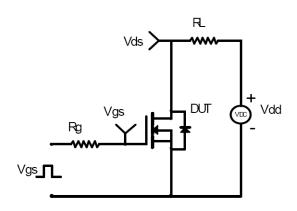
EAS Test Circuit:



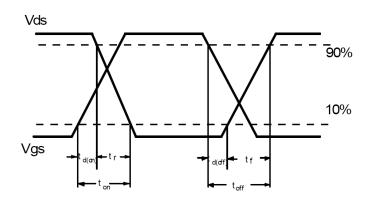
Gate charge test circuit:



Switching Time Test Circuit:



Switching Waveforms:



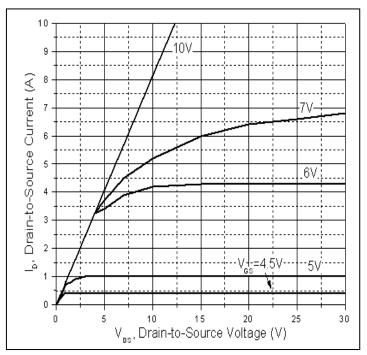
Version: 1.0P

Notes:

- ①Calculated continuous current based on maximum allowable junction temperature.
- ②Repetitive rating; pulse width limited by max. junction temperature.
- ③The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.
- 4The value of $R_{\texttt{6JA}}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with TA =25°C



Typical electrical and thermal characteristics



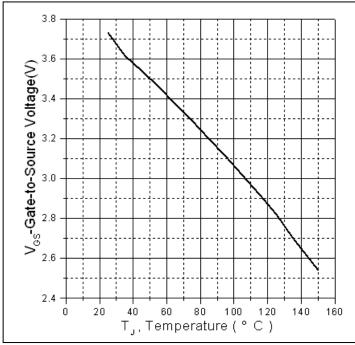
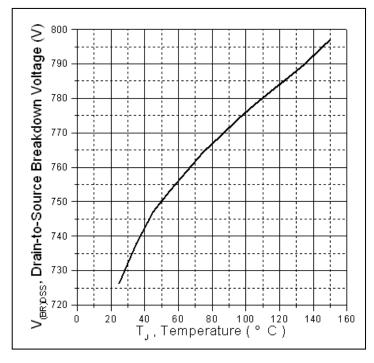
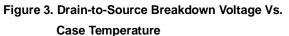


Figure 1: Typical Output Characteristics

Figure 2. Gate to source cut-off voltage





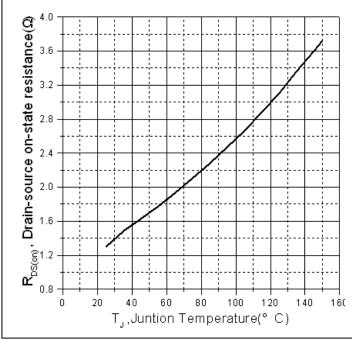
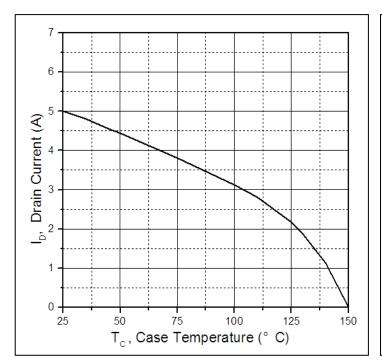


Figure 4: Normalized On-Resistance Vs. Case Temperature





Typical electrical and thermal characteristics



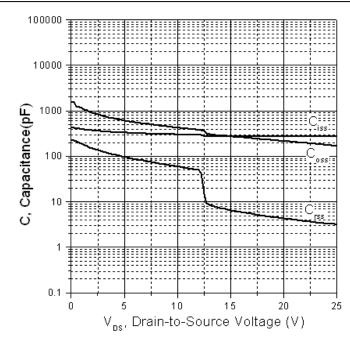
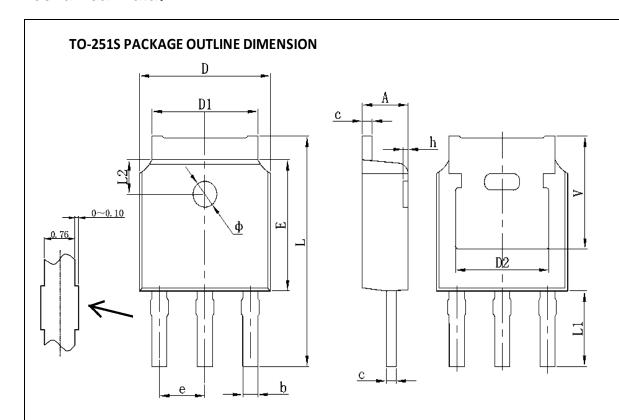


Figure 5. Maximum Drain Current Vs. Case Temperature

Figure 6. Typical Capacitance Vs. Drain-to-Source Voltage



Mechanical Data:



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	2.200	2.400	0.087	0.094	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830	REF.	0.190 REF.		
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	10.400	11.000	0.409	0.433	
L1	3.500 REF.		0.138	REF.	
L2	1.600 REF.		0.063	REF.	
Φ	1.100	1.300	0.043	0.051	
h	0.000	0.300	0.000	0.012	
V	5.350	REF.	0.211	REF.	





Ordering and Marking Information

Device Marking: SSF5NS70GB

Package (Available)
TO-251S
Operating Temperature Range
C: -55 to 150 °C

Devices per Unit

Package	Units/	Tubes/Inner	Units/Inner	Inner	Units/Carton
Туре	Tube	Box	Box	Boxes/Carton	Box
				Box	

Reliability Test Program

Test Item	Conditions	Duration	Sample Size
High	T _j = 150℃ @ 80% of	168 hours	3 lots x 77 devices
Temperature	Max V _{DSS} /V _{CES} /VR	500 hours	
Reverse		1000 hours	
Bias(HTRB)			
High	T _j =150℃ @ 100% of	168 hours	3 lots x 77 devices
Temperature	Max V _{GSS}	500 hours	
Gate		1000 hours	
Bias(HTGB)			





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