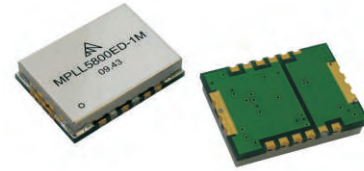


Features

- Standard package 20.0*15.0*4.0mm
- Low power supply +5V or +3V
- Frequency 50~7500MHz, relative bandwidth up to 20%



Part number	Frequency MHz	Power output dBm	Step size kHz	Switching time mS	Harmonic output 2nd dBc	Spurious output dBc	Phase noise @10/100kHz dBc/Hz	Vpll/lpll V/mA	Vvco/lvco V/mA
MPLL55AD-10k	50-60	2±2	10	<15	-30	-75	-110/-130	+5/5	+5/15
MPLL105AD-20k	100-110	0±2	20	<30	-30	-75	-112/-133	+5/5	+5/15
MPLL140AD-20k	135-145	0±2	20	<30	-20	-75	-113/-135	+5/5	+5/15
MPLL150AD-20k	145-155	0±2	20	<30	-20	-75	-113/-135	+5/5	+5/15
MPLL290AD-20k	285-295	0±2	20	<30	-20	-75	-113/-135	+5/5	+5/15
MPLL385BD-25k	380-390	0±2	25	<15	-15	-75	-111/-132	+5/7	+5/20
MPLL615BD-25k	610-620	0±2	25	<15	-15	-75	-111/-132	+5/7	+5/20
MPLL755BD-30k	730-780	0±2	30	<15	-15	-75	-108/-128	+5/7	+5/20
MPLL836BD-30k	824-849	0±2	30	<15	-15	-75	-108/-128	+5/7	+5/20
MPLL881BD-30k	869-894	0±2	30	<15	-15	-75	-109/-129	+5/7	+5/20
MPLL1015BD-30k	990-1040	0±2	30	<15	-15	-75	-108/-128	+5/7	+5/20
MPLL1620BD-50k	1590-1650	0±2	50	<15	-15	-75	-102/-122	+5/7	+5/28
MPLL1777BD-200k	1747-1807	2±2	200	<6	-15	-75	-90/-117	+5/7	+5/28
MPLL1810BD-50k	1780-1840	0±2	50	<15	-15	-75	-103/-123	+5/7	+5/28
MPLL2140BD-200k	2110-2170	2±2	200	<6	-15	-75	-90/-117	+5/7	+5/28
MPLL2280BD-50k	2240-2320	0±2	50	<15	-25	-75	-101/-121	+5/7	+5/28
MPLL2386BD-200k	2341-2431	3±2	200	<5	-25	-75	-92/-120	+5/7	+5/28
MPLL2650CD-200k	2600-2700	2±2	200	<5	-25	-75	-92/-117	+5/12	+5/28
MPLL2800CD-200k	2700-2900	2±2	200	<5	-25	-75	-92/-117	+5/12	+5/28
MPLL3250CD-250k	3200-3300	2±2	250	<5	-25	-75	-92/-117	+5/12	+5/30
MPLL3550ED-250k	3500-3600	2±2	250	<5	-25	-75	-90/-115	+3.3/17	+5/30
MPLL4450ED-500k	4400-4500	0±2	500	<5	-25	-75	-90/-113	+3.3/17	+5/35
MPLL5150ED-500k	5100-5200	0±2	500	<5	-25	-75	-85/-111	+3.3/17	+5/35
MPLL5800ED-1M	5750-5850	0±2	1000	<5	-25	-70	-85/-110	+3.3/17	+5/35
MPLL7200FD-500k	7160-7240	0±2	500	<5	-25	-70	-82/-108	+3.3/17	+5/35

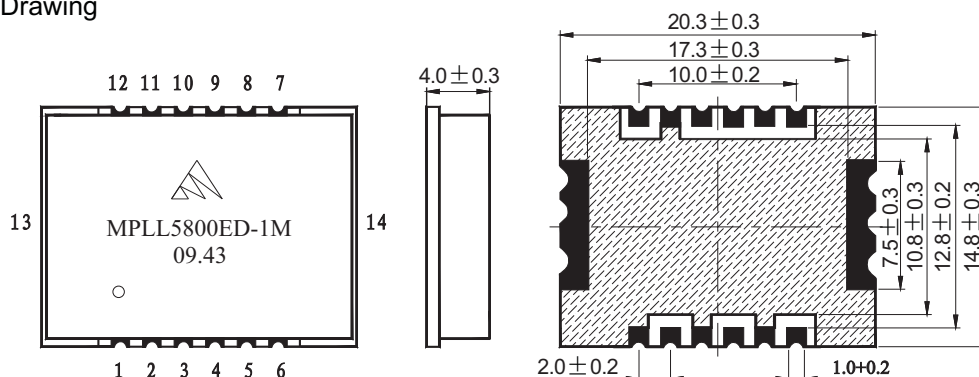
Part number	Frequency MHz	Power output dBm	Step size kHz	Switching time mS	Harmonic output 2nd dBc	Spurious output dBc	Phase noise @10/800kHz dBc/Hz	Vpll/lpll V/mA	Vvco/lvco V/mA
MPLL749CD-200k	719-778	5±2	200	<0.45	-20	<-80	-90/-147	+3.3/13	+5/40
MPLL825CD-200k	805-844	5±2	200	<0.45	-20	<-80	-90/-147	+3.3/13	+5/40
MPLL1001CD-200k	976-1025	5±2	200	<0.45	-20	<-80	-90/-145	+3.3/13	+5/40
MPLL1066CD-200k	1046-1085	5±2	200	<0.45	-20	<-80	-90/-145	+3.3/13	+5/40
MPLL1537CD-200k	1499-1575	5±2	200	<0.45	-20	<-80	-85/-141	+3.3/13	+5/40
MPLL1635CD-200k	1597-1672	5±2	200	<0.45	-20	<-80	-85/-140	+3.3/13	+5/40
MPLL1669CD-200k	1639-1699	5±2	200	<0.45	-20	<-80	-85/-141	+3.3/13	+5/40
MPLL1752CD-200k	1722-1782	5±2	200	<0.45	-20	<-80	-85/-140	+3.3/13	+5/40

MPLL-D series Phase locked synthesizer

Part number	Frequency MHz	Power output dBm	Step size kHz	Switching time mS	Harmonic output 2nd dBc	Spurious output dBc	Phase noise @1/100kHz dBc/Hz	Vpll/lpll V/mA	Vvco/lvco V/mA
MPLL540DD-200k	500-580	2±2	200	<15	-15	<-75	-90/-122	+3.3/20	+5/25
MPLL1010DD-30k	970-1050	2±2	30	<15	-15	<-70	-82/-122	+3.3/20	+5/25
MPLL1250DD-100k	1200-1300	2±2	100	<15	-15	<-70	-85/-120	+3.3/20	+5/25
MPLL1530DD-125k	1480-1580	2±2	125	<15	-20	<-75	-85/-120	+3.3/20	+5/25
MPLL1765DD-50k	1670-1860	2±3	50	<15	-20	<-70	-82/-120	+3.3/20	+5/35
MPLL1910DD-250k	1860-1960	2±2	250	<15	-25	<-75	-85/-120	+3.3/20	+5/35
MPLL2350DD-250k	2300-2400	2±2	250	<15	-25	<-75	-85/-118	+3.3/20	+5/35
MPLL2600DD-250k	2500-2700	2±3	250	<15	-25	<-75	-85/-117	+3.3/20	+5/35
MPLL3100DD-250k	3000-3200	2±3	250	<15	-25	<-75	-85/-115	+3.3/20	+5/35
MPLL3500DD-250k	3400-3600	2±3	250	<15	-25	<-75	-85/-115	+3.3/20	+5/35
MPLL4440DD-500k	4400-4480	2±3	500	<15	-25	<-70	-80/-112	+3.3/30	+5/35
MPLL5210DD-500k	5180-5240	2±3	500	<15	-25	<-65	-80/-110	+3.3/30	+5/35

Package information

- Outline Drawing



- Pin functions

Pin No.	Function	Pin No.	Function
1	Vpll	10	Clock
3	REFin	11	Data
5	Vvco	12	LE
7	REFout	Others	GND
9	LD		