

UNISONIC TECHNOLOGIES CO., LTD

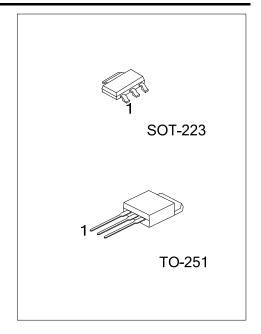
1N40A **Preliminary Power MOSFET**

1A, 400V N-CHANNEL POWER MOSFET

DESCRIPTION

The UTC 1N40A is an N-channel mode power MOSFET using UTC's advanced technology to provide customers with planar stripe and DMOS technology. This technology is specialized in allowing a minimum on-state resistance and superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

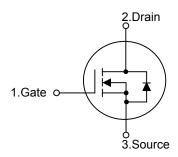
The UTC 1N40A is universally applied in electronic lamp ballast based on half bridge topology and high efficient switched mode power supply.



FEATURES

- * High switching speed
- * $R_{DS(ON)}$ < 6.8 Ω @ V_{GS} = 10V, I_D = 0.5A
- * 100% avalanche tested

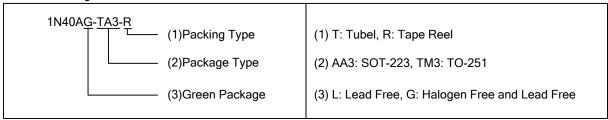
SYMBOL



ORDERING INFORMATION

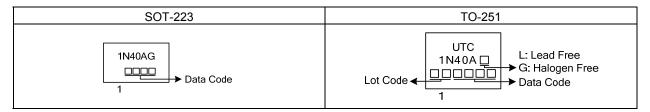
Ordering Number		Doolsons	Pin Assignment			Daakina	
Lead Free	Halogen Free	Package	1	2	3	Packing	
-	1N40AG-AA3-R	SOT-223	G	D	S	Tape Reel	
1N40AL-TM3-T	1N40AG-TM3-T	TO-251	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source



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MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	400	٧
Gate-Source Voltage		V_{GSS}	±30	V
Drain Current	Continuous (T _C =25°C)	I_{D}	1	Α
	Pulsed (Note 2)	I_{DM}	4	Α
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	40	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation	SOT-223	ם	1	W
	TO-251	P_{D}	25	W
Derate above 25°C	SOT-223	0	125	W/°C
	TO-251	P_{D}	0.2	W/°C
Junction Temperature		T_J	+150	°C
Storage Temperature		T _{STG}	-55~+150	ů

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature
- 3. L = 80mH, I_{AS} = 1A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25 $^{\circ}$ C
- 4. $I_{SD} \le 1.8A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	SOT-223	0	150	°C/W
	TO-251	θ_{JA}	110	°C/W
Junction to Case	SOT-223	0	125	°C/W
	TO-251	θις	5	°C/W

■ ELECTRICAL CHARACTERISTICS (T_C=25°C, unless otherwise noted)

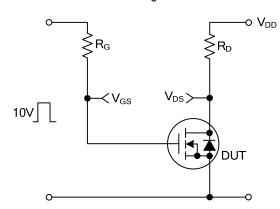
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PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	$I_D=250\mu A, V_{GS}=0V$	400			V
Breakdown Voltage Temperature Coefficient		△BV _{DSS} /△T _J	Reference to 25°C, I _D =250µA		0.4		V/°C
Drain-Source Leakage Current		I _{DSS}	V _{DS} =400V, V _{GS} =0V			1	μΑ
Gate- Source Leakage Current	Forward Reverse	I _{GSS}	V _{GS} =+30V, V _{DS} =0V V _{GS} =-30V, V _{DS} =0V			+100	nA nA
ON CHARACTERISTICS	•			•			
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	2.0		4.0	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =0.5A		4.0	6.8	Ω
DYNAMIC PARAMETERS							
Input Capacitance		C _{ISS}			125	150	pF
Output Capacitance		Coss	V _{GS} =0V, V _{DS} =25V, f=1.0MHz		20	30	pF
Reverse Transfer Capacitance		C _{RSS}			17	28	pF
SWITCHING PARAMETERS							
Total Gate Charge		Q_G	\\ -10\\ \\ -50\\ -1.3A		10	15	nC
Gate to Source Charge		Q_{GS}	V _{SS} =10V, V _{DS} =50V, I _D =1.3A (Note 1, 2)		3.22		nC
Gate to Drain Charge		Q_{GD}			0.8		nC
Turn-ON Delay Time	Turn-ON Delay Time				33	40	ns
Rise Time		t _R	V_{DD} =30V, I_{D} =0.5A, R_{G} =25 Ω		20	35	ns
Turn-OFF Delay Time		t _{D(OFF)}	(Note 1, 2)		58	78	ns
Fall-Time		t _F			17	30	ns
SOURCE- DRAIN DIODE RATI	NGS AND	CHARACTERIS	STICS				
Maximum Body-Diode Continuous Current		I _S				1.0	Α
Maximum Body-Diode Pulsed Current		I _{SM}				4.0	Α
Drain-Source Diode Forward Voltage		V_{SD}	I _S =1A, V _{GS} =0V			1.5	V

Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%

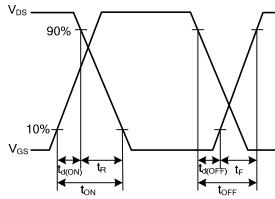
^{2.} Essentially independent of operating temperature

■ TEST CIRCUITS AND WAVEFORMS

Resistive Switching Test Circuit



Resistive Switching Waveforms



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