TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7WH00FC

#### Dual 2-Input NAND Gate

#### **Features**

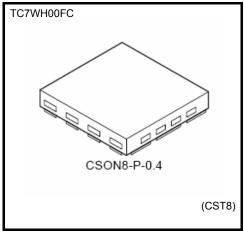
- High speed operation
- High noise immunity

5.5-V tolerant inputs

- : t<sub>pd</sub> = 3.7ns (typ.) at  $V_{CC}$  = 5 V,  $C_{L}$  = 15pF
- Low power dissipation

Operating voltage range

- :  $I_{CC} = 2\mu A (max)$  at Ta = 25°C
  - : V<sub>NIH</sub> = V<sub>NIL</sub> = 28% V<sub>CC</sub> (min)
  - : V<sub>CC</sub> = 2 to 5.5 V



Weight: 0.002 g (typ.)

# Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	-0.5 to 7.0	V
DC input voltage	VIN	-0.5 to 7.0	V
DC output voltage	V <sub>OUT</sub>	–0.5 to V <sub>CC</sub> + 0.5 (Note1)	V
Input diode current	IIK	-20	mA
Output diode current	I <sub>OK</sub>	±20 (Note2)	mA
DC output current	I <sub>OUT</sub>	±25	mA
DC V <sub>CC</sub> /GND current	ICC	±50	mA
Power dissipation	PD	150 (Note3)	mW
Storage temperature	T <sub>stg</sub>	–65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

> Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: High or Low State.

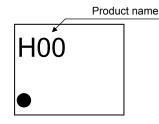
I<sub>OUT</sub> absolute maximum rating must be observed.

Note 2:VOUT < GND, VOUT > VCC

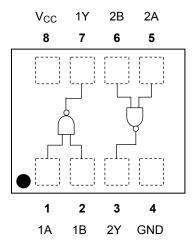
Note 3: Mounted on an FR4 board.

(25.4 mm × 25.4 mm × 1.6t, Cu Pad: 11.56 mm<sup>2</sup>)

## Marking



# Pin Assignment (top view)



Start of commercial production 2005-06

# <u>TOSHIBA</u>

# IEC Logic Symbol



А	В	Y
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

Truth Table

# **Operating Ranges**

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	2.0 to 5.5	V
Input voltage	V <sub>IN</sub>	0 to 5.5	V
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub>	V
Operating temperature	T <sub>opr</sub>	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 100 (V_{CC} = 3.3 V $\pm$ 0.3 V)	ns/V
		0 to 20 (V_{CC} = 5.0 V $\pm$ 0.5 V)	115/ V

# **Electrical Characteristics**

#### **DC Characteristics**

Characteristic Symbol		Test	Test condition		Ta = 25°C			Ta = -40 to 85°C		Unit
Characteristic	Characteristic Symbol Test condition		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit	
				2.0	1.5	_	_	1.5	_	
High-level input voltage V <sub>IH</sub>		_		3.0 to 5.5	V <sub>CC</sub> × 0.7		_	V <sub>CC</sub> × 0.7	_	
				2.0			0.5		0.5	V
Low-level input voltage VIL –		_	3.0 to 5.5	_		V <sub>CC</sub> × 0.3	_	$V_{CC} \times 0.3$		
			I <sub>OH</sub> = –50 μA	2.0	1.9	2.0		1.9		V
		V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>		3.0	2.9	3.0		2.9	_	
high-level output voltage	V <sub>OH</sub>			4.5	4.4	4.5		4.4		
			I <sub>OH</sub> = –4 mA	3.0	2.58			2.48		
			I <sub>OH</sub> = –8 mA	4.5	3.94			3.80		
Low-level output voltage			I <sub>OL</sub> = 50 μA I <sub>OL</sub> = 4 mA	2.0	—	0.0	0.1	—	0.1	
				3.0	—	0.0	0.1	—	0.1	
	V <sub>OL</sub>	$V_{IN} = V_{IH}$		4.5	—	0.0	0.1	—	0.1	
				3.0	_		0.36	—	0.44	
			I <sub>OL</sub> = 8 mA	4.5	_		0.36	—	0.44	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V	/ <sub>IN =</sub> 5.5 V or GND		_		±0.1	—	±1.0	μA
Quiescent supply current	ICC	$V_{IN} = V_{CC}$ or GND		5.5	_	—	2.0	—	20.0	μA

# AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristic	Symbol		Test condition		Ta = 25°C			Ta = –40 to 85°C		Unit
			V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Тур.	Max	Min	Max	
Propagation delay time	<sup>t</sup> pLH t <sub>pHL</sub>		$3.3\pm0.3$	15	_	5.5	7.9	1.0	9.5	- ns
				50	_	8.0	11.4	1.0	13.0	
			5.0 ± 0.5	15	_	3.7	5.5	1.0	6.5	
		5.0 ± 0.5	50	_	5.2	7.5	1.0	8.5		
Input capacitance	C <sub>IN</sub>		_		_	4	10	_	10	pF
Power dissipation capacitance	C <sub>PD</sub>			(Note 4)	_	19	_	_	_	pF

Note 4: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

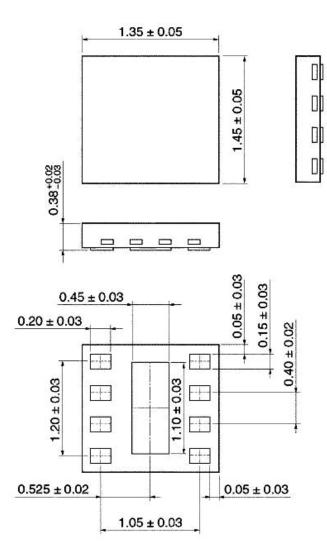
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$ 

# <u>TOSHIBA</u>

## **Package Dimensions**

CSON8-P-0.4

Unit: mm



Weight: 0.002 g (typ.)

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