# VM114

| Version    | : | PRE.001              |
|------------|---|----------------------|
| Issue Date | : | 2010/01/14           |
| File Name  | : | SP-VM114-PRE.001.doc |
| Total Page | : | 8                    |

# Micro-actuator Driver with PC Interface



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#### VM114

# Micro-actuator Driver with $f^2C$ Interface

#### **General Specifications**

VM114 is a micro-actuator driver IC with miniature package. It is one channel low voltage bi-directional motor driver IC. The design is optimal for driving different type micro-actuator, such as voice coil motor, piezo-actuator, or other DC motor actuators. It is suitable for camera module application or other portable devices.

#### **Features and Benefits**

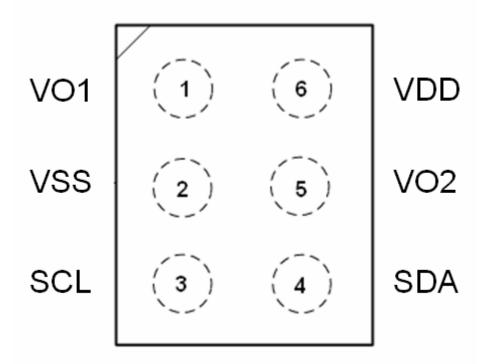
- Low voltage operation (V<sub>DD Min</sub> = 1.8 V)
- Low input current
- Zero standby current
- ♦ I<sup>2</sup>C serial interface
- Automatic power on reset
- Ultra small package: WLCSP (0.78\*1.27\*0.35mm)

# **Ordering Information**

| Part Number | Package     | Marking |
|-------------|-------------|---------|
| VM114 WLCSP | WLCSP, 6Pin | TBD     |

Micro-actuator Driver with  $I^2C$  Interface

**TOP** View



| Pin Number | Pin Name | Description  |  |
|------------|----------|--|--|
| 1          | VO1      | Driver output 1  |  |
| 2          | VSS      | Ground   |  |
| 3          | SCL      | I <sup>2</sup> C Interface Clock Line(Serial Clock Line) |  |
| 4          | SDA      | I <sup>2</sup> C Interface Data Line(Serial Data Line)   |  |
| 5          | VO2      | Driver output 2  |  |
| 6          | VDD      | Power supply   |  |

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| Characteristic              | Symbol             | Rating               | Unit |  |
|-----------------------------|--------------------|----------------------|------|--|
| Supply Voltage              | V <sub>DD</sub>    | 4.5                  | V    |  |
| Input Voltage               | V <sub>P1</sub>    | V <sub>DD</sub> +0.4 | V    |  |
| Io Peak Current             | I <sub>OPeak</sub> | 400                  | mA   |  |
| I <sub>ODC</sub> Current    | I <sub>ODC</sub>   | 280                  | mA   |  |
| Power Dissipation           | P <sub>D</sub>     | 300                  | mW   |  |
| Operating Temperature Range | T <sub>OPR</sub>   | -40 ~ 80             | °C   |  |
| Storage Temperature Range   | T <sub>STG</sub>   | -65 ~ 150            | °C   |  |

## Absolute Maximum Ratings (Unless otherwise noted, $T_A=25\,^\circ$ )

## **Electrical Characteristic**

(Unless otherwise noted,  $T_A = 25^{\circ}C \& V_{DD} = 2.8V$ )

|  |   |  | ,  |   |  |  |  |
|--|---|--|--|---|--|--|--|
| SUB  | Condition   |  | Unit   |   |  |  |  |
| Sym.   | Condition   | Min.   | Тур.   | Max.  |  |  |  |
| $V_{\text{DD}}$                                |   | 1.8  | 2.8  | 4.5   | V  |  |  |
| I <sub>DD</sub>                                | No load   | -  | -  | 3   | $\mu A$  |  |  |
| SDA SCL Input Terminal ( $T_J = 25^{\circ}C$ ) |   |  |  |   |  |  |  |
| V <sub>IH</sub>                                | -   | $0.5^{*}V_{DD}$  | -  | V <sub>DD</sub> +0.4  | V  |  |  |
| V <sub>IL</sub>                                | -   | -0.4   | -  | $0.2^{*}V_{DD}$   | V  |  |  |
| I <sub>IH</sub>                                | $V_{IN} = V_{DD}$   | -  | -  | ±1  | $\mu A$  |  |  |
| IIL  | V <sub>IN</sub> = 0 V   | -  | -  | ±1  | $\mu A$  |  |  |
| Output Terminal (O1, O2)                       |   |  |  |   |  |  |  |
| R <sub>OH</sub>                                | I <sub>OUT</sub> =200mA   | -  | 1.3  | 1.6   | Ohm  |  |  |
| R <sub>OL</sub>                                | I <sub>OUT</sub> =200mA   | -  | 0.7  | 0.9   | Ohm  |  |  |
|  | I <sub>DD</sub><br>℃)<br>V <sub>IH</sub><br>V <sub>IL</sub><br>I <sub>IH</sub><br>I <sub>IL</sub> | $V_{DD}$ $I_{DD}$ No load $^{\circ}C$ $V_{IH}$ $V_{IH}$ - $V_{IL}$ - $I_{IH}$ $V_{IN} = V_{DD}$ $I_{IL}$ $V_{IN} = 0 V$ $R_{OH}$ $I_{OUT}=200mA$ | Min.       Min. $V_{DD}$ 1.8 $I_{DD}$ No load       - $^{\circ}C$ $ 0.5^{*}V_{DD}$ $V_{IH}$ - $0.5^{*}V_{DD}$ $V_{IL}$ - $-0.4$ $I_{IH}$ $V_{IN} = V_{DD}$ - $I_{IL}$ $V_{IN} = 0$ - $R_{OH}$ $I_{OUT}$ =200mA       - | Min.         Typ. $V_{DD}$ Min.         Typ. $V_{DD}$ 1.8         2.8 $I_{DD}$ No load         -         - $C$ $0.5^*V_{DD}$ - $V_{IH}$ - $0.5^*V_{DD}$ - $V_{IL}$ -         -         - $I_{IH}$ $V_{IN} = V_{DD}$ -         - $I_{IL}$ $V_{IN} = 0$ -         - $R_{OH}$ $I_{OUT}$ =200mA         -         1.3 | Sym.         Condition         Min.         Typ.         Max. $V_{DD}$ 1.8         2.8         4.5 $I_{DD}$ No load         -         -         3 $^{\circ}$ C) $^{\circ}$ C         0.5*V_{DD}         -         V_{DD}+0.4 $V_{IH}$ -         0.5*V_{DD}         -         V_{DD}+0.4 $V_{IL}$ -         -0.4         -         0.2*V_{DD} $I_{IH}$ $V_{IN} = V_{DD}$ -         -         ±1 $I_{L}$ $V_{IN} = 0$ V         -         -         ±1 $R_{OH}$ $I_{OUT}$ =200mA         -         1.3         1.6 |  |  |

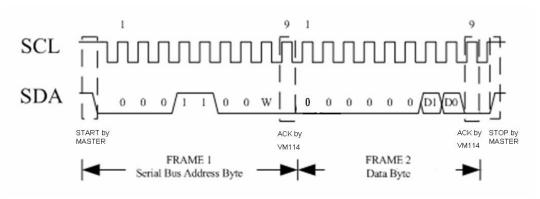
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#### **Data Format**

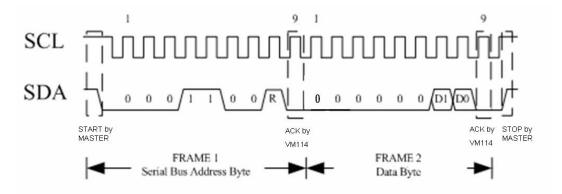
#### VM114 Write Mode

In the writing mode, data is written to VM114 and shifted into a 8-bit input register. After all 8 bits of data have been shifted in, a STOP signal is generated by master controller. The data in the input register is transferred to VM114 internal controller at the same time.



#### VM114 Read Mode

In reading mode, data is read from IC to a master controller in the same bit order.



<u>Table</u>

|                  | Address |     |     |     |     | Address Data |     |     |   |   |   |   |   |   |    |    |
|------------------|---------|-----|-----|-----|-----|--------------|-----|-----|---|---|---|---|---|---|----|----|
| Serial Data Bits | 7       | 6   | 5   | 4   | 3   | 2            | 1   | 0   | 7 | 6 | 5 | 4 | 3 | 2 | 1  | 0  |
| Function         | ID6     | ID5 | ID4 | ID3 | ID2 | ID1          | ID0 | R/W | 0 | 0 | 0 | 0 | 0 | 0 | D1 | D0 |

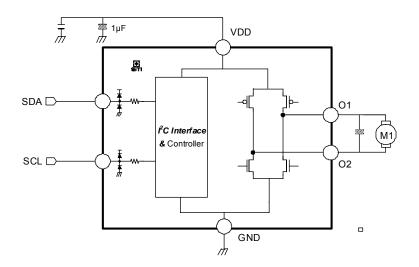
**D[1:0]:** The output O1/O2 is set by D[1:0] as defined below.

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| Inj | out | Output |    |  |
|-----|-----|--------|----|--|
| D1  | D0  | O1     | O2 |  |
| 0   | 0   | Н      | н  |  |
| 0   | 1   | Н      | L  |  |
| 1   | 0   | L      | н  |  |
| 1   | 1   | L      | L  |  |

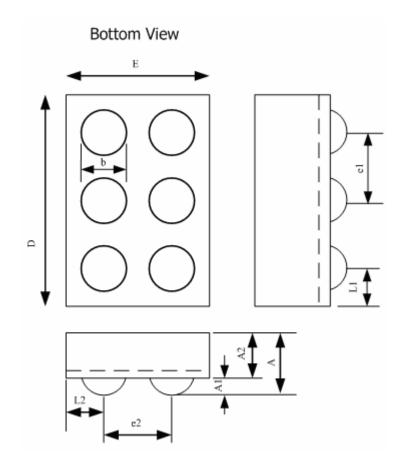
# **Function Block**



## **Application Notes**

- The O1/O2 output H/H or L/L will brake the motor. Though the active current of driver is near to zero, if the application is required to turn off driver, please turn off the driver's power from VDD.
- □ The capacitor connected between the output nodes O1/O2 will reduce the noise generated by the motor when the motor is switched to opposed direction.

Package Specifications (WLCSP1): (0.78\*1.27\*0.35mm)



| SYMBOL | DIMENSION<br>(mm) |       |       |  |  |  |
|--------|-------------------|-------|-------|--|--|--|
|        | MIN.              | NOM.  | MAX.  |  |  |  |
| A      | 0.325             | 0.350 | 0.375 |  |  |  |
| A1     | 0.090             | 0.100 | 0.110 |  |  |  |
| A2     | 0.235             | 0.250 | 0.265 |  |  |  |
| b      | 0.234             | 0.260 | 0.286 |  |  |  |
| D      | 1.255             | 1.270 | 1.285 |  |  |  |
| E      | 0.765             | 0.780 | 0.795 |  |  |  |
| e1     | 0.380             | 0.400 | 0.420 |  |  |  |
| e2     | 0.380             | 0.400 | 0.420 |  |  |  |
| L1     | 0.215             | 0.235 | 0.255 |  |  |  |
| L2     | 0.170             | 0.190 | 0.210 |  |  |  |



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