



REV A January 2011

Oscilent Controlled Document

Ordering Code / Part Number	Product Description
821-IF75.0M-30A	75.0 MHz IF SAW Filter 29.97 MHz Bandwidth

Specification Contents

- o Mechanical Dimensions
- o Test Circuit
- o Maximum Ratings
- o Electrical Specification
- o Frequency Response
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Notes

- o Electrostatic Sensitive Device (ESD) 
- o Avoid excessive ultrasonic exposure
- o Solderability compatible with JEDEC J-STD-020C Pb-free process, 260°C peak reflow temperature
- o This product complies with EU directive 2002/95/EC (RoHS compliance)



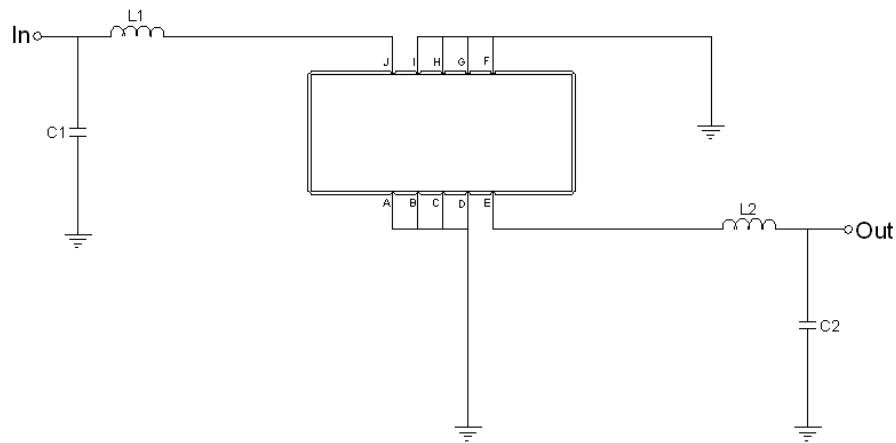


Mechanical Dimensions (mm)



Pin Description	
A, B, C, D, F, G, H, I	Ground
J	Input
E	Output

Test Circuit



Test Fixture & Values	
Input	L1=82nH, C1=10pF
Output	L2=82nH, C2=24pF
Source/Load Impedance	50 Ω



Maximum Ratings

Parameters Description	Unit	Minimum	Typical	Maximum
Operating Temperature Range	°C	0	-	60
Storage Temperature Range	°C	-30	-	80
Maximum DC Voltage	V	-	-	10
Maximum Input Power	dBm	-	-	10
Source Impedance (single ended) ⁽¹⁾	Ω	-	50	-
Load Impedance (single ended) ⁽¹⁾	Ω	-	50	-

Notes: With Matching Network (Ref. Testing Environment Circuit as shown above).

Those impedances could be modified with different impedance values and/or structures, if necessary.

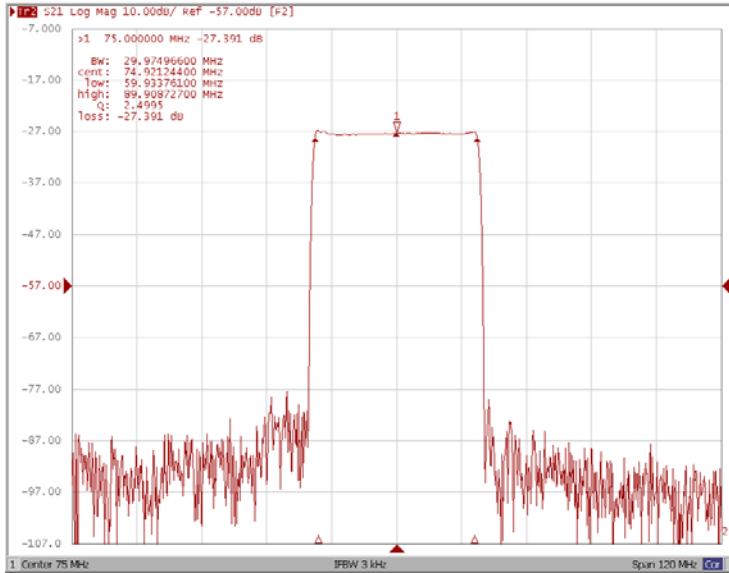
Electrical Specification

Parameters Description	Unit	Minimum	Typical	Maximum
Center Frequency (Fo)	MHz	74.90	75.00	75.10
Insertion Loss at Fo	dB	-	27.5	30.0
Group Delay Variation (Fo±14.5MHz)	ns	-	30	80
Absolute Delay	us	-	2.03	-
Passband Ripple (Fo±14.5MHz)	dB	-	0.8	-
Bandwidth at -1dB	MHz	29.85	29.97	-
Bandwidth at -3dB	MHz	-	30.36	-
Bandwidth at -40dB	MHz	-	32.08	32.15
Ultimate Rejection	dB	-	50	-
Temperature coefficient	ppm/°C	-	-72	-

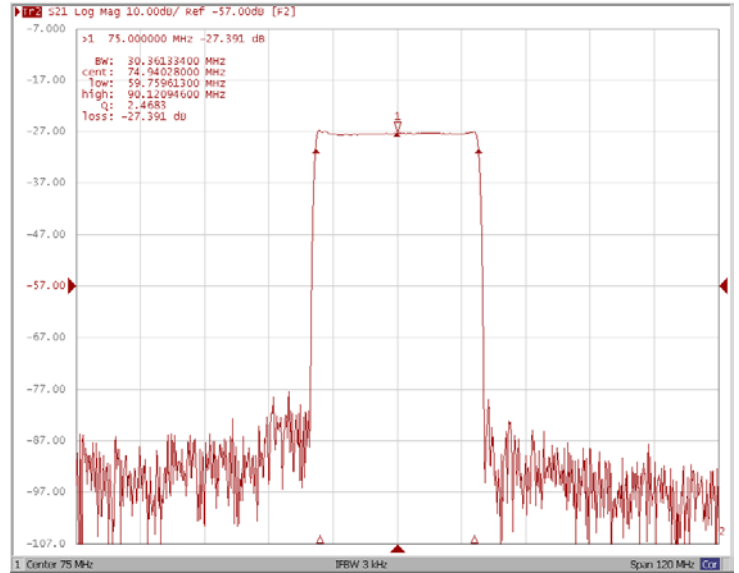


Frequency Response

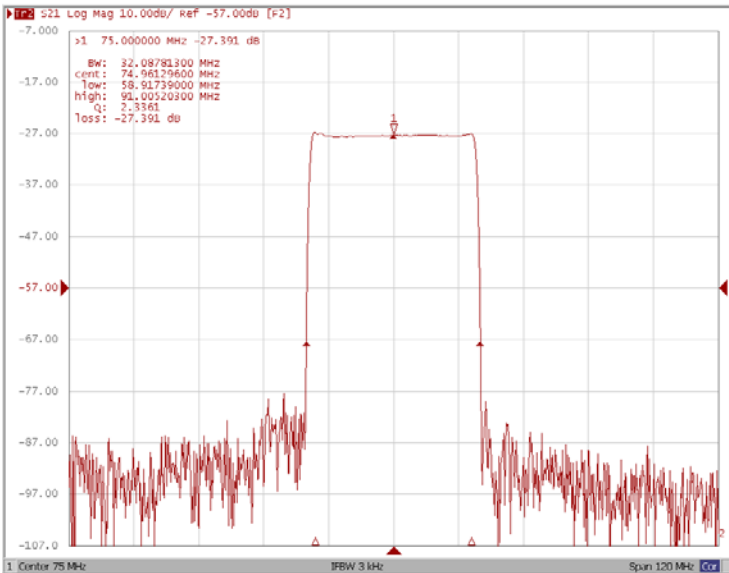
Bandwidth at -1.0 dB



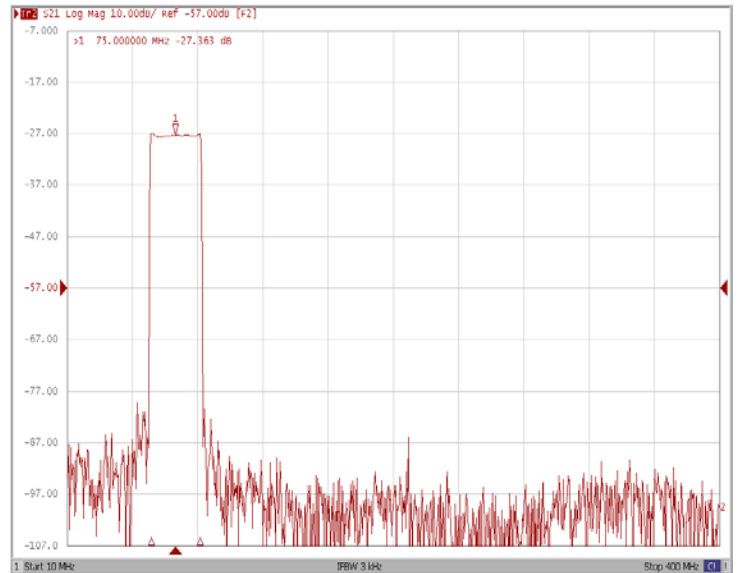
Bandwidth at -3.0 dB



Bandwidth at -40.0 dB

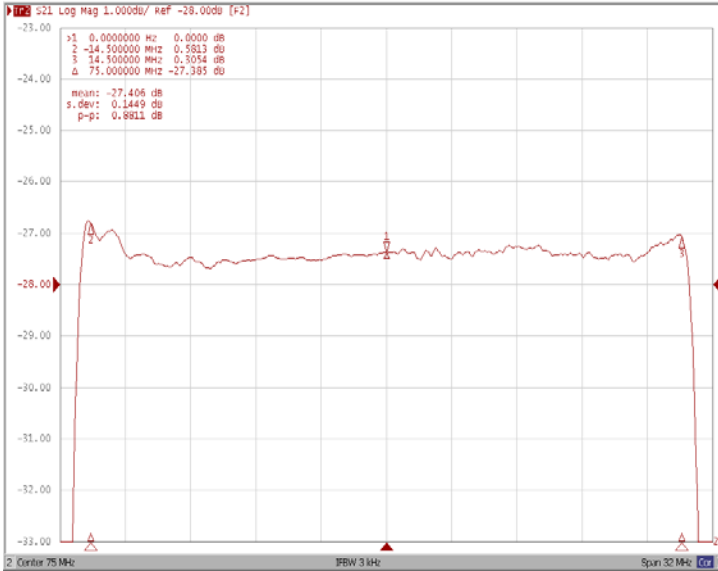


Wide Band

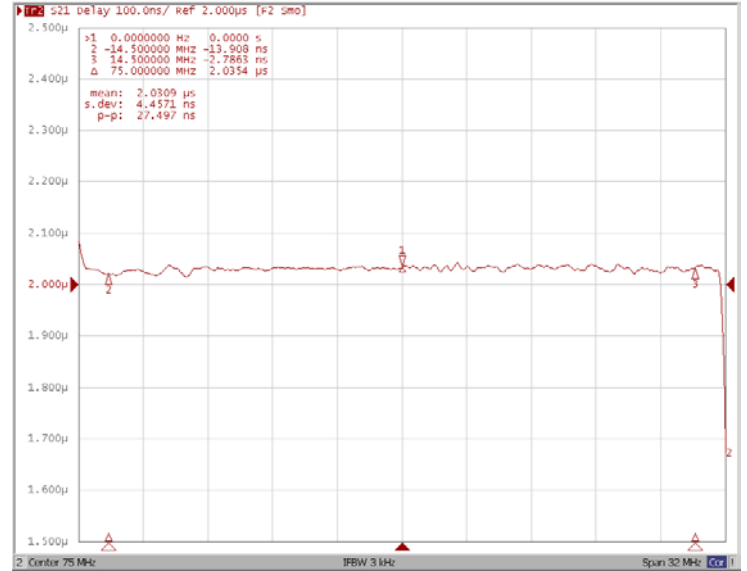




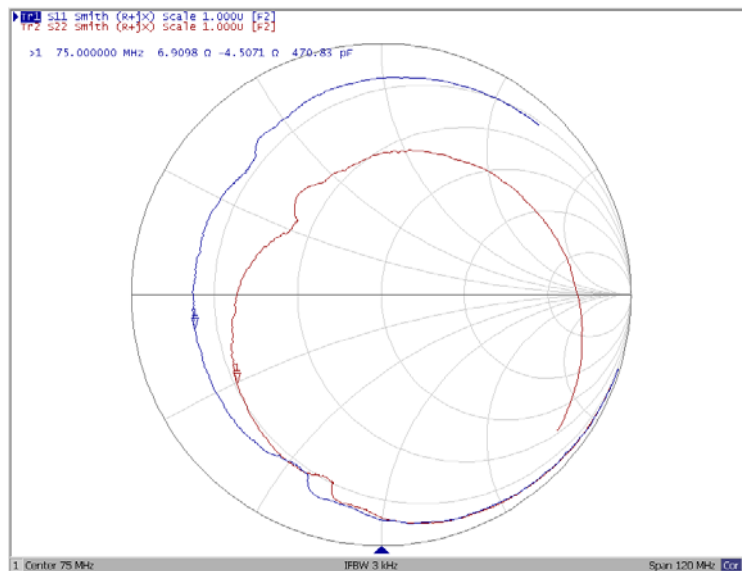
Ripple Variation Fo±14.5MHz



Group Delay Variation Fo±14.5MHz



Smith Chart





VSWR

